Industrial Measurement and Automation

Distributed systems









Interference free PC boards



www.addi-data.com



For us, 2014 is a special year: We are celebrating our 30th company anniversary!

During these 30 years, we were able to gain a lot of expertise. Thank you for your trust!

This success is also a result of the values we have been living by: quality, flexibility, long-term availability, reliability and spirit of innovation. By choosing ADDI-DATA, you have chosen high-quality products which are available for years, expert technical advice and the possibility to adapt our products to your requirements.

And tomorrow? We would be happy to let you benefit further from our know-how and 30 years of expertise. We are looking forward to standing at your side for your future projects.

R. GAMann.

René Ohlmann Managing Director, ADDI-DATA GmbH



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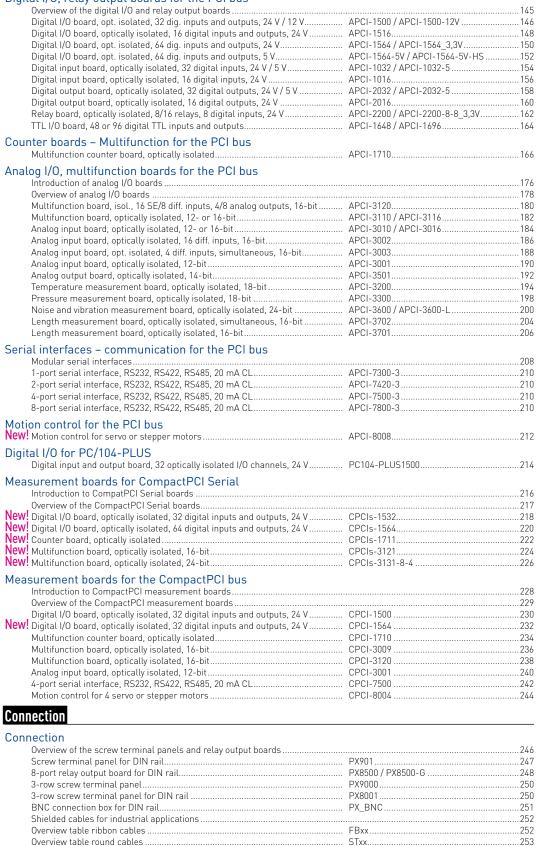
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Digital I/O, relay output boards for the PCI bus



Appendix

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Our partners worldv	dwide	
Imprint		





CompactPCI® Serial

CompactPCITh



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Fast processing of high data volumes



PCI express

PCI

CompactPCI[™] *CompactPCI® Serial* With ADDI-DATA measurement boards, you can precisely acquire numerous types of signals and sensor and thus control processes reliably. The extremely interference-resistant measurement boards are especially designed for the harsh industrial environment.

- DMA for more speed
- FPGA: onboard calculation of the measurement
- RTX real-time drivers for time-critical applications with Windows
- Measurement boards for the following signal types: digital, counter, analog, temperature, pressure, vibrations, length, motion control, serial interfaces

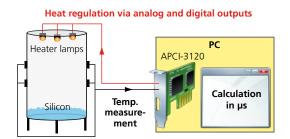
Temperature regulation for wafer production

Challenge

- PC-based solution
- Acquisition cycle 1 ms
- FPGA technology for taking load off the external software (algorithm)

Solution

- Analog I/O board APCI-3120 for the PCI bus for temperature measurement and PWM regulation
- Satisfies all requirements: Speed, precision and long-term availability
- Numerous protective circuits for the use in an environment with interferences



See also data sheet APCI-3120, page 180

Time-critical applications





The Programmable Automation Controller system (PAC) MSX-Box has been especially developed for industrial measurement, control and automation applications in real time where processes have to be carried out within a defined time.

- Based on established standard technologies like PCI backplane or CompactPCI backplane
- Programmable, free programming tools
- Working with Linux operating system with RTAI extension
- No update obligations, no licence costs
- I/O PCI boards or CompactPCI boards from other manufacturers can be used

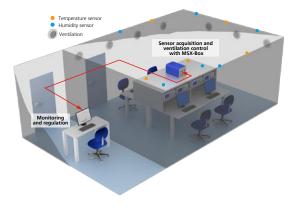
Intelligent monitoring of temperature and air humidity in clean rooms, laboratories and calibration rooms

Challenge

- Monitoring of the room atmospheric environment in real time
- Ensure constant values

Solution

- Definition of reference values for the room temperature and air humidity
- Real-time PAC system MSX-Box with temperature measurement and analog input board for the acquisition of sensor data
- Digital output board APCI-2032 for ventilation control



See also data sheet PAC system MSX-Box, page 22



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Measurement and control in the field



Relieve your PLC and expand its range of functionality with useful measurement tasks close to the sensor or the machine: the intelligent Ethernet systems of the MSX-E series are perfect for this!

- Robust metal housings, degree of protection IP 65/ IP 67
- Easy connection to PLCs and to the company network through Ethernet
- Integrated Modbus TCP/IP server
 Modbus library in preparation
- High accuracy for precise control commands
- Onboard data calculation
- Synchronisation of several (same or different type) Ethernet systems in the µs range

NEW! MSX-E3121 as a substitute for a small PLC or in addition to a PLC

Analog and digital I/O for measurement and control tasks in one device!

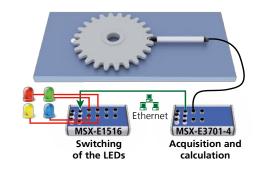
Precise and error-free diameter detection of gear wheels

Challenge

- Automation / replacement of a manual test station
- Improve the measurement accuracy
- Avoid measurement errors

Solution

- Real-time Ethernet system MSX-Box with counter-, analog I/O boards and serial interfaces
- Onboard data calculation, time stamp
- Several sensor transmission protocols: CAN, serial, etc



See also data sheet MSX-E3121, page 70

Measurement and control with EtherCAT, ProfiNet, VARAN









The x-ARTS real-time systems are I/O slave systems for EtherCAT, ProfiNET and VARAN for measurement and control tasks.

- Highly precise inputs
- Data acquisition faster than the bus clock, data buffering
- Starts the acquisition independently from the bus by using the 24 V trigger input
- Can be combined with external devices which are not connected to the bus

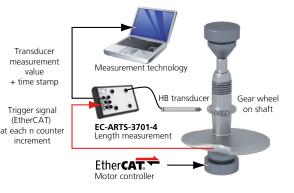
Extension of an optical shaft measurement machine with a tactile measurement device

Challenge

- Clear assignment of the measured values to the axis position
- Autonomous data transmission to the measurement machine
- High precision and interference-resistance

Solution

- EtherCAT system for the connection of half-bridge transducers to an optical shaft measurement machine
- 24-bit high resolution and numerous protective circuits
- Data package for the measurement system with measured data incl. time stamp



See also data sheet EC-ARTS-3701-4, page 19





First CompactPCI Serial I/O boards

For the new CompactPCI Serial Bus, ADDI-DATA now presents the **CPCIs-1564** and the **CPCIs-1532**, the first digital I/O boards, 24 V for industrial I/O control. They have 64 or 32 I/O, interruptible inputs and up to 3 counter inputs. To avoid undefined states, the 32 outputs are set to "0" at power-on. Both boards are designed for an extended temperature range from -40 °C to +85 °C.

Also included in the CompactPCI Serial board range are a counter board with programmable functions,

the **CPCIs-1711**, and 2 analog boards: the **CPCIs-3121** (16 inputs, 16-bit, and 8 outputs, 16-bit) and the **CPCIs-3131** (8 inputs, 24-bit, and 4 outputs, 16-bit).

Also new: 5 V and 12 V versions of digital I/O boards PCI-Express: APCIe-1564-5V-HS and APCIe-1532-12V PCI: APCI-1564-5V-HS and APCI-1500-12V.



Intelligent Ethernet systems: new functions and signal combinations

New functions and new signal combinations are now available! The range of counter systemse has been extended with the **MSX-E1731** for the acquisition of EnDat2.2 sensors. The **NPN** version of the **MSX-E1516** digital system can acquire values from bipolar transistors, which can amplify and switch high-current or voltage signals.

New types of applications are now possible with systems featuring new signal combinations on one device: forcepath measurement with the **MSX-E3x17** and measurement and control with the **MSX-E3121** and **MSX-E3122** analog/ digital systems. **Do you need your own customized signal combination? Contact us!** Also new: Customized functions as firmware extensions. With the **XT-17x1-TickCounter** extension, an internal time base can be defined which is independent from the system time.



New EtherCAT systems

Two new EtherCAT systems have been added to the product range: the **EC-ARTS-3011** system has 16 analog, differential inputs. The inputs can be configured as current or voltage inputs. They are organised in 4 groups of 4 channels each. There is one multiplexer per group which means that 4 channels can be acquired simultaneously. The system has a 16-bit resolution.

The **EC-ARTS-3701-4** system for length measurement allows the simultaneous acquisition of 4 inductive displacement transducers – Half-Bridge, LVDT or Mahr transducers – with a 24-bit resolution. The system detects short-circuits or line break of the connected transducers.



64-bit

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64-bit drivers / 64-bit ADDIPACK version

64-bit drivers for Windows 8/7/XP are now available for many PCI-Express, PCI, CompactPCI and CompactPCI-Serial boards. They belong to the new generation of drivers which can be used for both 64-bit and 32-bit applications. They are therefore optimally suited for applications scheduled to be switched from 32-bit to 64-bit.

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ADDIPACK drivers are very useful when you have to administrate different board types.

The 64-bit version of ADDIPACK is now available!

The concept is simple: the functions of all boards which are installed in the PC are listed as resources in an overview. The user selects the functions he needs without having to figure out from which board they come.



DISTRIBUTED SYSTEMS

Product overview

The distributed systems by ADDI-DATA have been developed especially for the harsh industrial environment and can be used directly in the field. They have high-precision inputs as an outstanding feature, save space and reduce the wiring significantly. All systems are available in the long term in order to safeguard your investment. You will find in our product range systems for the well-proven network buses Ethernet, EtherCAT, ProfiNET and VARAN – also in real-time versions.

Many different types of distributed solutions can be found on the market.

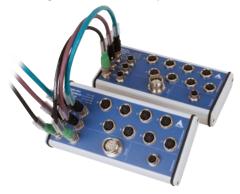
However, on closer examination there are not many systems which can actually cope with the high requirements of production facilities. Technology which may work perfectly in the laboratory is not intended for the use in an environment submitted to current or voltage peaks or electromagnetic disturbances. The distributed systems from ADDI-DATA are robust because they have been developed especially for the use in the field.

1. Industrial data loggers



The industrial data loggers of the MSX-ilog series are used for continual data acquisition and storage over longer periods of time. Diverse physical measurements can be acquired and shown. Data storage and visualisation take place parallel to one another. The data loggers from ADDI-DATA need no installation, are platform-independent and save expensive licence costs.

2. Intelligent Ethernet systems



Precision, autonomy and flexibility are key features of the intelligent Ethernet systems of the MSX-E series.

Measurement and control tasks can be effected reliably directly at the machine. If necessary, the signals can be processed onboard. This saves resources from external PCs or PLCs.

3. Real-time open source PAC systems



The MSX-Box product range is used for measuring and controlling tasks in real time. Versions for PCI and for CompactPCI backplanes are available. The user defines the functions of the Ethernet-based systems by selecting the corresponding PC boards.

In addition to flexibility, the systems offer another advantage: you save licence costs.

4. Real-time Ethernet systems



ADDI-DATA offers I/O slave systems for EtherCAT, ProfiNET and VARAN. They feature highly-precise inputs. The systems can measure faster than the bus cycle and buffer the acquired data. Moreover, the measurement can be started independently from the bus, since by using the 24 V trigger input, the x-ARTS can be combined with hardware that is not connected to the bus.



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ETHERNET DATA LOGGERS

*i*ndustrial + *i*nternet technology + *i*ntelligent + *i*ntuitive + *i*ntegrated = *i*nnovative data loggers from ADDI-DATA

Data recording and visualisation made easy



MSX-ilog data loggers from ADDI-DATA are used for continual data acquisition and storage over long periods of time. Diverse physical measurements can be acquired and shown in different display modes. Data storage and visualisation take place parallel to one another.

Different hardware versions

Different hardware versions are available to meet the various application requirements: Ethernet systems, PCI and CompactPCI solutions. The version with Ethernet systems is primarily designed for use in the field: the systems have numerous protective circuits and IP 65 protection levels and they can be used with an operating temperature from -40 °C to +85 °C. In order to safeguard your investments, MSX-ilog data loggers can be supplied by ADDI-DATA over many years.



Application areas

- Environmental technology
- Aviation
- Research and development
- Engineering
- Building services



BENEFITS

- Industry standard solutions
- One-time acquisition costs (no additional license costs)
- Independent from operating systems
- Software integrated in the hardware

 no installation necessary
- Simple operation via the web-based user interface
- Recording of a number of signal types as physical measurement data
- Rapid acquisition

- Monitoring of infrastructure
- Energy industry
- Transport monitoring
- Stock controlChemicals
- Chemicals



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MSX-7log

Functions

- Long-term recording of many signal types
- Visualisation: Live or recorded data
- using a curve diagram, display of value
- Setup of the test point without programming knowledge
 Web-based user interfaces without installation of programs
- Web-based user interfaces without insta
- Can be used as a stand-alone system

Configuration

Real channels

• Channel selection • Colour • Identifier • Unit • Frequency • Offset • Multiplication factor • Polarity • Gain

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Virtual channels

 \bullet Channel selection \bullet Colour \bullet Identifier \bullet Unit \bullet Operation \bullet Frequency

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10		0					193
	-11	0	_				193 -
	9 - 42	0 1					188
	-0	0					183
	- M - M		_				100 -
		0 1					100

Triggers

Activate trigger • Pre-trigger acquisition • Pre-trigger channels
 Start/Stop type (digital, analog, stop time) • Channel selection

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Alarms



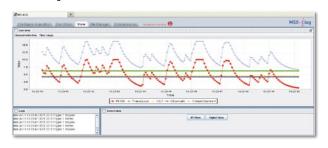
Saving modes

• View only • Save all the time • Trigger mode



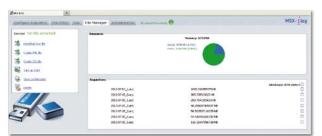
Live view

• XY view • Digital view



Exporting data

• CSV • XML • View as chart • View/load acquisition configuration • Delete acquisition files



Administration

Software download • Auto start • System configuration • System reboot/shutdown
 NTP/Time configuration



Bespoke solutions

In order to make the data recording as efficient as possible, it is important to adapt the measurement system as closely to your requirements as possible. This is not always possible with standard products. We are happy to advise on finding the optimum solution for your applications and perform the necessary adaptations for you. **Just ask us!**

Advice needed?

Then just call us at +49 7229 1847-0 or send an e-mail to: info@addi-data.com



Ethernet data logger 16 differential analog inputs, 16-bit



IP 65

Technology

Integrated Ethernet switch









More information at www.addi-data.com The intelligent Ethernet data logger MSX-ilog-AI-16 has 16 differential analog inputs, 16-bit, with a transfer rate of 1 kHz/channel. The parametering and visualisation of the measured values are carried out via an integrated web site. Thus no additional software installation is needed. The acquisition, visualisation and data storage take place automatically.

Features

• Onboard ARM®9 32-bit processor

On

40 °C request

- 4 GB memory, data remains stored at power loss
- The buffered real-time clock keeps the system time even without supply voltage
- Robust metal housing
- Power Save Mode: Reduced power consumption when no acquisition runs
- 24 V digital trigger input

Analog inputs

- 16 diff. inputs, 16-bit, 5-pin M12 female connectors
- Sampling frequency max. 1 kHz, up to 4 simultaneous channels
 - Input ranges: ± 5 V, ± 10 V (16-bit)
 - 0-5 V, 0-10 V (15-bit)
- Current inputs optional

Acquisition

- Automatic acquisition and recording of measured data
- Conversion of measured data into real values e.g. mm,
- bar, temperature, etc. • Acquisition of virtual channels

Trigger

- Acquisition triggered via hardware or software
- 24 V hardware trigger
- Threshold trigger (when the defined level of the analog inputs is exceeded)

- Optional pre-trigger (records events which have occurred before the trigger event)
- Triggers from external hardware, e. g. MSX-E systems, are possible

Alarm functions

• Upper and lower limits of channels

Flash memory,

real-time clock

- Data storage depending on alarms
- Can be combined with the pre-trigger

Analysis

Online graphical analysis of measured data

• Data export (xml, csv) Safety features

- LED status display for fast error diagnostics
- Optical isolation Input filters
- Overvoltage protection ± 40 V
- · Internal temperature monitoring

Applications

- Data logger Long-term data recording
- Monitoring of infrastructure

Interfaces

- Fast 24 V trigger input
- Ethernet switch with 2 ports
 - Trigger In/Out

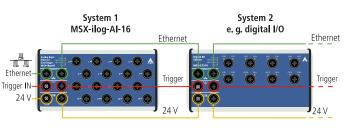
24 V supply and cascading

Communication interfaces

- Web server (configuration and monitoring)
- Data server (TCP/IP or UDP socket) for sending acquisition data

Combination with external hardware

Ethernet and supply signals can be looped e.g. from the MSX-ilog-AI-16 to MSX-E systems. These can then react to the values measured by the MSX-ilog-AI-16 (e.g. via alarm or trigger) and acquire and switch distributed I/O signals. Monitoring or regulation tasks can be realised.





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MSX-1log

0 to 30 V
11 mA at 24 VDC, typical
2 MHz at 24 V
1 x 5-pin flange connector M12
1 x 5-pin flange-type socket M12

1 trigger input

24 V external

1000 V

1 trigger output

Low-pass/transorb diode

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IÉC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

System features

Trigger Number of inputs:

Number of outputs:

Optical isolation:

Nominal voltage

Filters/protective circuit:

Interface:	Ethernet acc. to specification IEEE802.3
Dimensions:	215 x 110 mm x 50 mm
Weight:	850 g
Degree of protection:	IP 65
Current consumption at 24 V:	160 mA
Operating temperature:	-25 °C to +85 °C (-40 °C to +85 °C on request)

Features

System requirements

Standard browser (Internet Explorer, Firefox) with Java from version 1.6.x

Status LEDs

16 analog inputs, differential, 16-bit, emale connector, 5-pin

Ordering information

Ethernet data logger, 16 analog inputs, differential, 16-bit. Incl. technical description. **Connection cables**

Voltage supply

MSX-ilog-AI-16

	Shielded cable, M12 5-pin female connector/open end, IP 65 For cascading, shielded cable, M12 5-pin female connector/male connector IP 65
Trigger	
CMX-4x:	Shielded cable, M12 5-pin female connector/open end, IP 65
CMX-5x:	For cascading, shielded cable, M12 5-pin
	female connector/male connector IP 65
Ethernet	
CMX-6x:	CAT5E cable, M12 D-coded male connector/RJ45 connector
CMX-7x	For cascading: CATSE cable 2 x M12 D-coded male connector

Connection to peripherals

CMX-8x: Shielded cable, M12 5-pin male connector/open end, IP 65 Options

PC-Diff: Current input 0(4)-20 mA for 1 input, diff. (please indicate the number of channels) MSX-E 5V-Trigger: Level change of the trigger inputs and outputs to 5 V MX-Clip, MX-Rail (Please specify when ordering!), MX-Screw, PCMX-1x

* Preliminary product information



ge :	24 V 			
2:	18-30 V			
on:	1000 V			
ge protection:	1 A max.			
	1 x 5-pin male N			
t	1 x 5-pin female	M12 connector		
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	Ethernet acc. to	IEEE802.3 specification		
rts:	2			
	150 m	max. at CAT5E UTP		
	10 Mbps	auto-negotiation		
	100 Mbps	auto-negotiation		
	10Base-T	IEEE802.3 compliant		
	100Base-TX	IEEE802.3 compliant		
on:	1000 V			-

I LAJII.	4 Mb for System data
Extended FLASH memory:	4 GB (2 GB for measured data)
Buffered real-time clock:	approx. 4 weeks at 20 °C
Voltage supply	
roccage suppry	
Nominal voltage :	24 V
Supply voltage:	18-30 V
Optical isolation:	1000 V
Reverse voltage protection:	1 A max.
Connectors	
	1 x 5-pin male M12 connector
24 VDC output	1 x 5-pin female M12 connector
	Extended FLASH memory: Buffered real-time clock: Voltage supply Nominal voltage : Supply voltage: Optical isolation: Reverse voltage protection:

4 MB for system data

16 differential inputs

4 groups with 4 channels each

multiplexer per converter 16-bit, SAR ADC

 \pm 1.221 mV typ. (\pm 4 LSB)

 \pm 5 V, \pm 10 V software-programmable

80 dB min. DC up to 60 Hz (diff. amplifier)

x1, x2, software-programmable

160 kHz limited through TP filters

16 Hz version with differential filter

Digital input, software-programmable

10⁹ Ω // 10nF against GND

2.3 x V_{in} + 22.5 (µV / °C) typ.

8 x 5-pin female M12 connector

± 2.442 mV max

 \pm 3 LSB max. (ADC) 1000 V

1 kHz per channel

 \pm 1 LSB (\pm 305 μ V)

4.5 ppm/°C FSR

 \pm 2.5 LSB

64 MB

4-port simultaneous converter with one 4-channel

Specifications*

Analog inputs

Relative precision (INL):

Common mode rejection:

Input impedance (PGA):

Bandwidth (-3 dB):

Temperature drift : V_{in} : input voltage in Volts (-10 V \leq V_{in} \leq +10 V) In the temperature range: from -40 °C to +85 °C

Connectors for sensors

Data storage

Optical isolation: Input ranges:

Input frequency:

Number/type:

Architecture

Resolution:

Accuracy:

Gain:

Trigger:

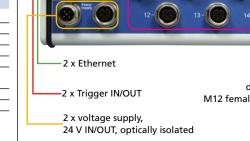
RAM:

FLASH:

Offset error:

Gain error:

Ethernet			
Interface:	IEEE802.3 specification		
Number of ports:	2		
Cable length:	150 m	max. at CAT5E UTP	
Bandwidth:	10 Mbps	auto-negotiation	
	100 Mbps	auto-negotiation	
Protocol:	10Base-T	IEEE802.3 compliant	
	100Base-TX	IEEE802.3 compliant	
Optical isolation:	1000 V		
MAC address:		00:0F:6C:##:##:##, unique for each device	
Connectors	2 x 4-pin flange	-type socket, D-coded M12	
	for Port 0 and P	for Port 0 and Port1	



tion		5- (O)) 10-(3- (O)
UTP		
n		
n Iliant	2 x Ethernet	
liant	2 x Trigger IN/OUT	M12 fe
device d M12		
A 19112	2 x voltage supply, 24 V IN/OUT, optically isolated	

For cascading:





IP 65

Integrated Ethernet switch



40 °C request







More information at www.addi-data.com The intelligent Ethernet data logger MSX-ilog-RTD/TC has 8 or 16 differential inputs for thermocouples or resistance temperature detectors (RTD, Pt100/Pt1000). The parametering and visualisation of the measured values are carried out via an integrated web site. Thus no additional software installation is needed.

The acquisition, visualisation and data storage take place automatically.



Features

- Onboard ARM[®]9 32-bit processor
- 4 GB memory, data remains stored at power loss
- The buffered real-time clock keeps the system time even without supply voltage
- Robust metal housing
- Power Save Mode: Reduced power consumption when no acquisition runs
- Analog inputs
- 8-pin M12 female connectors
- 16/8 differential inputs for thermocouples or RTD, 24-bit
- Max. sampling frequency 1 kHz

Acquisition

- Automatic acquisition and recording of measured data
- Conversion into temperature (°C)
- Acquisition of virtual channels

Trigger

- Acquisition triggered via hardware or software
- 24 V hardware trigger
- Threshold trigger (when the defined level of the analog inputs is exceeded)
- Optional pre-trigger (records events which have occurred before the trigger event)

Triggers from external hardware, e. g. MSX-E systems, are possible

Alarm functions

• Upper and lower limits of channels

Flash memory,

real-time clock

- Data storage depending on alarms •
- Can be combined with the pre-trigger

Analysis

- Online graphical analysis of measured data
- Data export (xml, csv)

Safety features

- LED status display for fast error diagnostics
- Optical isolation Input filters
- Overvoltage protection ± 40 V
- Internal temperature monitoring

Applications

- Data logger Long-term data recording
- Monitoring of infrastructure

Interfaces

Combination with external hardware

Ethernet-

Trigger IN-

24 V

0

System 1

Temperature data logger

- Fast 24 V trigger input
- Ethernet switch with 2 ports
- Trigger In/Out
- 24 V supply and cascading

Communication interfaces

- Web server (configuration and monitoring)
- Data server (TCP/IP or UDP socket) for sending acquisition data

Ethernet

Trigger

24 V



System 2

e.g. digital I/O

0 0 0 Ethernet

Trigger

24 V

* Preliminary product information

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Ethernet and supply signals can be

looped e.g. from the MSX-ilog-RTD/TC

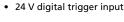
to MSX-E systems. These can then react to the values measured by the MSX-

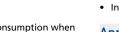
ilog-RTD/TC (e.g. via alarm or trigger)

and acquire and switch distributed I/O

signals. Monitoring or regulation tasks

can be realised.





Technology



MSX-7log

Specifications*

Analog inputs

/ mateg mpate	
Number of inputs:	16 or 8 differential inputs for thermocouples or RTD
Resolution:	24-bit
Optical isolation:	1000 V
Throughput:	max. 1000 Hz
Data storage	
RAM:	64 MB
FLASH:	4 MB for system data
Extended FLASH memory:	4 GB (3.7 GB for measured data)
Buffered real-time clock:	approx. 4 weeks at 20 °C

System features	
Interface:	Ethernet acc. to specification IEEE802.3
Dimensions:	220 x 140 mm x 50 mm
Weight:	620 g
Degree of protection:	IP 65
Current consumption at 24 V:	150 mA ± 10 % typ. in Idle/Powersave
Operating temperature:	-25 °C to +85 °C (-40 °C to +85 °C on request)
Sensor connectors	
Analog inputs:	8 x 8-pin female connectors, M12

System requirements

Standard browser (Internet Explorer, Firefox) with Java from version 1.6.x

	Features		
Status LEDs			
Temperature Ethernet Data Logger		0	0
	G92-613	CIH-CIIS	CH5-CH7
	CHIP-CHI1	CHI2-CHI3	CH14-CH15
2 x Ethernet 16/8 differential for temperature acqu 8-pin M12 female conr			cquisition,
Trigger IN/OUT		i 2 Temale C	onnectors
24 V IN/OUT, optically is	olateu		

Ordering information

Voltage supply

Nominal voltage :	24 V
Supply voltage:	18-30 V
Optical isolation:	1000 V
Reverse voltage protection:	1 A max.
Connectors	
24 VDC input	1 x 5-pin male M12 connector
24 VDC output	1 x 5-pin female M12 connector

Ethernet

Ethernet acc. to	Ethernet acc. to IEEE802.3 specification		
2			
150 m	max. at CAT5E UTP		
10 Mbps	auto-negotiation		
100 Mbps	auto-negotiation		
10Base-T	IEEE802.3 compliant		
100Base-TX	IEEE802.3 compliant		
1000 V			
	00:0F:6C:##:##:##, unique for each device		
Connectors 2 x 4-pin flange-type socket, D-coded M			
for Port 0 and Port1			
	2 150 m 10 Mbps 100 Mbps 10Base-T 100Base-TX 1000 V 00:0F:6C:##:##: 2 x 4-pin flange		

Trigger

Number of inputs:	1 trigger input	
Number of outputs:	1 trigger output	
Filters/protective circuit:	Low-pass/transorb diode	
Optical isolation:	1000 V	
Nominal voltage:	24 V external	
Input voltage:	0 to 30 V	
Input current:	11 mA at 24 VDC, typical	
Input frequency (max.):	2 MHz at 24 V	
Connectors		
Trigger input :	1 x 5-pin flange connector M12	
Trigger output:	1 x 5-pin flange-type socket M12	
1		

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

MSX-ilog-RTD / MSX-ilog-TC

Ethernet data logger for temperature measurement, 16/8 channels for thermocouples or RTD, 24-bit. Incl. technical description.

Versions

MSX-ilog-RTD-16:	for 16 RTD
MSX-ilog-RTD-8:	for 8 RTD
MSX-ilog-TC-16:	for 16 thermocouples
MSX-ilog-TC-8:	for 8 thermocouples

Connection cables

Voltage supply

CMX-2x:	Shielded cable, M12 5-pin female connector/open end, IP 65
CMX-3x:	For cascading, shielded cable, M12 5-pin
	female connector/male connector IP 65

Trigger

CMX-4x: Shielded cable, M12 5-pin female connector/open end, IP 65
 CMX-5x: For cascading, shielded cable, M12 5-pin female connector/male connector IP 65

Ethernet

CMX-6x: CAT5E cable, M12 D-coded male connector/RJ45 connector **CMX-7x:** For cascading: CAT5E cable, 2 x M12 D-coded male connector

Options

MSX-E 5V-Trigger: Level change of the trigger inputs and outputs to 5 V MX-Clip, MX-Rail (Please specify when ordering!), MX-Screw, PCMX-1x

* Preliminary product information













More information at www.addi-data.com

The intelligent Ethernet data logger MSX-ilog-Al16-Dl40 has 16 differential analog inputs, 16-bit, with a transfer rate of 200 kHz as well as 36 digital inputs, 24 V. Four additional 24 V digital outputs are available for the switching of actuators and the transfer of signals.

The parametering and visualisation of the measured values are carried out via an integrated web site which is accessible over a standard browser (Internet Explorer, Firefox) with Java from version 1.6x. Thus no additional software installation is needed.

The acquisition, visualisation and data storage take place automatically.

Features

- 64-bit MIPS processor
- 128 GB memory (SSD hard disk), data remains stored at power loss
- The buffered real-time clock keeps the system time even without supply voltage
- Robust metal housing
- Power Save Mode: Reduced power consumption when no acquisition runs

Analog inputs

- 16 diff. inputs, 16-bit, 37-pin D-Sub connector
- Sampling frequency max. 200 kHz
- Input ranges: 0-10 V, ±10 V, 0-5 V, ±5 V, 0-2 V, ±2 V, 0-1 V, ±1 V
- Current inputs: 0-20 mA (option) can be combined freely with voltage inputs

Digital inputs and outputs

- 36 opto-isolated digital inputs, 24 V
- 4 opto-isolated digital outputs, 5 V to 30 V, open collector
- Output current for each channel max. 50 mA typ.
- 2x 37-pin D-Sub connector (1x 32 digital inputs, 1x 8 digital I/O)

Acquisition

- Automatic acquisition and recording of measured data
- Conversion of measured data into real values e. g. mm, bar, temperature, etc.
- Acquisition of virtual channels

MSX-ilog-AI16-DI040

16 analog inputs, differential, 16-bit

Voltage or current inputs	
36 dig. inputs, 4 dig. outputs, 24 V	
Acquisition, visualisation and analysis in one device	
No software installation needed	
Automatic storing of measured values	

(internal SSD hard disk)

Trigger

- Acquisition triggered via hardware or software
- 24 V hardware trigger (external)
- Trigger through timer (internal)
- Threshold trigger (when the defined level of the analog inputs is exceeded)
- Trigger from external software (Software trigger)
- Manual trigger (web interface)
- Optional pre-trigger (records events which have occurred before the trigger event)

Alarm functions

- Upper and lower limits of channels
- Data storage depending on alarms
- Can be combined with the pre-trigger

Analysis

- Online graphical analysis of measured data
- Data export (xml, csv)

Safety features

- Optical isolation 1000 V
- Input filters
- Overvoltage protection ± 40 V
- Internal temperature monitoring

Applications

- Data logger Long-term data recording
- Monitoring of infrastructure

Interfaces

- Fast 24 V trigger input
- Fast Ethernet (100 MBit/s)

Communication interfaces

- Web server (configuration and monitoring)
- Data server (TCP/IP or UDP socket) for sending acquisition data



* Preliminary product information

> info@addi-data.com www.addi-data.com



MSX-1log

Specifications*

Analog inputs

Number/type:	16 differential inputs
Resolution:	16-bit
Input ranges:	0-10 V, ±10 V, 0-5 V, ±5 V, 0-2 V,
	±2 V, 0-1 V, ±1 V
	0-20 mA optional
Input frequency:	200 kHz
Connector for sensors:	37-pin D-Sub connector

Digital inputs

J 1 1 1 1	
Number:	36
Optical isolation:	Over opto-couplers, 1000 V
Nominal voltage:	24 V
Connectors:	2x 37-pin D-Sub connector (inputs 1-32 and 33-36)

Digital outputs

Number:	4
Output type:	Open collector
Optical isolation:	Over opto-couplers, 1000 V
Max. switching current:	50 mA typ.
Nominal voltage:	24 V
Connectors:	1 x 37-pin D-Sub connector
	(together with digital inputs 33-36)

Data storage

- and ottor age	
RAM:	128 MB
FLASH:	16 MB for system data
SSD hard disk:	128 GB (127 GB for measuring data)
Buffered real-time clock:	approx. 2 years at 20 °C
	RAM: FLASH: SSD hard disk:

100 V - 240 V, AC, 47-63 Hz (other on request)

Voltage supply

Input voltage:

the sum of

Ethernet	
Interface:	Ethernet acc. to IEEE802.3 specification
Number of ports:	1
Cable length:	150 m max. at CAT5E UTP
Bandwidth:	10 Mbps auto-negotiation
	100 Mbps auto-negotiation
Protocol:	10Base-T IEEE802.3 compliant
	100Base-TX IEEE802.3 compliant
MAC address:	unique for each device
Connector:	RJ45

Trigger Number of inputs:

Number of inputs:	1 trigger input (digital input 33)	
Filter/protective circuit:	Low-pass/transorb diode	_
Optical isolation:	1000 V	_
Nominal voltage:	24 V external	

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

System features

Housing:	Chromated aluminium, colour RAL 5010, "Enzianblau"	
Heat dissipation:	Through programmable fan	
Interface:	Ethernet acc. to IEEE802.3 specification	
Housing dimensions:	278 x 170 x 165 mm	
Temperature range:	0 – 50°C	

System requirements

Standard browser (Internet Explorer, Firefox) with Java from version 1.6.x

Ordering information

MSX-ilog-AI16-DI040

Ethernet data logger, 16 analog inputs, 16-bit, diff., 40 dig. I/O. Incl. technical description.

Terminal panels / Connection cables	
PX901-AG:	Screw terminal panel with transorb diodes with housing
	for DIN rail for connecting the analog I/O
PX901-ZG:	Screw terminal panel for connecting the digital inputs
	(channel 33-36) and outputs, for DIN rail
PX901-DG:	Screw terminal panel with LED status display for DIN rail
ST011:	Standard round cable, shielded, twisted pairs, 5 m
ST010:	Standard round cable, shielded, twisted pairs, 2 m

Options

Option PC-Diff: Current input for 1 diff. channel 0(4)-20 mA **Option DF:** Precision filter for 1 channel

MSX-SCREW: Assembly equipment for direct mounting on machines **MSX-RAILDIN:** Assembly equipment for DIN-rail mounting

Additional analog inputs: on request Additional digital I/O: on request SSD hard disk with more storage space: on request



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REAL-TIME ETHERNET



YOUR BENEFITS

- Precise, fast, robust, reliable
- Hot-plug enabled
- Long-term availability of the product

x-ARTS: ADDI-DATA Realtime Slave System

Real-time Ethernet systems for the field: precise, robust and reliable

The real-time component plays an important role in distributed control and regulation tasks. That's why ADDI-DATA has developed a new product family of real-time Ethernet systems: x-ARTS. These robust systems are designed for measurement, control and automation applications with various real-time requirements.

High level of protection

The x-ARTS are available for EtherCAT (EC-ARTS), ProfiNet (PN-ARTS) and VARAN (V-ARTS). They are particularly suited for use in the field, where interferences are an everyday event. In order to assure reliable operation, many protective mechanisms are built-in.

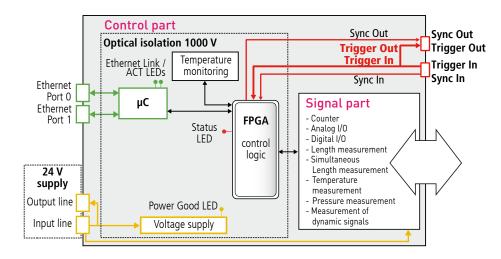
- Protective circuits such as optical isolation, etc.
- Robust metal housing
- IP 65 degree of protection

• Extended temperature range from -40 °C to +85 °C (oper. temperature) The x-ARTS stand for quality and reliability. Like all other ADDI-DATA products, they are available for years. So for you, they make an all-around safe investment.

The optimal solution

The real-time Ethernet systems from ADDI-DATA in many respects make an optimal choice for measurement and automation tasks. They are characterised by highly precise inputs. The systems can measure faster than the bus cycle and buffer the acquired data. Moreover, the measurement can be started independently from the bus, since by using the 24 V trigger input, the x-ARTS may be combined with hardware that is not connected to the bus. For example, a light barrier can serve as a trigger signal.

In addition, several systems or signals can be synchronised with one another using the synchro line, all in a period of less than 1 µs. The x-ARTS can also tie together signals from various external devices, such as encoders and analog inputs, and in this way acquire values faster than the bus cycle. This increases the efficiency of your application. Various diagnosis possibilities, retrievable via Ethernet, can be set to work in the real-time Ethernet systems from ADDI-DATA.



The x-ARTS consist of a signal part and a control part with optical isolation.

The real-time connection can be accomplished through EtherCAT, Profi-Net or VARAN. Real-time Ethernet can be used looped-through or as a point-to-point connection.



* Preliminary product information









More information: www.addi-data.com

ETHERCAT

EtherCAT is appropriate for both hard and soft real-time requirements. It makes possible a large variety of topologies, such as lines, trees, rings, stars and combinations of these. Switches are thus made superfluous. In order to optimise the speed, processing the frame begins immediately, even if the frame still has not been entirely received. Sending follows the same principle. In order to assure precise synchronisation, even for widely separated network participants, the master clock is always compared to the slave clocks.

EC-ARTS-Systeme

The EC-ARTS are slave systems that are entirely compatible with EtherCAT. Programming of the systems is done through SDO (Service Data Objects).

Available functions

EC-ARTS-3011: Analog inputs (16-bit),

EC-ARTS-3701-4: 4 inputs (24-bit) for inductive transducers, LVDT, half-bridge, Mahr Further systems in preparation: Temperature (RTD/TC, 24-bit), pressure (24-bit)

Examples of EC-ARTS applications

Various signals are acquired or output in a test bench in the automobile industry. In order to reduce cable complexity, distributed systems are used. The acquisition runs in real time in order to regulate precisely.

Advantage of EC-ARTS: can be mounted directly on the machine, precise data acquisition

PROFINET



ProfiNet supports both standard Ethernet and real-time connections. It is based on the provider-consumer model, which envisages granting equal rights to the network participants. This model stands in contrast to the standard master-slave process. Not only are process data transferred via ProfiNet, but functions such as web server, e-mail and FTP data transfer are also supported.

Basically, ProfiNet is divided into two function classes: ProfiNet CBA and ProfiNet IO. These are in turn broken down into three "performance classes". Classes RT and IRT are relevant to real-time requirements. RT is used for real-time I/O data traffic in automation technology. IRT is an asynchronous real-time communication that was developed especially for motion control applications.

PN-ARTS systems

The PN-ARTS systems from ADDI-DATA are suitable for the RT and IRT performance classes. That means that transfer rates of less than 1 ms (IRT) to 10 ms (RT) can be supported, depending on requirements.

Available functions

PN-ARTS-AI-16: Analog inputs (16-bit), Further systems in preparation: Temperature (RTD/TC, 24-bit), pressure (24-bit)

Examples of PN-ARTS applications

Profi-Net is very well suited to support PLCs. Since it was in part developed by Siemens, the interaction of a PLC and ProfiNet devices is quite smooth. In this way, the PN-ARTS real-time systems can take on tasks that must be completed within a defined time period. They thus relieve the load on the PLCs.

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www.addi-data.com







VARAN – Versatile Automation Random Access Network

The VARAN bus was completed as a hardware solution and developed for hard real-time requirements. It is characterised by high speed, short cycle times and minimal synchronicity jitters. The possibility of asynchronous access is unique. The information exchange consists of simple memory write/read commands. The bus manager coordinates the entire data traffic in order to avoid collisions. Data packets can be repeated within a cycle up until receipt of a valid back confirmation.

The VARAN bus can also transport standard Ethernet frames.

V-ARTS systems

The V-ARTS are slave systems that are completely compatible with VARAN. They are connected with the master by a pointto-point connection and can send 10 data packets per analog input.

Available functions

V-ARTS-AI-16: Analog inputs (16-bit), Further systems in preparation: Temperature (RTD/TC, 24-bit), pressure (24-bit)

Examples of V-ARTS applications

V-ARTS are, for example, perfectly suited for hydraulic testing in the aviation sector. They assure that all measured values (100 kHz) are acquired and the data is safely transferred while the interplay with the control runs without interruption.



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EtherCAT analog input system 16 analog inputs, diff., 16-bit





EC-ARTS-3011

16 analog inputs, differential, 16-bit

Voltage or current inputs

Simultaneous acquisition of 4 channels

Fast distributed data acquisition

M12 connector



Applications

- Industrial process control and measurement
- · Industrial measurement and monitoring
- Process monitoring
- Remote diagnosis
- Control of chemical processes
- Factory automation

Interfaces

- EtherCAT interface
- RS422 trigger interfaces
- 24 V supply

	Current consumption at 24 V:	to be defined
	Reverse voltage protection:	
	Trigger	
ne 4-channel	Number of inputs:	1 trigger input
	Optical isolation:	1000 V
	Signal type:	RS422
	EMC - Electromag	netic compatibility

EMC - Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

System features Interface: EtherCAT 215 x 110 x 50 Dimensions (mm): 850 g Weight: Degree of protection: IP 65 40 °C to + 85°C Operating temperature:

EC-ARTS-3011 interface connectors

EtherCAT:	2 x 4-pin M12 female connector, D-coded for port 0 and port 1
RS422 interface:	1 x 5-pin M12 male connector,
	1 x 5-pin M12 female connector
Voltage supply	
24 VDC input:	1 x 5-pin M12 male connector
24 VDC eviteriute	1 v E nin M12 fomale connector

24 VDC output: 1 x 5-pin M12 female connector

Sensor connectors for analog inputs 16 x 5-pin M12 female connector

Ordering information

EC-ARTS-3011

EtherCAT analog input system, 16 analog inputs, diff., 16-bit. Incl. technical description and software drivers

* Preliminary product information



Number/input type:

Architecture:	4 groups with 4 channels each
, a cance can en	4-port simultaneous converter with one 4-channel
	multiplexer per converter
Resolution:	16-bit, SAR ADC
Accuracy:	± 1.221 mV typ. (± 4 LSB)
···· ,	± 2.442 mV max.
Relative precision (INL):	\pm 3 LSB max (ADC)
Optical isolation:	1000 V
Input ranges:	± 5 V, ± 10 V (16-bit), 0-5 V, 0-10 V (15-bit)
	software-programmable, current inputs optional
Input frequency:	25 kHz per channel / 100 kHz max.
Gain:	x1, x2, software-programmable
Common mode rejection:	80 dB min. DC up to 60 Hz (diff. amplifier)
Input impedance (PGA):	$10^9 \Omega$ // 10nF against GND
Band width (-3dB):	160 kHz limited through TP filters
	16 Hz version with differential filter
Trigger:	digital input, synchro, software-programmable
Offset error:	± 1 LSB (± 305 μV)
Gain error:	± 2,5 LSB
Temperature drift V_{in} : input voltage in Volts (-10 V $\leq V_{in} \leq +10$ V) in the temperature range	2.3 x V _{in} + 22.5 (μ V/ °C) typ.
from -40°C to +85°C:	4.5 ppm/°C FSR
Power Supply	
Nominal voltage:	24 V
Supply voltage:	18-30 V
Optical isolation:	1000 V

Speci Analog inputs 16 differential inputs

• 64 MB onboard SDRAM for storing data

• 16 diff. inputs 16-bit, M12 5-pin female connector

• Robust normed metal housing

Features

runs

Analog inputs

Input ranges:

• Current inputs optional

Acquisition modes:

Untriggerted live signal
Acquisition triggered through external motor controller
Average mode
Specifications*
nolog innuto

• Power Save Mode: Reduced power consumption when no acquisition

• Sampling frequency max. 100 kHz, up to 4 simultaneous channels

± 5 V, ± 10 V (16-bit)

0-5 V, 0-10 V (15-bit)

EtherCAT system for length measurement, 24-bit 4 inductive transducers, LVDT, Half-Bridge, Mahr





EC-ARTS-3701-4

Simultaneous acquisition of up to 4 inductive transducers

For Half-Bridge, LVDT or Mahr transducers

RS422 trigger interface



Features

- Connection of all commercially available transducers (Half-Bridge, LVDT, Mahr)
- 4 channels
- 24-bit resolution
- Fast distributed data acquisition
- 16 MB onboard SDRAM for storing data

Ether**CAT**

- Diagnostic possibility at short-circuits or line break of the transducers
- Robust metal housing
- Power Save Mode: Reduction of the power consumption when no acquisition runs
- RS422 trigger interface Acquisition modes:
- Untriggered live signal
- Acquisition triggered by external motor controller
- Average mode

Specifications*

Inputs for inductive transducers

Channel features	
Number:	4 simultaneous
Input type:	single ended
Coupling:	DC
Resolution:	24-bit
Sampling frequency f_s :	At primary frequency $f_{\rm p}$ of
	5 kHz
	7.69 kHz
$f_{\rm s} = f_{\rm P}$	10 kHz
	12.5 kHz
	20 kHz
Example with TESA GT21:	$f_{\rm s} = f_{\rm p}$ = 12.5 kHz
Input level	
Input impedance:	2 kΩ for HB
	10 k Ω for LVDT
	100 k Ω on request, 10 M Ω on request
Sensor supply (sine generation	
Туре:	Sine differential (180° phase-shift)
Coupling:	AC
Programmed signals:	
Output frequency f_{P}	2-20 kHz depending on the transducer
(primary frequency)	
Output impedance:	$< 0.1 \Omega$ typ., $> 30 \text{ k}\Omega$ typ. in shutdown mode
Short-circuit current:	0.7 A typ. at 25°C with thermal protection
Voltage Supply	
Nominal voltage:	24 V
Voltage supply:	18-30 V
Optical isolation:	1000 V

Transducer precision: Measurement example

Type TESA GT21, range \pm 2 mm (\triangle 4 mm), 16-bit precision

 $\frac{4 \text{ mm}}{2^{16}}$ = ± 61 nm = 0.061 µm

Applications

IP 65

- Gear wheel measurement Gauge block measurement
- Sensor data acquisition
- Quality assurance, automatic component testing
- Industrial process control Profile and surface measurement

Interfaces

- EtherCAT
- RS422 interfaces
- 24 V supply

Current consumption at 24 V:	to be defined
Reverse voltage protection:	
Trigger	
Number of inputs:	1 trigger input
Optical isolation:	1000 V
Signal type:	RS422
EMC - Electromagnetic compatibility	

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

System features		
Interface:	EtherCAT	
Dimensions (mm):	220 x 140 x 50	
Weight:	530 g	
Degree of protection:	IP 65	
Operating temperature:	40 °C to + 85°C	
EC ADTC 2701 int	orface connectors	

EC-ARTS-3701 Interface connectors EtherCAT: 2 x 4-pin M12 female connector, D-coded for port 0 and port 1 R5422 interfaces: 1 x 5-pin M12 male connector, 1 x 5-pin M12 female connector Voltage supply 24 VDC input: 24 VDC input: 1 x 5-pin M12 male connector 24 VDC output: 1 x 5-pin M12 male connector

Connectors for connecting inductive transducers 4 x 5-pin M18 female connectors

EC-ARTS-3701-4

EtherCAT system for length measurement, 24-bit, 4 inductive transducers, LVDT, Half-Bridge, Mahr-compatible. Incl. technical description and software drivers Versions

EC-ARTS-3701-HB-4: for 4 HB inductive transducers

* Preliminary product information



Ordering information

PAC SYSTEMS MSX-BOX

Distributed data acquisition and control in real time

The MSX-Box is an open Programmable Automation Controller system (PAC). It has been specially developed for industrial measurement, control and automation applications in real time where processes have to be carried out within a defined time.



The concept

- Modular platform for distributed measurement, control and regulation applications in real time
- Based on established standard technologies like PCI backplane or CompactPCI backplane
- Non-proprietary system: I/O PCI boards or CompactPCI boards from other producers can be used.
- Low maintenance: Linux operating system with RTAI extension no update obligations
- Reduced costs: no software licence costs
- No unnecessary multimedia features: Full machine time only for your application
- Optimise your system: Free access to the software down to the kernel source code for extensive adaptations of your measuring system
- Real-time development tools without additional costs
- Investment security: Long-term availability of the products thanks to the ADDI-DATA supply philosophy

Server Process control system I LEVEL e. g. PLC, IPC, etc. HIGH-LEVEL CONTROL e. g. Ethernet, Profibus, etc. Production data Production data Production data SIGNALS: digital I/0, counter, analog I/0, serial interfaces, motion control, etc.

Between the production and IT level

The MSX-Box acquires sensor and machine data, processes them, and controls or regulates the corresponding terminals.

The MSX-Box can be integrated into a higher-level control system via Ethernet or Profibus. By integrating the MSX-Box into the company network via Ethernet, data can be forwarded to software packages on the IT level for use in statistics or process optimisation.



PAC systems

Programmable Automation Controller

PAC systems are mainly used for industrial measurement and control or regulation tasks as well as for motion control.

They execute several tasks simultaneously and in a deterministic way.

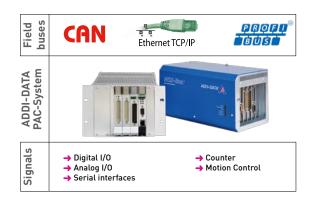
Core features of a PAC system:

- Compact and robust design
- Programmable
- Standard Ethernet (TCP/IP)
- CPU board as system controller
- Different I/O modules



Integrated into the field level

Measurement and control systems that monitor entire processes and interact with machines or hardware must be capable of working with data of different origins.



Field buses and signals

With the MSX-Box, you can acquire signals from different field buses: CAN, Profibus, Ethernet, or signals from serial lines such as ultrasound sensors or scales.

The PAC system can also process the following signals:

- Digital I/O
- •
- Counter: Incremental, SSI etc.Analog I/O
- Serial interfaces
 Motion control
- etc.

Good for retrofits, too

→ The MSX-Box is suitable both for setting up a new automation project and for optimising existing installations. That means you continue to benefit from your familiar hardware, while still equipping your installation with the latest technology for more efficiency where it counts.

Application examples

Example 1

In a steel plant, the MSX-Box is used to measure the thickness of sheets. Laser sensors (± 10 V) are used to measure the sheet thickness. Simultaneously with the thickness measurement, incremental counters are used to determine the position of the sheet and the feed rate.

The deviation in sheet thickness is calculated in real time.

The result of the calculation is used to control the position of

the rollers in the next process step and thus to produce sheets of a uniform thickness.

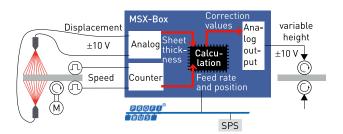
This information is then provided to higher-level control systems through a Profibus interface, e.g. for statistics, process analysis, or as specification values for downstream processes.

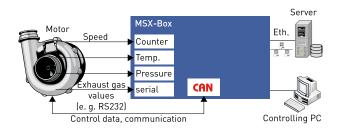
Example 2

During inspection of engines, different metrics are acquired based on different signal types: speeds, temperatures, pressures, exhaust gas values, etc. Using the integrated CAN interface, messages from the CAN bus can also be recorded.

To obtain a meaningful measurement result, all metrics must be acquired at defined points in time. The value of all metrics is then measured at time t.

To use the data for later evaluation, the MSX-Box stores the measured values in a database.







PAC system MSX-Box for the PCI bus





- Open and transparent Programmable
 - Automation Controller system
- With free development tools
- Live DVD based on open source programs
- Real-time measurement system

PAC systems

Programmable Automation Controller

PAC systems are mainly used for industrial measurement and control or regulation tasks as well as for motion control.

They execute several tasks simultaneously and in a deterministic way.

Core features of a PAC system:

- Compact and robust design
- Programmable
- Standard Ethernet (TCP/IP)
- CPU board as system controller
- Different I/O modules

Set course for freedom

Experience with the MSX-Box what freedom of decision-making really means:

- You select the components of your PAC system: The MSX-Box is based only on reliable standard technologies like for example PCI backplane. Freedom also means that you can use any of the numerous standard PCI I/O boards.
- You decide, whether and when to update your operating system: Using the realtime operating system Linux with RTAI extension, no need to take care of updates. Save time and money!
- You have free access to the software down to the kernel source code: You can make extensive system adaptations and realize your own optimized measurement system.

Boost your applications

Working with the MSX-Box that fits to your needs will boost your measurement and control applications.

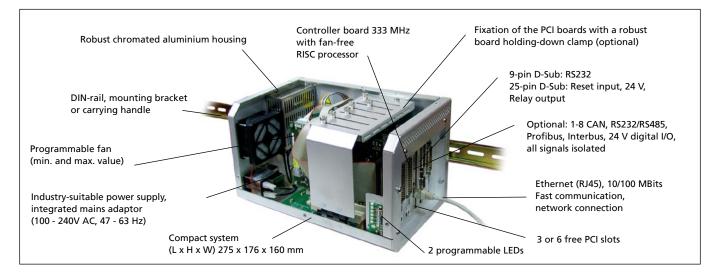
The MSX-Box is supplied with development tools: You can realize even very complex tasks quite easily.

The most important advantage of a PAC system with such a transparent structure is that in case of emergency, you can react fast and efficiently.

Thanks to the long-term ADDI-DATA supply philosophy, you secure your investments for a long time.

Furthermore, the fact that the MSX-Box is supplied with free development tools limits the purchase price for serial equipment.

Experience today how to realize your applications of tomorrow: www.msx-box.com





info@addi-data.com 22 www.addi-data.com



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MSX-Box-500

PCI	controller	r board
-----	------------	---------

RISC processor:	64-bit MIPs, no fan
Clock:	333 MHz
Memory:	16 MB flash, 128 MB SDRAM, Option up to 256 MB
Installed OS:	Embedded RTAI Linux
Standard interfaces:	D-Sub 9-pin: 1 x RS232
	D-Sub 25-pin: Reset input 24 V; "H" active
	1 x relay output, free prog.,
	closing contact
Optional:	D-Sub 25-pin.: 1-8 CAN, Master/Slave, isolated
	1 x RS232/RS485, isolated
	additional bracket:
	1 x Profibus/Slave, isolated
	2 x Interbus/Master, isolated
	4 x dig. input, 24 V/10 mA, isolated
	3 x dig. output, 24 V/200 mA, isolated
Dimensions:	PCI half-size board

Mains supply unit

	Input voltage:	100 V - 240 V, AC, 47-63 Hz (other voltage on request)
	Output voltage:	5 VDC-40 W (max. 6 A) (other voltage on request)
	Protection against:	Short circuit; overload, overvoltage
	Connection:	2 m power cable

ATX backplane with 5 PCI slots

PCI slots:	Total amount: Reserved: Free:	1 x PCI controller board 1 x PCI Ethernet board for 3 additional PCI half-size boards
Compliance:	PCI specification	on PICMG rev. 2.1.

MSX-Box-800

Same as MSX-Box-500 with 8 PCI slots on the ATX backplane, incl. 6 free slots for PCI I/O boards

Mains supply unit

Input voltage:	100 V - 240 V, AC, 47-63 Hz (other voltage on request)
Output voltage:	5 VDC/12 VDC/60 W (max. 6A)
Protection against:	Short circuit; overload, overvoltage
Connection:	2 m power cable

ATX backplane with 8 PCI slots

PCI slots:	Total amount: Reserved: Free:	8 1 x PCI controller board 1 x PCI Ethernet board for 6 additional PCI half-size boards
Compliance:	PCI specification	on PICMG rev. 2.1.

For MSX-Box-500 and MSX-Box-800

PCI Ethernet board (RJ45)

Data transfer rate: 10/100 MBits

Extensive software support

Free development tools (GNU Compiler, Cygwin, samples in source code), Knoppix Live DVD development environment

Housing

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Material:	Chromated aluminium, colour RAL 5010 blue "Enzianblau"
Heat dissipation:	Through programmable fan
Temperature range:	0 - 50°C
Temperature monitoring:	Configuration at delivery 5 °C to 45 °C, min. and max. value programmable through software. The temperature value can be monitored. Resolution: 0.5 °C
Front openings:	For 5 PCI-boards and 1 bracket (MSX-Box-500) For 8 PCI-boards and 3 brackets (MSX-Box-800)
Housing dimensions	278 x 170 x 165 mm (MSX-Box-500)
(L x H x W):	292 x 170 x 292 mm (MSX-Box-800)
Weight:	approx. 2 kg (standard MSX-Box system) MSX-Box-500
	approx. 3 kg (standard MSX-Box system) MSX-Box-800
Status display:	5 LEDs, incl. 2 freely programmable

Optional accessories

Board fixation:	Board holding-down clamp
Mounting possibilities:	DIN rail Removable mounting bracket Carrying handle
Cable:	2 m Ethernet patch cable, shielded, RJ45 connector (PC \leftrightarrow MSX-Box)
Network card: MSX-ComboCard with additional functions:	 2 x PCI FireWire IEEE 1394, 1 x internal, 1 x ext. connection, data transfer rate up to 400 Mbps 2 x PCI USB 2.0, 2 external, 1 x internal connection, 1 x RJ-45 LAN, 10/100 Mbps connection 1 x 5-pin female connector, 12 V Network card PCI 10/100 Mbps, 10Base-T, 100Base-TX, IEEE802.3, 802.3 u protocol, recognition of data transfer rate 10 Mbps or 100 Mbps, data transfer rate 10 Mbps and 100 Mbps, Chipset REaltek RTL8139, 32-bit PCI system 5 V voltage
Colours:	Other housing colours (according to RAL scale) and inscriptions (on request)



MSX-Box: PAC system, incl. development tools (GNU compiler, Cygwin, source code samples, ...) and technical description Versions

MSX-Box 500: 5 PCI slots (incl. 2 slots reserved for controller and Ethernet board: 3 free PCI slots for half-size boards) MSX-Box 800: 8 PCI slots (incl. 2 slots reserved for controller and Ethernet board; 6 free PCI slots for half-size boards)

Options

MSX-256MB: Memory extension up to 256 MB MSX-485/ MSX-232: 1-port serial interface, RS485 or RS232, optically isolated

MSX-Basis: Basic equipment for options MSX-CAN, MSX-Profibus, MSX-IBS and MSX-DIO-IO MSX-CAN-x: 1/2/4/8 x CAN bus, master/slave, optically isolated MSX-Profibus: 1 x Profibus, slave

FB-Profibus: 9-pin D-Sub female connector for the option MSX-Box Profibus (please order separately)

MSX-IBS-x: 1/2 x Interbus-S, master

MSX-DIG-IO: 4 digital inputs and 3 digital outputs, 24 V.

All extensions are isolated and include a ribbon cable with a 9-pin D-Sub male connector with bracket

MSX-RTSYNC: for the synchronisation of several MSX-boxes (with time stamp)

Accessories MSX-CLAMP-500/-800: Board holding-down clamp for board fixation

MSX-SCREW: Wall mounting for MSX-Box-500 MSX-SCREW-800: Wall mounting for MSX-Box-800 MSX-RAILDIN: DIN rail mounting MSX-GRIP: Carrying handle MSX-COMBOCARD: Network card LAN / USB /Firewire connection MSX-COMBOGIGA: Network card Giga LAN /USB /Firewire connection MSX-500-PS-12V/-24V: Mains power supply unit 12 V DC or 24 V DC ST ETH-2: Ethernet patch cable 2 m, shielded, RJ45, between PC and MSX-Box MSX-CBLRS232: RS232 cable, 1.5 m - 9-pin.

On request: Other housing colour or inscriptions on the front side



PAC system MSX-Box for the CompactPCI bus





- Open and transparent Programmable
 - Automation Controller system
- With free development tools
- Live DVD based on open source programs
- Real-time measurement system

PAC systems

Programmable Automation Controller

PAC systems are mainly used for industrial measurement and control or regulation tasks as well as for motion control.

They execute several tasks simultaneously and in a deterministic way.

Core features of a PAC system:

- Compact and robust design
- Programmable
- Standard Ethernet (TCP/IP)
- CPU board as system controller
- Different I/O modules

Set course for freedom

Experience with the MSX-Box-CPCI what freedom of decision-making really means:

- You select the components of your PAC system: The MSX-Box-CPCI is based only on reliable standard technologies like for example CompactPCI backplane. Freedom also means that you can use any of the numerous standard CompactPCI I/O boards.
- You decide, whether and when to update your operating system: Using the realtime operating system Linux with RTAI extension, no need to take care of updates. Save time and money!
- You have free access to the software down to the kernel source code: You can make extensive system adaptations and realize your own optimized measurement system.

Boost your applications

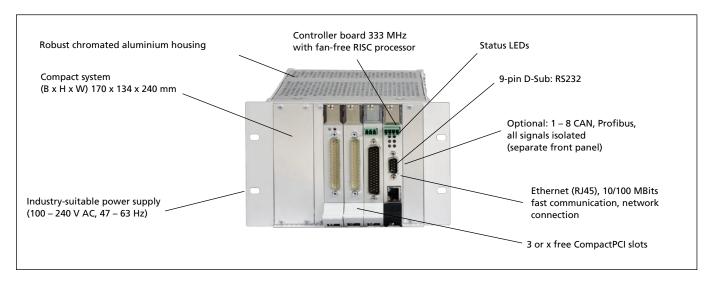
Working with the MSX-Box-CPCI that fits to your needs will boost your measurement and control applications. The MSX-Box-CPCI is supplied with development tools: You can realize even very complex tasks quite easily.

The most important advantage of a PAC system with such a transparent structure is that in case of emergency, you can react fast and efficiently.

Thanks to the long-term ADDI-DATA supply philosophy, you secure your investments for a long time.

Furthermore, the fact that the MSX-Box-CPCI is supplied with free development tools limits the purchase price for serial equipment.

Experience today how to realize your applications of tomorrow: www.msx-box.com





gompactPCI^m

MSX-Box-CPCI-400

CompactPCI controller board

oompace of c		
RISC processor:	64-bit MIPS, no fan	
Clock:	333 MHz	
Memory:	16 MB Flash, 128 MB SDRAM, option up to 256 MB	
Installed OS:	Embedded RTAI Linux	
Standard interface:	D-Sub 9-pin: 1 x RS232	
Safety features:	24 V reset input, H-active;	
	Relay output, freely programmable, closing contact	
Optional:	Additional front panel:	
	D-Sub 25-pin: 1 – 8 CAN, Master/Slave, isolated	
	D-Sub 9-pin: 1 x Profibus/Slave, isolated	
Transfer rate:	10/100 MBits	

Mains supply unit

Input voltage:	100 V – 240 V, AC, 47 – 63 Hz (other voltage on request)
Output voltage:	5 V (depending on the system)
Noise immunity:	Short circuit, overload, overvoltage
Connection:	Power cable, 2 m

CompactPCI backplane with 4 CompactPCI slots

CompactPCI slots:	Total amount:	4							
	Reserved:	1 x CompactPCI controller board							
	Free:	for 3 additional CompactPCI boards							
Specification:	cation: PCI specification PICMG rev. 2.1.								
	PICMG2.0 R3.	0 CPCI Core Specification							
	V I/O +5V								

MSX-Box-CPCI-xxxx

Same as MSX-Box-CPCI-400, but with a CompactPCI-Backplane with x slots, incl. x-1 free slots for CompactPCI boards

Mains supply unit

Input voltage:	100 V – 240 V, AC, 47 – 63 Hz (other voltage on request)
Output voltage:	5 V, 3,3 V, \pm 12 V (depending on the system)
Noise immunity:	Short circuit, overload, overvoltage
Connection:	Power cable, 2 m

CompactPCI backplane with x CompactPCI slots

Number of the CompactPCI	slots according to requirements										
	Reserved: 1 x CompactPCI controller board,										
further slots free for CompactPCI boards											
Specification:	PCI specification PICMG rev. 2.1.										
	PICMG2.0 R3.0 CPCI Core Specification										
	PICMG 2.6 Bridging Specification (according to requirements)										
	V I/O +5 V										

For MSX-Box-CPCI-400 and -xxxx

Extensive software support

Free development tools (GNU compiler, Cygwin, samples in source code ...), Knoppix Live-DVD development environment.

Housing

Material:	Chromated aluminium
Heat dissipation:	Through programmable fan
Temperature range:	0 – 60 °C
Temperature monitoring:	Configuration at delivery: 5 °C to 45 °C, min. and max. value programmable through software. The temperature value can be monitored. Resolution: 1 °C
Front openings:	for 3 CompactPCI boards and 1 bracket (MSX-Box-CPCI-400) for x CompactPCI boards and 1 bracket (MSX-Box-CPCI-xxxx)
Housing dimensions: (L x H x W)	170 x 134 x 240 mm (without fan) (MSX-Box-CPCI-400)
Weigth:	approx. 2.5 kg (standard MSX-Box-CPCI-400 system)
Status display:	6 LEDs, incl. 4 freely programmable

Ethernet patch cable 2 m, shielded, RJ45 connector (PC \leftrightarrow MSX-Box-CPCI)

Optional accessories

Cable:



You will find a large range of adapted CompactPCI boards on page 228

Ordering information

MSX-Box-CPCI: PAC system, incl. development tools (GNU compiler, Cygwin, source code samples, ...) and technical description

Versions

 MSX-Box-CPCI-400:
 4 CompactPCI slots (incl. 1 slot reserved for the controller board; 3 free slots)

 MSX-Box-CPCI-xxxx:
 x CompactPCI slots (incl. 1 slot reserved for the controller board; x-1 free slot)

Options

MSX-256MB: Memory extension up to 256 MB

MSX-Basis: Basic e	quipment for the options MSX-CAN, MSX-Profibus, MSX RTSync
MSX-CAN-x:	1/2/4/8 x CAN bus, master/slave, optically isolated, incl. FB-CPCI-CAN
MSX-Profibus:	1 x Profibus, slave
FB-Profibus:	FB-CPCI-Profi (please order separately)
MSX-RTSYNC:	for the synchronisation of several MSX-Boxes (with time stamp), incl. FB-CPCI-RTSync

On request: further housing dimensions



INTELLIGENT ETHERNET SYSTEMS

Measurement and control directly in the field



The intelligent Ethernet systems of the MSX-E series are especially suited for industrial measurement, control and regulation tasks directly at the measuring point. They are mounted in robust metal housings and comply with the degrees of protection IP67/IP 65/IP 40. Furthermore, they can be used in a temperature range from -40 C to +85 °C as they are equipped with many protective circuits. The Ethernet systems can be freely cascaded and synchronised in the µs range. Sensors can be connected directly to the measurement systems through screw connectors.

Driverless installation

The installation of the MSX-E systems is fast and easy: After connecting the systems just click on "import web services" in your compiler and enter the IP address of your MSX-E system. Then open the WSDL file, where all functionalities are described. After that you can access all system functionalities without driver installation. To get remote access to the system from a distant PC, each system has a SOAP server. The data transfer is realised with the network protocol HTTP.

Direct administration via PLC

The MSX-E systems can relieve PLCs by taking over fast measurement tasks. To administrate the systems from a PLC, the Modbus TCP Client library is available which enables a direct parameterisation of the systems, the installation of the measurement processes like for example the choice of the acquisition mode, start and stop commands or trigger functionalities and the administration and reading-out of system information.

Easy administration with ConfigTools

ConfigTools is a user-friendly tool with which all MSX-E systems in a network can be scanned and administrated and their status visualised. It is available for 32-bit and 64-bit Windows and Linux operating systems in German, English, French and Chinese.

ConfigTools features

- Automatic scan of all MSX-E systems in a network
- Administration of the MSX-E systems: IP address, firmware version
- System-specific plug-ins: for example sensor calibration and visualisation
- Plug-Ins clickable / selectable via buttons: for example upload / save configuration, firmware update
- Possibility of customised plug-ins
- Changes that are made are logged
- A direct access to the website of the MSX-E systems is possible



HIGHLIGHTS

- Designed for use in the field
- ARM[®]9 processor for intelligent systems
- Stand-alone operating
- Easy configuration

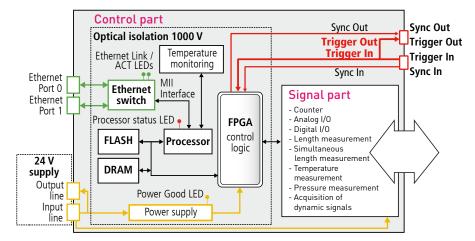




Measurement and control directly in the field

The MSX-E systems are organised in two parts:

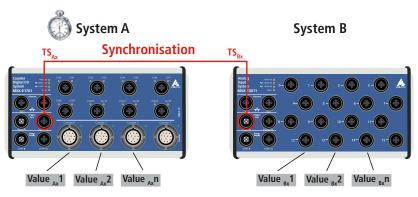
- The **control part** is common to all system types and allows a fast and reliable communication as well as signal processing.
- The signal part features the specific function of each system type: counter, digital I/O, analog I/O, length measurement etc.



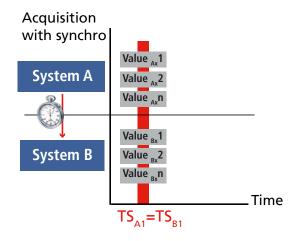
Time stamp

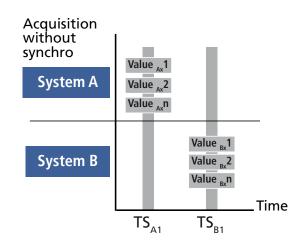
Several MSX-E systems can be synchronised with one another through a synchro connection. This allows to start a synchronous data acquisition, to generate trigger events and to synchronise the time on several MSX-E systems. Furthermore, the systems have a time stamp that logs the point in time at which the data was acquired by the system.

The combination of the **synchronisation** and **time stamp** allows a clear allocation of signals that were captured by several systems.



Without synchro: $TS_{Ax} \neq TS_{Bx}$ With synchro: $TS_{Ax} = TS_{Bx}$



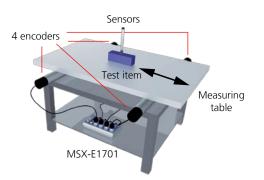




Intelligent Ethernet systems: Special functions

Synchro timer

With the "synchro timer function" you can choose whether a synchro trigger signal shall be generated and if yes define the frequency at which it shall be generated.



During the measurement of a test item the measuring table must move. To guarantee the parallelism of the axis there are two incremental encoders placed at each side of the table and connected to the counter system MSX-E1701. The cycle for the trigger which starts the acquisition is defined in the FPGA of the system. All counters are acquired simultaneously.

Synchro trigger

With the synchro trigger line a MSX-E system that serves as a Master can start a simultaneous acquisition on several other MSX-E systems, generate trigger events and synchronise the time.

Customisation

Hardware combination

Each Ethernet system has its specific functionality and can be freely combined with the other system types. Through synchronisation and cascading the systems work together fast and reliably. Create your own system combination according to your requirements!

Development mode

With the Development mode of the MSX-E systems you can customise your measurement, control and regulation applications to fit your requirements. The programs run directly on the MSX-E systems, which has two advantages: external PCs are relieved and you can process data freely according to your requirements. This helps you to improve the efficiency of your processes and to secure your investments.

Event logic for digital I/O

With the event logic of the digital I/O Ethernet system MSX-E1516 status changes of the inputs and outputs can be detected and logged.

Advantages:

- The generated data set contains the time stamp as well as the event mask, i.e. which input or output has generated the event, and the status of all inputs and outputs.
- The data set can be read out for example in databases for statistical purposes or in operating and machine data logging for process control.
- The "polling" on the inputs is not necessary anymore.
- Status changes are also registered when there is no Ethernet connection. The according data sets (events) can be read out as soon as the Ethernet connection is available again.

See more examples on www.addi-data.com

Function generator with analog outputs

The analog output system MSX-E3511 can generate up to 8 different analog signal curves like for example trapezoid signals, sine curves or sawtooth curves. Thus for example real processes or measuring processes can be simulated and automatic test processes can be realised at test benches.

Software tools

The MSX-E Ethernet systems come with a CD with samples for .NET, C, LabVIEW, etc. and technical descriptions. For applications that run in the development mode we provide you with a Live-DVD including numerous free development tools and a cross compiler for ARM. The Live-DVD is based on the Eclipse development environment and the Ubuntu distribution.

Firmware adaptations

The functionalities of the MSX-E systems can be extended through a change in the firmware. Calculations like for example calculation of the average value, data conversion or digital filter etc. can be implemented.

Our service: We develop your applications

Save time and resources without forgoing the advantages of a customised solution.

Describe us your requirements and we will take care of the programming..



28

info@addi-data.com www.addi-data.com



Measurement, Control, Regulation.

Discover the wide range of applications of the intelligent Ethernet systems!

Process optimisation and monitoring

The intelligent Ethernet systems MSX-E allow the direct connection to MES and ERP systems. The MSX-E systems acquire data directly at the measuring point, convert raw data into physical values and transfer them via the company network into the MES systems on the IT level.

Thanks to their integrated intelligence, the MSX-E systems can, in stand-alone operating mode, transfer measurement values, i.e. "meaningful" data and not raw data, directly to the MES.

Data base T-Level UT LEVEL UT LEVEL Data base Ethernet Production Production facility Production facility

88

Extend the functionality range of PLCs with MSX-E systems

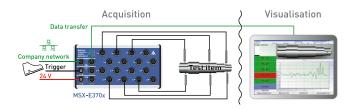
The MSX-E systems can be connected via Ethernet directly to a PLC and thus significantly extend its functionality. The actual measurement task is parameterised and stored on the systems via the website. With the autostart function, the systems load the measurement settings after booting and execute them independently, which makes additional programming unnecessary. The PLC accesses the data and stores it in a data block.

New! The Ethernet systems of the MSX-E series can now be managed directly from a PLC by means of a library. Frames enable the PLC to directly parameterise the MSX-E systems, to read system information and to start or stop measurements.

Data measurement and visualisation

For the visualisation of data acquired via the intelligent Ethernet systems MSX-E, ADDI-DATA offers two solutions: the software procella® by Q-DAS and SPC.kompakt by ProNES. There is no need for programming a connection to the hardware. The values acquired are directly recorded and displayed by procella® or SPC.kompakt. The graphical display allows the operator to distinguish fast and reliably between "good" and "incorrect" parts.

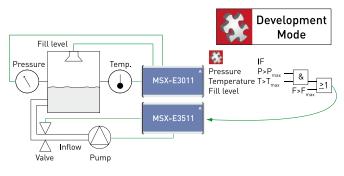
Configuration Start/stop PLC connection with Program Modbus TCP Client Library Measurement values Data Server FC6 TCP/IF MSX-E PLC New! PLC connection without ogramming with function library Modbus TCP Client Configuration Library (option) start/stop Modbus Serve FB Data Server EC₆ Measurement values TCP/IP PLC MSX-E



Stand-alone applications

The MSX-E systems feature a Development Mode which allows to realise and execute applications directly on the MSX-E systems. The MSX-E systems can access other MSX-E systems or any other Ethernet hardware through the Ethernet switch.

The connection via standard Ethernet allows to realise complex distributed measurement and control tasks on site, close to the test item. Such stand-alone applications would be suitable for fill level monitoring and regulation tasks.





Intelligent Ethernet systems



		Ne	ew!	6				New!	Ne	w						
Overview		Digita	l		Multi	functio	n counter	New.	Ana I/	log		Analog input	,	Ana out		Tempera- ture meas-
of the Ethernet		I/0, 24 V		ļ			1	1/	0		input	1	out	րու	urement	
systems	MSX-E1516	MSX-E1516-NPN	MSX-E1516-5V- Input	MSX-E1701	MSX-E1711	MSX-E1721	MSX-E1731	MSX-E1741-1VPP	MSX-E3121	MSX-E3122	MSX-E3011	MSX-E3021	MSX-E3027	MSX-E3511	MSX-E3511-C	MSX-E3211
Intelligent through Arm®9 technology		1		1		/	1	1	-	/		1		-	/	1
Ethernet		1		1		/	1	1	-	/		1		-	/	1
Optical isolation 1000 V		1		1		/	1	1	-	/		1		-	/	1
1 x trigger input / 1 x synchro input / time synchronisation		1		1		/	1	1				1			/	1
Compare logic generates synchro trigger signal				1	•	/	on request	on request	on re	quest	0	n reques	t			on request
Timer function generates synchro trigger signal		1		1		/	1	1		/	<u> </u>	1			/	<i>✓</i>
Cascading		1		1		/	1	1	~	·		1		~	/	- 1
Degree of protection		IP 65		IP 65	IP	65	IP 65	IP 65	IP	65	IP	65	IP 67	IP	65	IP 65
Temperature range from – 40 °C to + 85 °C (Internal temperature of the system)		✓ 		✓ 215 × 110 × 54	215 x 1	10	✓ 215 x 110 x 50	✓ 215 x 110 x 54	✔ 260 x 1		210	✓ × 110 ×	F0		10 E4	✓ 215 x 138 x 50
Dimensions (mm)	21:	5 x 110 x 16 / 5		215 x 110 x 54		6	16	215 X 110 X 54	200 X 1		21:	5 x 110 x	50	154 x 1	10 X 54	215 X 138 X 50
Digital I/O, 24 V / 5 V, status LEDs				10	- 1	0	10		3	2						
Event logic Input filter configuration through software		✓ ✓									<u> </u>					
M12 female connector,									1 x 3	7 nin						
5-pin (for 2 inputs or outputs)		8		8	-	8	8		D-9							
Multifunction counter				1	•	/	1	1								
Incremental counter inputs (A, B, C, D Signals) M23 female connector				4 x 12 pin												
Sin/Cos counter inputs (A, B, C signals), M23 female connector					4 x 12 pin 1 V _{ρρ}	4 x 9 pin 11μΑ _թ		1								
EnDat 2.2-inputs, M12 female connector							4 x 8 pin									
5 V inputs, RS422, 24 V inputs (opt.)				1			1									
Max. input frequency				5 MHz	250	kHz	4.5 MHz clock speed	250 kHz								
Analog input (channels)								3 diff.	6 d	liff.	4	x 4, dif	f.			16/8 diff.
Resolution								24 bit	24	bit		16 bit				24 bit
Туре								V/A	v	'A		V/A				Thermo cou- ples / RTD
Connector								63 x M12 fe- male connector 5 pin	6 x M12 connect			2 female tor 5 pin				8 x M12 female connector 8 pin
Simultaneous acquisition					up to 4 channel		nnels			8 channels						
Throughput								up to 100 kHz	up to 1	00 kHz	up t	to 100	kHz			up to 788 Hz
Input ranges								± 10 V, ± 1 V, ± 100 mV, ± 10 mV, 0-10 V, 0-1 V, 0-100 mV, 0-10 mV	± 10 V, ± 100 ± 10 0-10 V V, 0-10 0-10	0 mV, mV, V, 0-1 00 mV,	± 5 V, ± 10 V, 0-5 V, 0-10 V					
Current inputs (PC-Diff option): 0(4)-20 mA								1		1		1				
Analog output, 16-Bit									4	1				8	3	
M12 female connector									2 x 4	l pin				8 x 5	5 pin	
Output voltage: 0-10 V, ± 10 V									-	/					/	
Current outputs: 0-20 mA										/					/	
Length measurement																
Number of transducers (Half-Bridge, LVDT, Mahr)																
5-pin M18 female connector											<u> </u>					\mid
Simultaneous acquisition											<u> </u>					
Temperature input for Pt100											<u> </u>		r			\mid
Page		34	19.5	38	-	8	42	46	7	0	6	2	66	7	8	82
Software	Curren	t drive	r list on	the web: ww	w.addi-c	lata.con	1									



111 0 0

New! New!

	New!	Nem:						
Pressure measure- ment		listance rement	Acquisition of dynamic signals		Length me	asuremen	t	Protocol interpreter
MSX-E3311	MSX-E3017	 MSX-E3317 	MSX-E3601	MSX-E3711	MSX-E3701	 MSX-E3700 	MSX-E3701-DIO	 MSX-E7511
 ✓	1	1	1		1	 ✓	1	
1	1	1	1	1	1	1	1	1
1	1	1	1	1	1	1	1	1
1	1		1	1	1	1	1	1
on request				1			on request	on request
1				1	1	1	1	
1			1	1	1	1	1	1
IP 65			IP 65	IP 65	IP 65	IP 40	IP 65	IP 65
1			1	1	1	1	1	1
215 x 138 x 50			215 x 110 x 50	215 x 110 x 54	215 x 110 x 50	215 x 110 x 39	260 x 110 x 50	215 x 138 x 50
							32	
							1	
							1 x 37 pin,	
							D-Sub	
	1	1						Serial:
	1	1		1 x 12 pin				RS232, RS422,
	1	1						RS485, 20mA CL
	1	1						
				1				
				5 MHz				
16/8 diff.			8 SE, diff., ICP					
24 bit			24 bit					
Strain gauges			V / A / ICP					
8 x M12 female connector 8 pin			8 x BNC					
8 channels			up to 8 channels					
up to 1 kHz			up to 128 kHz					
			± 5 V, ± 10 V					
			1					
				24 bit	24 bit	24 bit	24 bit	
				8	4/8/16	4/8/16	16 only HB and LVDT	
				8	4/8/16	4/8/16	16	
				1				
				1				ļ
86	54	58	90	94	98	98	104	108

Common specifications for all MSX-E systems

Voltage supply	
Nominal voltage:	24 V
Supply voltage:	18-30 V
Optical isolation:	1000 V
Reverse voltage protection	n: 1 A max. (except MSX-E3711)
Connectors	
24 VDC input	1 x 5-pin M12 male connector
	(except MSX-E3700)
24 VDC output	1 x 5-pin M12 female connector
	(except MSX-E3700)
Ethernet	
Interface:	Ethernet acc. to IEEE802.3 specification
Number of ports:	2
Cable length:	150 m max. at CAT5E UTP
Bandwidth:	10 Mbps auto-negotiation
	100 Mbps auto-negotiation
Protocol:	10Base-T IEEE802.3 compliant
	100Base-TX IEEE802.3 compliant
Optical isolation:	1000 V
MAC address:	00:0F:6C:##:##:##, unique for each device
Connectors	
Ethernet:	2 x 4-pin female connector, D-coded M12
	for Port 0 and Port 1 (except MSX-E3700)
Trigger	
Number of inputs:	1 trigger input
Number of outputs:	1 trigger output
Filters/protective circuit:	Low-pass/transorb diode
Optical isolation:	1000 V
Nominal voltage:	24 V external
Input voltage:	0 to 30 V
Input current:	11 mA at 24 VDC, typical
Input frequency (max.):	2 MHz at 24 V
Connectors, common	1 ··· F ·······························
Connectors, common Trigger input:	1 x 5-pin M12 male connector
Connectors, common	(except MSX-E3700)
Connectors, common	

Synchro

-,		
Number of inputs:	1	
Number of outputs:	1	
Max. cable length:	20 m	
Optical isolation:	1000 V	
Signal type:	RS422	
Connectors, commo		
Synchro input:	1 x 5-pin M12 male connector	
	(except MSX-E3700)	
Synchro output:	1 x 5-pin M12 female connector	
	(except MSX-E3700)	

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.



Database connection

DatabaseConnect is an easy-to-use database interface software which does not require any programming skills. DatabaseConnect stores measurement data which has been acquired through MSX-E Ethernet systems directly into databases via standard Ethernet. System requirements and other important product information see the datasheet on page 112.



Accesso	ries for the Ethernet systems	Digital I/O, 24 V	Mult	ifunction cou	Inter
		MSX-E1516 MSX-E1516-NPN MSX-E1516-5V- Input	MSX-E1711 MSX-E1721 MSX-E1701	New! MSX-E1731	New! MSX-E1741
-	ature range from -25 °C to +80 °C; Bent cables and special length on re	quest			
	ded cable, M12 5-pin female connector/open end, IP65 -21 (3 m), CMX-22 (5 m), CMX-23 (10 m), CMX-29 (length on request)	1	1	1	1
Voltage supply – Ca	scading: Shielded cable, M12 5-pin female connector/male connector, IP65 -30 (1 m), CMX-31 (3 m), CMX-32 (5 m), CMX-39_0,3 (0,3 m), CMX-39 (length on request)	1	1	1	1
	elded cable, M12 5-pin female connector/open end, IP65 -41 (3 m), CMX-42 (5 m), CMX-43 (10 m), CMX-49 (length on request)	1	1	1	1
Trigger/Synchro – C	ascading: Shielded cable, M12 5-pin female connector/male connector, IP65 -50 (1 m), CMX-51 (3 m), CMX-52 (5 m), CMX-59_0,3 (0,3 m), CMX-59 (length on request)	1	1	1	1
	I, M12 D-coded male connector/RJ45 connector (1 (5 m), CMX-62 (10 m), CMX-69 (length on request)	~	1	1	1
Ethernet – Cascadin	g: CAT5E-Kabel, 2 x M12 D-coded male connector (0 (2 m), CMX-71 (5 m), CMX-72 (10 m), CMX-79_0,3 (0,3 m), CMX-79 (length on request)	1	1	1	1
Connecting periphe CMX-81 (3 m), CMX-8	ral equipment: Shielded cable, M12 8-pin male connector/open end, IP65 (9 (length on request)	1	1	1	_
Connecting periphe	ral equipment: Shielded cable, M12 5-pin male connector/open end, IP65				
CMX-9x (length on re					1
Connector	M12 E nin connector for connecting ones and rables				
SC-M12:	M12 5-pin connector for connecting open end cables		1	1	1
SC-M12-8:	M12 8-pin connector for connecting open end cables	1			
SC-M12-ABGW:	M12 5-pin 90° bent connector for connecting open end cables		<i>·</i>	<i>✓</i>	<i>·</i>
	M12 5-pin 90° bent female connector for connecting open end cables		1	1	1
SC-M12-8-ABGW: SC-M12-Y-M12:	M12 8-pin 90° bent connector for connecting open end cables				
SC-M12-1-M12:	5-pin Y-splitter cable with M12 connector to 2 x M12 female connectors M23 12-pin connector for the direct connection of rotary and linear encoders, digital transducers		<i>·</i>	<i>\</i>	
	r binders for voltage supply: 3-pin binder, 5.08 mm grid]	•	•	
	SMX-10 1-row screw connector, included in the delivery content	-	-	-	-
	SMX-11 2-row screw connector	-	-	-	-
*	SMX-12 2-row spring-cage connector	-	-	-	-
Screw connecto	r binders for trigger/synchro				
	SMX-20 3-pin binders, 5.08 mm grid, included in the delivery content	-	-	-	-
Options / Moun	ting	<u> </u>			
	MX-Clip 2 clips for DIN-rail mounting or for direct mounting on units.	1	1	1	-
	MX-Rail Assembly equipment for DIN-rail mounting. Please specify when ordering!	1	1	1	1
	MX-Screw Assembly equipment for direct mounting on machines	1	1	1	1
Options / Prote	ction caps				
	PCMX-10: 5 x protection caps for M12 connector (4 x female, 1 x male)	1	1	1	1
•	PCMX-11: 10 x protection caps for M18 connector	-	-	-	-
Q,	PCMX-12: 1 protection cap for M23 connector	-	\$		\$
	PCMX-13: 10 x protection caps for M12 connector	1	1	1	1





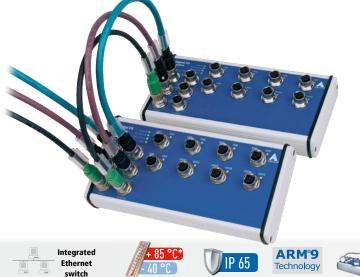


Analog Analog I/O input				halog Analog Analog For I/O input output me				Tempera- ture meas- urement	Pressure meas- urement	Acquisition of dynamic signals	Le	ent	Protocol inter- preter		
MSX-E3121	<mark>Ne</mark> w! MSX-E3122	MSX-E3011 MSX-E3021	MSX-E3027	MSX-E3511	<mark>New!</mark> MSX-E3511-C	MSX-E3017	MSX-E3317	MSX-E3211	MSX-E3311	MSX-E3601	MSX-E3711	MSX-E3701	MSX-E3700	MSX-E3701-DIO	MSX-E7511
1	1	1	1	1	1	1	1	1	1	1	1	1	_	1	1
1	1	1	1	1	1	1	1	1	1	1	1	1	-	1	1
1	1	1	1	1	1	1	1	1	1	1	1	1	-	1	1
1	1	1	1	1	1	1	1	1	1	1	1	1	-	1	1
1	1	1	~	1	1	1	1	1	1	1	1	1	-	1	1
1	1	1	1	1	1	1	1	1	1	1	1	1	-	1	1
1	1	~	1	1	1	1	1	-	-	-	-	-	-	1	-
1	1					1	1	1	1						
1	1	 ✓ 	1	1	1	1	1	1	 ✓ 	1	1	1		1	1
						1	1	1	 ✓ 						
\ \	✓ ✓	✓ ✓	✓ ✓	\ \	\ \	1 1	\ \	\ \	\ \	1	1	1		1	✓ ✓
					-	1	· ·	1	· ·						
1	1	1	1	1	1	1	1								
				I		1	1		[]		1				
-	-	-	-	-	-	-	-	-	-	-	-	-	1	-	-
-	-	-	-	-	-	-	-	-	-	-	-	-	1	-	-
-	-	-	-	-	-	-	-	-	-	-	-	-	1	-	-
															1
-	-	-	-	-	-	-	-	-	-	-	-	-	1	-	-
_	-	1	-	1	1	_	_	_	_	1	1	1	1	1	-
1	1	<i>、</i>	<i>✓</i>	1	1	1	1	1	1	<i>√</i>	1	1	1	1	1
	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
1	~	~	1	~	1	1	1	1	1	1	1	1	1	1	1
-	-	-	-	-	-	-	-	-	-	-	1	1	1	1	-
-	-	-	-	-	-	1	1	-	-	-	1	-	-	-	-
1	1	1	1	1	1	1	1	1	1	-	-	-	-	-	-



Ethernet digital I/O system 16 digital I/O, 24 V, with event logic







Timer function for

MSX-E1516 / MSX-E1516-NPN

Event logic for the inputs and outputs

16 digital I/O, 24 V, status LEDs Configurable inputs and outputs

24 V digital trigger input

M12 connectors

synchro trigger signal

Operating temperature







on reauest



DatabaseConnect see page 112



* Preliminary

Product information

Features

- 24 V digital trigger input
- ARM[®]9 32-bit processor
- 64 MB onboard SDRAM for storing data
- Robust standardized metal housing
- Power Save Mode: Reduced power consumption when no acquisition runs

Safety features

- Status LEDs for fast error diagnostics
- Optical isolation 1000 V
- Overtemperature and reverse voltage protection
- Internal temperature monitoring
- Filters on all inputs (software-programmable)
- Short-circuit protection
- Overvoltage protection 30 V
- Electronic fuse

Digital I/O

- 8 x 2 digital lines, 24 V, which can be parameterised as
- pairs of inputs or outputs
- Shutdown logic
- Watchdog for resetting the outputs to "0"
- At Power-On the outputs are set to "0" Electronic fuse
- Dual LED for each 24 V digital I/O with direction indication

Interfaces

- Fast 24 V trigger input
- Ethernet switch with 2 ports
- Synchronisation/trigger In/Out
- Line in for 24 V supply and cascading

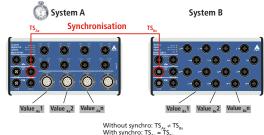
Communication interfaces

- Web server (configuration and monitoring)
- Command server SOAP for transferring commands
- Data server (TCP/IP or UDP socket) for sending
- acquisition data Event server (TCP/IP socket) for sending system events •
- (Diagnostics such as temperature, short-circuits ...)
- Command server Modbus TCP and Modbus (UDP) for sending commands

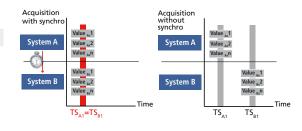
Synchronisation/time stamp

Time stamp

Several MSX-E systems can be synchronised with one another in the µs range through a synchro connection. This allows to start a synchronous data acquisition, to generate trigger events and to synchronise the time on several MSX-E systems. Furthermore, the systems have a time stamp that logs the point in time at which the data was acquired by the system.



The combination of synchronisation and time stamp (TS) allows the clear allocation of signals that were captured by several systems.



NOTE

NPN

While most sensors when activated will output a 24V signal (PNP sensors), a NPN sensor when activated switches to ground.

The swichting to ground of NPN sensors can be read by the MSX-E1516-NPN system.



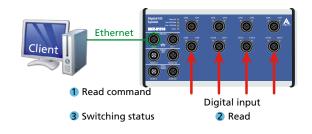
info@addi-data.com www.addi-data.com



Acquisition modes – There are 2 different possibilities for reading the digital inputs.

1. Asynchronous acquisition

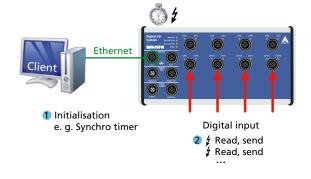
With the asynchronous acquisition, the digital inputs can be read out via SOAP or Modbus function. For each function call, the values of one channel are transmitted.



2. Synchronous acquisition

With the synchronous acquisition, the inputs are first initialised and then the acquisition is parameterised. The acquisition runs automatically depending from a trigger source.

Either the 24V trigger input or a synchro trigger can be used as trigger source.



Synchro latch

A periodic acquisition of the digital inputs is possible using the synchro timer (synchro latch). Several MSX-E systems (of same or different types) can be combined through synchro trigger. With the synchronous acquisition, as soon as there are measurement values available, they are sent to the clients via socket connection.

Event logic

The event logic of the MSX-E1516 can be used to detect a change of edge on the digital inputs (e.g. to show that 1 unit has been produced, or that the machine runs or stands still).

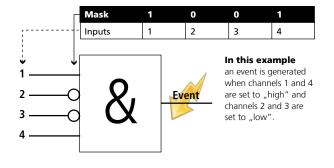
This information (time stamp + event mask) is stored on the data server of the MSX-E1516 and can be read through a socket connection. The information can be read either through an application written for this purpose or by using the DatabaseConnect software. DatabaseConnect

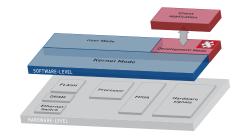
Onboard programming / stand-alone operation

writes the information in a database and can then evaluate it.

Development mode

With the Development mode of the MSX-E systems you can customise your measurement, control and regulation applications to fit your requirements. The programs run directly on the MSX-E systems, which has two advantages: external PCs are relieved and you can process data freely according to your requirements. This helps you to improve the efficiency of your processes and to secure your investments.





Safety

Watchdog

The MSX-E1516 Ethernet system has a 16-bit watchdog which can be programmed in 3 time units (µs, ms, s). The watchdog is used for automatically resetting the digital outputs to 0 V after a defined time in order e. g. to switch off actuators if an error has occurred.

* Preliminary product information



The **ConfigTools** program allows an easy administration of the MSX-E systems. These are automatically detected in the network. **ConfigTools** consists of common and specific functions.

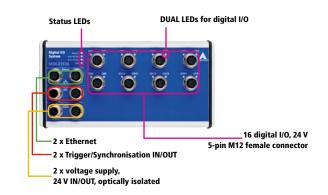
In addition, with **ConfigTools**, the complete configuration of a MSX-E system can be saved and transferred to another system of the same type (clone function).

ConfigTools is included in the delivery.

ConfigTools functions for MSX-E1516 / MSX-E1516-NPN:

- Change of IP address
- Display of web interface
- Firmware update
- Save/load system configuration
- Save/load channel configuration

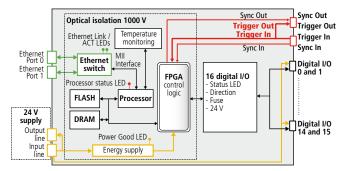
Features



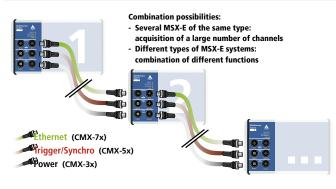
Simplified block diagram

Very easy use through the "ConfigTools" program; The MSX-E system is automatically detected in the network.

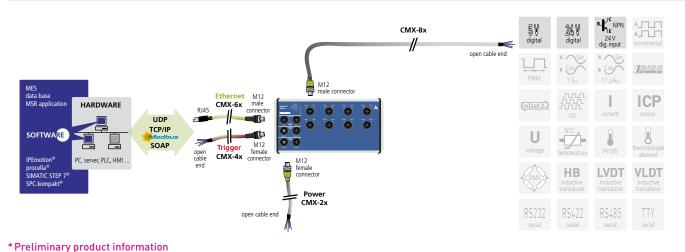




Cascading



ADDI-DATA connection technology





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Specifications*

Digital inputs	
Number of inputs:	16, 2 per M12 female connector Common ground acc. to IEC 1131-2
Overvoltage protection:	30 V
Optical isolation:	1000 V through opto-couplers
Nominal voltage:	24 VDC
Input voltage:	0 to 30 V
Input impedance:	> 1 MΩ
Logic input levels:	UH (max) 30 V typ. UH (min) 18 V typ. UL (max) 16 V typ.
	UL (min) 0 V typ.

Digital outputs

Number of outputs:	16, 2 per M12 female connector
Optical isolation:	1000 V through opto-couplers
Output type:	High-side, load to ground acc. to IEC 1131-2
Nominal voltage:	24 V
Voltage supply:	18 V-30 V
Current (max.):	1.85 A typ. for 8 channels through PTC at 20°C
Output current per channel:	500 mA max.
	Short-circuit current per output 1.7 A max
	Shut-down logic at 24 V, $R_{load} = 10 \text{ m}\Omega$
RDS ON Resistance:	280 mΩ max.
Switch-on time:	100 μ s max RL = 48 Ω of 80 % V _{out}
Switch-off time	150 μ s max RL = 48 Ω of 10 % V _{out}
Overtemperature (shutdown):	135°Cmax. (output driver)
Temperature hysteresis:	15°C typ. (output driver)
Diagnostics:	Common diagnostics bit for all 16 channels at
	overtemperature of one channel

Voltage supply, Ethernet, Trigger, Synchro

The specifications for the voltage supply, Ethernet, Trigger, Synchronisation and Electromagnetic Compatibility apply to all MSX-E systems. See page 31.

System features	
Interface:	Ethernet acc. to specification IEEE802.3
Dimensions:	215 mm x 110 mm x 50 mm
Weight:	900 g
Degree of protection:	IP 65
Current consumption at 24 V:	160 mA
Operating temperature:	-40 °C to +85 °C
Connectors for sensors	
For digital I/O:	8 x 5-pin M12 female connector

Ordering information

MSX-E1516 / MSX-E1516-NPN

Ethernet digital I/O system, 16 digital I/O, 24 V, with event logic. Incl. technical description, software drivers and ConfigTools.

Versions MSX-E1516:

16 digital I/O, 24 V MSX-E1516-NPN: 16 digital inputs, 24 V (NPN)

Connection cables

Voltage supply

CMX-2x: Shielded cable, M12 5-pin female connector/open end, IP 65 CMX-3x: For cascading, shielded cable, M12 5-pin female connector/male connector IP 65

Trigger/Synchro

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CMX-4x: Shielded cable, M12 5-pin female connector/open end, IP 65 CMX-5x: For cascading, shielded cable, M12 5-pin female connector/male connector IP 65

Ethernet

CMX-6x: CAT5E cable, M12 D-coded male connector/RJ45 connector CMX-7x: For cascading, CAT5E cable, 2 x M12 D-coded male connector **Connection to peripherals**

CMX-8x: Shielded cable, M12 5-pin male connector/open end, IP 65 Options

S7 Modbus TCP Client Library for S7: Easy use of the Ethernet systems MSX-E with PLCs

MSX-E 5V-Trigger: Level change of the trigger inputs and outputs to 5 V MX-Clip, MX-Rail (please specify when ordering!), MX-Screw, PCMX-1x

* Preliminary product information



Ethernet multifunction counter system 4 counter inputs (incremental, sin/cos), 16 digital I/O, 24 V



MSX-E1701 / MSX-E1711 / MSX-E1721

4 incremental counter inputs or 8 PWM outputs (MSX-E1701)

4 sin/cos counter inputs 1 V_{DD} (MSX-E1711),

11 µA_{nn} (MSX-E1721) with A, B, C (index) signals

16 digital I/O, 24 V, status LEDs

Configurable inputs and outputs

24 V digital trigger input

M12 and M23 connectors



Operating temperature









DatabaseConnect see page 112



Features

- 24 V digital trigger input
- ARM[®]9 32-bit processor
- 64 MB onboard SDRAM for storing data
- Robust standardized metal housing
- Power Save Mode: Reduced power consumption when no acquisition runs

Safety features

- Status LEDs for fast error diagnostics
- Optical isolation Input filters

Counter

- 4 x 32-bit incremental counter inputs (MSX-E1701), max. 5 MHz, or 8 x PWM outputs, can be configured through firmware
- 4 x 32-bit sin/cos counter inputs with 1V_{pp} (MSX-E1711) or 11 μA_{pp} (MSX-E1721), 250 kHz
- Voltage supply of the sensors through M23 female connector (24 V or 5 V)
- Single, double, quadruple edge analysis (MSX-E1701)
- Compare logic
- Status LEDs for incremental counter inputs

Digital I/O

- 8 x 2 digital lines, 24 V, which can be parameterised as pairs of inputs or outputs
- Shutdown logic
- Watchdog for resetting the outputs to "0"
- At Power-On the outputs are set to "0"
- Electronic fuse •
- Dual LED for each 24 V digital I/O with direction indication

Interfaces

- Fast 24 V trigger input
- Ethernet switch with 2 ports
- Synchronisation/trigger In/Out
- Line in for 24 V supply and cascading

Communication interfaces

- Web server (configuration and monitoring)
- Command server SOAP for transferring commands
- Data server (TCP/IP or UDP socket) for sending acquisition data
- Event server (TCP/IP socket) for sending system events (Diagnostics such as temperature, short-circuits ...)

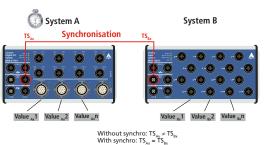


Command server Modbus TCP and Modbus (UDP) for sending commands

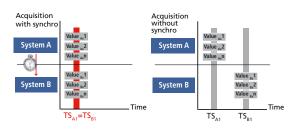
Synchronisation/time stamp

Time stamp

Several MSX-E systems can be synchronised with one another in the us range through a synchro connection. This allows to start a synchronous data acquisition, to generate trigger events and to synchronise the time on several MSX-E systems. Furthermore, the systems have a time stamp that logs the point in time at which the data was acquired by the system.



The combination of synchronisation and time stamp (TS) allows the clear allocation of signals that were captured by several systems.



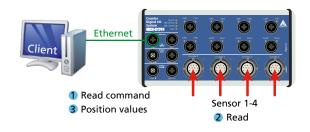




Acquisition modes – There are 2 different possibilities for reading the counter inputs.

1. Asynchronous acquisition

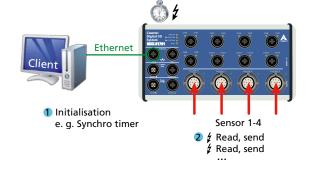
With the asynchronous acquisition, the counter inputs can be read out via SOAP or Modbus function. For each function call, the values of one channel are transmitted.



2. Synchronous acquisition

With the synchronous acquisition, the inputs are first initialised and then the acquisition is parameterised. The acquisition runs automatically depending from a trigger source.

Either the 24V trigger input or a synchro trigger can be used as trigger source.



Synchro latch

A periodic acquisition of the counter inputs is possible using the synchro timer (synchro latch). Several MSX-E systems (of same or different types) can be combined through synchro trigger. With the synchronous acquisition, as soon as there are measurement values available, they are sent to the clients via socket connection.

Compare logic

With the compare logic, a synchro-trigger signal can be generated in order to latch the counter value as soon as the counter value is equal to the compare value.

With the additional "Modulo-Mode" (Modulo Compare), a trigger can also be generated at the n value of the compare value.

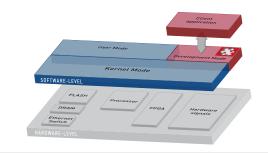
Thus it is possible, e. g. when using an encoder with 3,600 steps / revolution to obtain each degree of a measurement value (Modulo Compare = 10). The thus generated synchro-trigger can also be used for data acquisition on further MSX-E systems.

Onboard programming / stand-alone operation

Development mode

With the Development mode of the MSX-E systems you can customise your measurement, control and regulation applications to fit your requirements. The programs run directly on the MSX-E systems, which has two advantages: external PCs are relieved and you can process data freely according to your requirements. This helps you to improve the efficiency of your processes and to secure your investments.





Safety

Watchdog

39

The MSX-E17x1 Ethernet system has a 16-bit watchdog which can be programmed in 3 time units (μ s, ms, s). The watchdog is used for automatically resetting the digital outputs to 0 V after a defined time in order e.g. to switch off actuators if an error has occurred.



The **ConfigTools** program allows an easy administration of the MSX-E systems. These are automatically detected in the network. **ConfigTools** consists of common and specific functions.

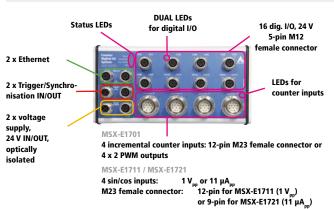
In addition, with **ConfigTools**, the complete configuration of a MSX-E system can be saved and transferred to another system of the same type (clone function).

ConfigTools is included in the delivery.

ConfigTools functions for MSX-E1701 / MSX-E1711 / MSX-E1721:

- Change of IP address
- Display of web interface
- Firmware update
- Save/load system configuration
- Save/load channel configuration

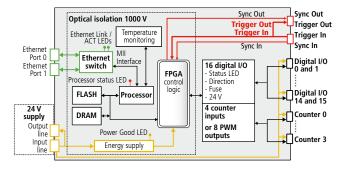
Features



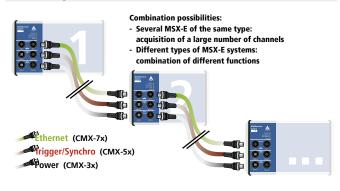
Simplified block diagram



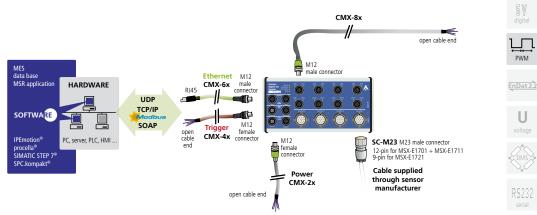




Cascading



ADDI-DATA connection technology





B-K

24 V

^___ ¤____





Incremental counter	er inputs (MSX-E1701)
Number of inputs:	4 x incremental counters each with A, B, C and D signals
5 V inputs (MSX-E1701 versi	
Differential inputs:	Complies with the EIA standards RS422A
Input type:	Differential or TTL (with reference voltage)
Common mode range:	+12/-7V
Input sensitivity:	± 200 mV
Input hysteresis:	50 mV typ.
Input impedance:	$12 \text{ k}\Omega \text{ min.}$
Max. input frequency:	5 MHz
"Open Circuit Fail Safe Receiver	
ESD protection:	Up to ±15 kV
24 V inputs (MSX-E1701-24 v	
	For 24 V encoders. Only 24 V signals can be connected.
Nominal voltage:	24 VDC
Max. input frequency:	1 MHz at nominal voltage
Input impedance:	>1 MΩ
Logic input levels:	UH (max): 30 V typ.
5	UH (min): 18 V typ.
	UL (max): 16 V typ.
	UL (min): 0 V typ.
Voltage supply:	Incremental encoder selectable, 5 V or 24 V, max. 500 mA
PWM outputs (MSX	-E1701)
Number of outputs:	8
Differential I/O:	Complies with the EIA standards RS422A
Output type:	Differential
Common mode range:	+12 / -7 V
Input sensitivity:	± 200 mV
Input hysteresis:	50 mV typ.
Input impedance:	12 k Ω min.
Time base:	250 ns, 1 µs, 1 ms, 1 s
Min. pulse duration:	250 ns
Max. output rate:	n: number of pulses (Duty Cycle), $n \ge 2$
	$f = \frac{1}{n \cdot \text{time base}}$
Examples (time base = 250 ns)	
Duty Cycle 50%	Duty Cycle 66%
1 high pulse, 1 low p	ulse, n=2 2 high pulses, 1 low pulse, n=3
	иц _а f _ 1 _ 1 33 МНа
	1Hz $f = \frac{1}{3 \cdot 250 \text{ ns}} = 1.33 \text{ MHz}$
$f = \frac{1}{2 \cdot 250 \text{ ns}} = 2 \text{ N}$ Voltage supply:	$\frac{112}{5 \text{ V or } 24 \text{ V, max. 500 mA}} = 1.55 \text{ While}$
$f = \frac{1}{2 \cdot 250 \text{ ns}} = 2 \text{ N}$ Voltage supply: Sin/cos counter inp	$\frac{1}{5 \text{ V or } 24 \text{ V, max. 500 mA}}$ 5 V or 24 V, max. 500 mA 5 uts (MSX-E1711, MSX-E1721)
$f = \frac{1}{2 \cdot 250 \text{ ns}} = 2 \text{ N}$ Voltage supply: Sin/cos counter inp Number of inputs:	5 V or 24 V, max. 500 mA
$f = \frac{1}{2 \cdot 250 \text{ ns}} = 2 \text{ N}$ Voltage supply: Sin/cos counter ing Number of inputs: Resolution:	5 V or 24 V, max. 500 mA Duts (MSX-E1711, MSX-E1721) 4 x sin/cos counter inputs each with A, B, C and D signals 32-bit
$f = \frac{1}{2 \cdot 250 \text{ ns}} = 2 \text{ N}$ Voltage supply: Sin/cos counter inp Number of inputs:	5 V or 24 V, max. 500 mA Duts (MSX-E1711, MSX-E1721) 4 x sin/cos counter inputs each with A, B, C and D signals 32-bit
$f = \frac{1}{2 \cdot 250 \text{ ns}} = 2 \text{ N}$ Voltage supply: Sin/cos counter inp Number of inputs: Resolution:	5 V or 24 V, max. 500 mA Duts (MSX-E1711, MSX-E1721) 4 x sin/cos counter inputs each with A, B, C and D signals

Interpolation factor:	Up to 8192
Max. input frequency:	max. 250 kHz (at min. interpolation), on request
ESD protection:	2 kV
Digital inputs	
Number of inputs:	max. 16, 2 per M12 female connector, common ground acc. to IEC 1131-2
Overvoltage protection:	30 V
Optical isolation:	1000 V through opto-couplers
Nominal voltage:	24 VDC
Input voltage:	0 to 30 V
Input impedance:	>1 MΩ
Logic input levels:	UH (max): 30 V typ. UH (min):18 V typ. UL (max):16 V typ. UL (min):0 V typ.
Digital outputs	
Number of outputs:	max. 16, 2 per M12 female connector
Optical isolation:	1000 V through opto-couplers
Output type:	High-side, load to ground acc. to IEC 1131-2
Nominal voltage:	24 V
Voltage supply:	18 V-30 V
Current (max.):	1.85 A typ. for 8 channels through PTC
Output current / output:	500 mA max.
Short-circuit current / output:	1.7 A max., shut-down logic at 24 V, R_{load} = 10 m Ω
RDS ON resistance:	280 mΩ max.
Switch-on time:	100 μ s, max. RL = 48 Ω from 80 % V _{out}
Switch-off time:	150 μs, max. RL=48 Ω from 10 % V _{out}
Overtemperature (shutdown):	135°C max. (output driver)
Temperature hysteresis:	15°C typ. (output driver)
Diagnostics:	Common diagnostics bit for all 16 channels at overtemperature of one channel
Voltage supply, Eth	ernet, Trigger, Synchro
	e supply, Ethernet, Trigger, Synchronisation and pply to all MSX-E systems. See page 31.

System features

Interface:	Ethernet acc. to specification IEEE802.3
Dimensions (mm):	215 x 110 x 54
Weight:	900 g
Degree of protection:	IP 65
Current consumption at 24 V:	150 mA without load
Operating temperature:	-40 °C to +85 °C
Connectors for sensors	
Digital I/O:	8 x 5-pin M12 female connector
Incremental counter inputs:	4 x 12-pin M23 female connector
Sin/cos counter input 1 V _{ss} :	4 x 12-pin M23 female connector
Sin/cos counter input 11 µA _{ss} :	4 x 9-pin M23 female connector

Ordering information

MSX-E1701 / MSX-E1711 / MSX-E1721

Ethernet multifunction counter system, 4 counter inputs (incremental, sin/cos), 16 digital I/O, 24 V. Incl. technical description, software drivers and ConfigTools.

Versions

MSX-E1701:	5 V RS422 incremental counter inputs
MSX-E1701-24V:	24 V incremental counter inputs
MSX-E1711:	Sin/cos inputs, 1 V _{pp}
MSX-E1721:	Sin/cos inputs, 11 μA_{pp}
Connection cables	
Voltago cumply	

Voltage supply

CMX-2x: Shielded cable, M12 5-pin female connector/open end, IP 65
 CMX-3x: For cascading, shielded cable, M12 5-pin female connector/male connector IP 65

Trigger/Synchro

CMX-4x: Shielded cable, M12 5-pin female connector/open end, IP 65 **CMX-5x:** For cascading, shielded cable, M12 5-pin

female connector/male connector IP 65

Ethernet

CMX-6x: CAT5E cable, M12 D-coded male connector/RJ45 connector **CMX-7x:** For cascading: CAT5E cable, 2 x M12 D-coded male connector

Connection to peripherals

CMX-8x: Shielded cable, M12 5-pin male connector/open end, IP 65

Options

S7 Modbus TCP Client Library for S7:

Easy use of the Ethernet systems MSX-E with PLCs **MSX-E 5V-Trigger:** Level change of the trigger inputs and outputs to 5 V **MX-Clip, MX-Rail** (Please specify when ordering!), **MX-Screw, PCMX-1x**





MSX-E1731

4 EnDat 2.2 inputs

16 digital I/O, 24 V, with status LEDs

24 V digital trigger input

M12 connectors











DatabaseConnect on request



www.addi-data.com

Features

- 24 V digital trigger input
- ARM[®]9 32-bit processor
- 64 MB onboard SDRAM for storing data
- Robust standardized metal housing
- Power Save Mode: Reduced power consumption when no acquisition runs

Safety features

- Status LEDs for fast error diagnostics
- Optical isolation 1000 V
- Input filters

Sensor inputs

- 8-pin M12 female connectors
- 4 x EnDat counter inputs for the acquisition of EnDat encoders
- Max. clock frequency 4.5 MHz
- Voltage supply of the EnDat encoders via M12 female ٠ connectors: 5 V ±10%
- Output of the values as raw value or position value (mm or °)
- Communication LED for each EnDat input

Interfaces

- Fast 24 V trigger input
- Ethernet switch with 2 ports
- Synchronisation/trigger In/Out
- Line in for 24 V supply and cascading

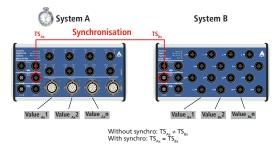
Communication interfaces

- Web server (configuration and monitoring)
- Command server SOAP for transferring commands • Data server (TCP/IP or UDP socket) for sending
- acquisition data Event server (TCP/IP socket) for sending system events
- (Diagnostics such as temperature, short-circuits ...)
- Command server Modbus TCP and Modbus (UDP) for sending commands

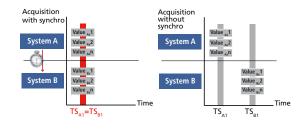
Synchronisation/time stamp

Time stamp

Several MSX-E systems can be synchronised with one another in the µs range through a synchro connection. This allows to start a synchronous data acquisition, to generate trigger events and to synchronise the time on several MSX-E systems. Furthermore, the systems have a time stamp that logs the point in time at which the data was acquired by the system.



The combination of synchronisation and time stamp (TS) allows the clear allocation of signals that were captured by several systems.





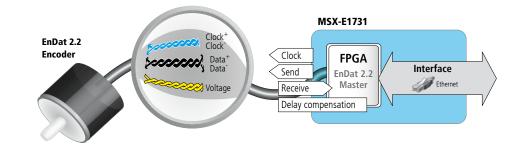
* Preliminary product information



EnDat

EnDat is a bidirectional synchronous-serial interface for position measurement devices. This interface allows to read out absolute position values and parameters, to write status and initialisation registers and to transfer additional information about the position value. In addition, ADDI-DATA EnDat 2.2 solutions support the evaluation of diagnostic values and access to the OEM memory. Data is transferred serially.

- Fast data transfer
- Signal delay time compensation
- High contour accuracy
- High transmission safety
- No need for additional sensors: Evaluation (temperature, limit switch, etc.)
- Serial transmission: only 4 lines necessary (EnDat 2.2)
- Single-line wiring (M12, 8-pin)
- Automatic parameterisation through electronic type plate



Clien

Client

Initialisation

e. g. Synchro timer

Acquisition modes

There are two different acquisition modes for EnDat sensors:

Asynchronous acquisition

With the asynchronous acquisition, the EnDat sensors can be read out after initialisation via SOAP or Modbus function.

For each function call, one position value is transmitted.

EnDat 2.2 also allows to read out additional sensor-specific values (e. g. temperature,...)

Synchronous acquisition

With the synchronous acquisition, at first the sensors are initialised and then the acquisition is parameterised. The acquisition runs automatically in relation to a trigger source.

Either the 24 V trigger input or a Synchro timer can be used as a trigger source.

When using the Synchro timer, a periodical acquisition of the EnDat inputs is also possible.

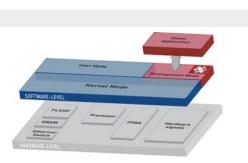
With the synchronous acquisition, it is possible to acquire all 4 sensor inputs of the MSX-E1731 simultaneously.

It is also possible to combine several MSX-E systems (even of different types) through the Synchro trigger. In synchronous acquisition mode, measurement data is sent to the clients as soon as it is available via a socket connection.

Onboard programming / stand-alone operation

Development mode

With the Development mode of the MSX-E systems you can customise your measurement, control and regulation applications to fit your requirements. The programs run directly on the MSX-E systems, which has two advantages: external PCs are relieved and you can process data freely according to your requirements. This helps you to improve the efficiency of your processes and to secure your investments.



* Preliminary product information

0 0

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<u>o</u> e

Ethernet

Read command

3 Position values

Ethernet

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0

Sensor 1-4

2 Read

ź

0

0

Sensor 1-4

2 Read, send

Read, send

6



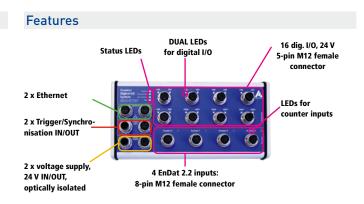
The ConfigTools program allows an easy administration of the MSX-E systems. These are automatically detected in the network. ConfigTools consists of common and specific functions.

In addition, with ConfigTools, the complete configuration of a MSX-E system can be saved and transferred to another system of the same type (clone function).

ConfigTools is included in the delivery.

ConfigTools functions for MSX-E1731:

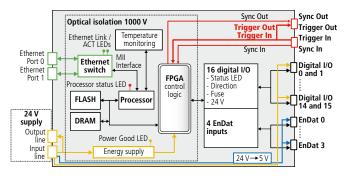
- Change of IP address
- Display of web interface
- Firmware update ٠
- Save/load system configuration ٠
- Save/load channel configuration ٠



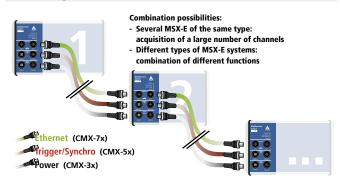
Simplified block diagram

Very easy use through the "ConfigTools" program; The MSX-E system is automatically detected in the network.





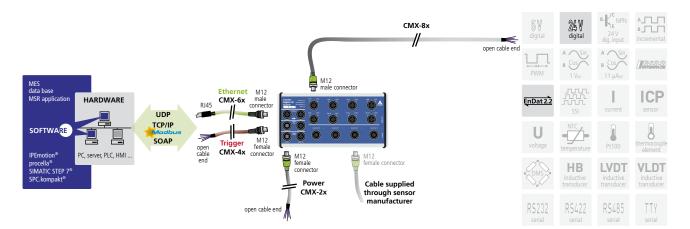
Cascading



ADDI-DATA

SPIRIT OF EXCELLENCE

ADDI-DATA connection technology



* Preliminary product information



Specifications*

Counter inputs

Input type:	EnDat 2.2
Differential inputs:	Complies with the EIA standards RS422A
Input type:	Differential
Common mode range:	+12 / -7 V
Input sensitivity:	± 200 mV
Input hysteresis:	50 mV typ.
Input impedance:	12 kΩ min.
Max. input frequency:	5 MHz
ESD protection:	Up to ±15 kV
Clock frequencies:	4500 kHz
	2500 kHz
	1500 kHz
	900 kHz
	500 kHz

30 V

24 VDC

>1 MΩ

from 0 to 30 V

UH (max):30 V typ.

UH (min): 18 V typ. UL (max): 16 V typ. UL (min): 0 V typ.

max. 16, 2 per M12 female connector, common ground acc. to IEC 1131-2

1000 V through opto-couplers

Short-circuit current / output:	1.7 A max. Shut-down logic at 24 V, Rload=10 mΩ
RDS ON resistance:	280 mΩ max.
Switch-on time:	100 µs
	max RL=48 Ω from 80 % Vout
Switch-off time:	150 µs
	max RL=48 Ω from 10 % Vout
Overtemperature (shutdown):	135°C max. (output driver)
Temperature hysteresis:	15°C typ. (output driver)
Diagnostics:	Common diagnostics bits for all 16 channels
	at overtemperature

Watchdog

Number:	1
Resolution:	16-bit
Time base:	μs, ms, s (programmable)
Time value range:	1 to 65535

Voltage supply, Ethernet, Trigger, Synchro

The specifications for the voltage supply, Ethernet, Trigger, Synchronisation and Electromagnetic Compatibility apply to all MSX-E systems. See page 31.

System features

Interface:	Ethernet acc. to specification IEEE802.3
Dimensions (mm):	215 x 110 x 54 mm
Weight:	approx. 900 g
Degree of protection:	IP 65
Current consumption at 24 V:	150 mA without load
Operating temperature:	-40 °C to +85 °C
Connectors for sensors	
Digital I/O:	8 x 5-pin M12 female connector
Counter inputs:	4 x 8-pin M12 female connector

Digital outputs

Digital inputs Number of inputs:

Overvoltage protection:

Optical isolation: Nominal voltage:

Input impedance: Logic input levels:

Input voltage:

Number of outputs:	max. 16, 2 per M12 female connector
Optical isolation:	1000 V through opto-couplers
Output type:	High-side, load to ground acc. to IEC 1131-2
Nominal voltage:	24 V
Voltage supply:	18 V-30 V
Current (max.):	1.85 A typical for 8 channels through PTC
Output current / output:	500 mA max.

Ordering information

MSX-E1731

Ethernet multifunction counter system, 4 EnDat counter inputs, 16 digital I/O. Incl. technical description, software drivers and ConfigTools.

Connection cables

Voltage supply

CMX-2x:	Shielded cable, M12 5-pin female connector/open end, IP 65		
CMX-3x:	For cascading, shielded cable, M12 5-pin female connector/male connector IP 65		
Trigger/S	ynchro		
CMX-4x:	Shielded cable, M12 5-pin female connector/open end, IP 65		
CMX-5x:	For cascading, shielded cable, M12 5-pin		
	female connector/male connector IP 65		

Ethernet

CMX-6x: CAT5E cable, M12 D-coded male connector/RJ45 connector **CMX-7x:** For cascading: CAT5E cable, 2 x M12 D-coded male connector

Connection to peripherals

CMX-8x: For the digital I/O, shielded cable, M12 5-pin male connector/open end, IP 65

Options

S7 Modbus TCP Client Library for S7: Easy use of the Ethernet systems MSX-E with PLCs MSX-E 5V-Trigger: Level change of the trigger inputs and outputs to 5 V MX-Clip, MX-Rail (Please specify when ordering!), MX-Screw, PCMX-1x

* Preliminary product information



Ethernet counter system 3 sin/cos counter inputs 1 V_{pp} , 1 analog input, 24-bit



MSX-E1741-1VPP

3 sin/cos counter inputs 1 V_{DD}

New

1 analog input

24 V digital trigger input

M12 and M23 connectors



*Operating temperature







on reauest



see page 112



Features

- 24 V digital trigger input
- ARM[®]9 32-bit processor
- 64 MB onboard SDRAM for storing data

IP 65

- Robust standardized metal housing
- Power Save Mode: Reduced power consumption when no acquisition runs

ARM[®]9

Technoloav

Safety features

- Status LEDs for fast error diagnostics
- Optical isolation 1000 V
- Input filters

Counter

- Compare logic
- Status LED for counter inputs

Interfaces

- Fast 24 V trigger input
- Ethernet switch with 2 ports
- Synchronisation/trigger In/Out •
- Line in for 24 V supply and cascading

Communication interfaces

- Web server (configuration and monitoring)
- Command server SOAP for transferring commands
- Data server (TCP/IP or UDP socket) for sending •
- acquisition data Event server (TCP/IP socket) for sending system events ٠
- (Diagnostics such as temperature, short-circuits ...)
- Command server Modbus TCP and Modbus (UDP) for sending commands

Synchronisation/time stamp

Time stamp

Cascadable, can be

synchronised

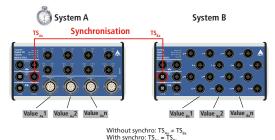
in the µs range

Several MSX-E systems can be synchronised with one another in the µs range through a synchro connection. This allows to start a synchronous data acquisition, to generate trigger events and to synchronise the time on several MSX-E systems. Furthermore, the systems have a time stamp that logs the point in time at which the data was acquired by the system.

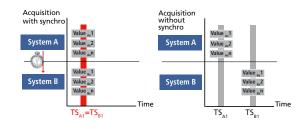
Timer function or

compare logic for

synchro trigger signal



The combination of synchronisation and time stamp (TS) allows the clear allocation of signals that were captured by several systems.





* Preliminay product information

- 3 x 32 bit sin/cos counter inputs 1 V_{pp}, 250 kHz
- Voltage supply of the sensors via M23 female
- connectors (5 V)
- •

Analog input

• 1 diff./SE inputs, 24-bit, 4-pin M12 female connector

• Sampling frequency 100 kHz/channel max.

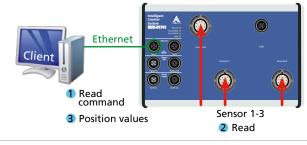
Phone: +49 7229 1847-0 info@addi-data.com www.addi-data.com



Acquisition modes – There are 2 different possibilities for reading the counter inputs.

1. Asynchronous acquisition

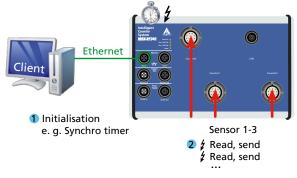
With the asynchronous acquisition, the counter inputs can be read out via SOAP or Modbus function. For each function call, the values of one channel are transmitted.





With the synchronous acquisition, the inputs are first initialised and then the acquisition is parameterised. The acquisition runs automatically depending from a trigger source.

Either the 24V trigger input or a synchro trigger can be used as trigger source.



Synchro latch

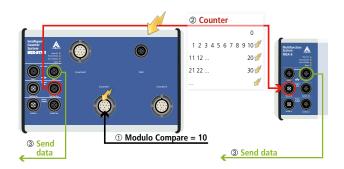
A periodic acquisition of the counter inputs is possible using the synchro timer (synchro latch). Several MSX-E systems (of same or different types) can be combined through synchro trigger. With the synchronous acquisition, as soon as there are measurement values available, they are sent to the clients via socket connection.

Compare logic

With the compare logic, a synchro-trigger signal can be generated in order to latch the counter value as soon as the counter value is equal to the compare value.

With the additional "Modulo-Mode" (Modulo Compare), a trigger can also be generated at the n value of the compare value.

Thus it is possible, e. g. when using an encoder with 3,600 steps / revolution to obtain each degree of a measurement value (Modulo Compare = 10). The thus generated synchro-trigger can also be used for data acquisition on further MSX-E systems.



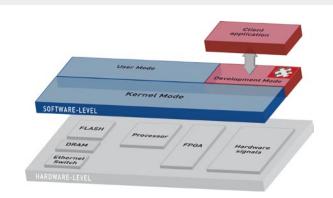
Index logic

The Index track of the encoder can also be used as trigger source. Either the selected edge of the index signal can directly start the acquisition or a synchrotrigger can be generated and then used on further MSX-E systems. Furthermore, the index signal can be used to delete the counter channel.

Onboard programming / stand-alone operation

Development mode

With the Development mode of the MSX-E systems you can customise your measurement, control and regulation applications to fit your requirements. The programs run directly on the MSX-E systems, which has two advantages: external PCs are relieved and you can process data freely according to your requirements. This helps you to improve the efficiency of your processes and to secure your investments.







The **ConfigTools** program allows an easy administration of the MSX-E systems. These are automatically detected in the network. **ConfigTools** consists of common and specific functions.

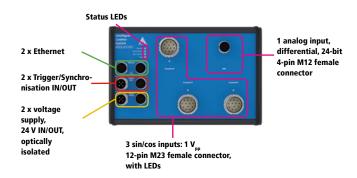
In addition, with **ConfigTools**, the complete configuration of a MSX-E system can be saved and transferred to another system of the same type (clone function).

ConfigTools is included in the delivery.

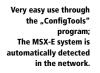
ConfigTools functions for MSX-E1741-1VPP:

- Change of IP address
- Display of web interface
- Firmware update
- Save/load system configuration

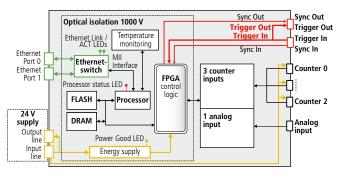
Features



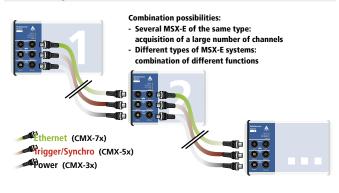
Simplified block diagram



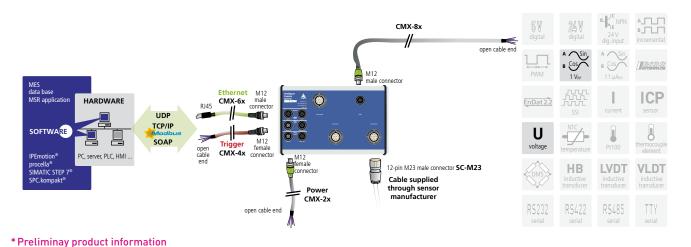




Cascading



ADDI-DATA connection technology





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Specifications*

Specifications	
Sin/cos counter in	puts
Number of inputs:	3 x sin/cos counter inputs, each with A, B, C signals
Resolution:	32-bit
Differential inputs:	1 V _{np}
Interpolation factor:	up to 8192
Max. input frequency:	max. 250 kHz (at min. interpolation)
ESD protection:	2 kV
Analog input	
Number/type:	1 differential / single-ended input (software-selectable)
Resolution:	24-bit
Optical isolation:	1000 V
Input ranges:	\pm 10 V, \pm 1 V, \pm 100 mV, \pm 10 mV (24-bit), 0-10 V, 0-1 V,
	0-100 mV, 0-10 mV (23-bit),
	software-programmable, current input 0(4) – 20 mA optional
Sampling frequency:	100 kHz
Gain:	x1, x10, x100, software-programmable
Trigger:	digital input, synchro, software-programmable
Voltage supply, Et	hernet, Trigger, Synchro
	ge supply, Ethernet, Trigger, Synchronisation and
Electromagnetic Compatibility	apply to all MSY E systems. See page 21

Interface:	Ethernet acc. to specification IEEE802.3
Dimensions (mm):	215 x 110 x 54
Weight:	in preparation
Degree of protection:	IP 65
Current consumption at 24 V:	in preparation
Operation temperature:	-40 °C to +85 °C
Connectors for sensors	
Sin/cos counter input 1 V	3 x 12-pin M23 female connector
Analog input:	1 x 4-pin M12 female connector

Electromagnetic Compatibility apply to all MSX-E systems. See page 31.

Ordering information

MSX-E1741-1VPP

Ethernet counter system, 3 sin/cos counter inputs 1 V_{pp}, 1 analog input, 24-bit. Incl. technical description, software drivers and ConfigTools.

Connection cables

Voltage	supply	CMX-8x:
CMX-2x:	Shielded cable, M12 5-pin female connector/open end, IP 65	
CMX-3x:	For cascading, shielded cable, M12 5-pin	Options
	female connector/male connector IP 65	S7 Modb
Trigger/S	ynchro	
CMX-4x:	Shielded cable, M12 5-pin female connector/open end, IP 65	MSX-E 5V
CMX-5x:	For cascading, shielded cable, M12 5-pin	MX-Clip,
	female connector/male connector IP 65	- • • •
Ethernet		
CMX-6x:	CAT5E cable, M12 D-coded male connector/RJ45 connector	
CMX-7x:	For cascading: CAT5E cable, 2 x M12 D-coded male connector	

Connection to peripherals

For the analog input, shielded cable, M12 5-pin male connector/open end, IP 65

ous TCP Client Library for S7:

Easy use of the Ethernet systems MSX-E with PLCs V-Trigger: Level change of the trigger inputs and outputs to 5 V MX-Rail (Please specify when ordering!), MX-Screw, PCMX-1x



* Preliminay product information

Ethernet analog input system 16 analog inputs, diff., 16-bit

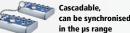


Integrated Ethernet switch



Operating temperature

ARM[®]9 IP 65



On request:









DatabaseConnect see page 112

Features

- 24 V digital trigger input
- ARM®9 32-bit processor
- 64 MB onboard SDRAM for storing data
- Robust standardized metal housing
- Power Save Mode: Reduced power consumption when no acquisition runs

Analog inputs

- 16 diff. inputs, 16-bit, 5-pin M12 female connector
- Sampling frequency max. 100 kHz, ٠ up to 4 simultaneous channels
 - Input ranges: ± 5 V, ± 10 V (16-bit) 0-5 V, 0-10 V (15-bit)
- Current inputs optional

Safety features

- Status LEDs for fast error diagnostics
- Optical isolation Input filters
- Overvoltage protection ± 40 V
- · Internal temperature monitoring

Interfaces

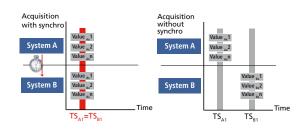
- Fast 24 V trigger input
- Ethernet switch with 2 ports
- Synchronisation/trigger In/Out •
- Line in for 24 V supply and cascading

Communication interfaces

sending commands

- Web server (configuration and monitoring)
- Command server SOAP for transferring commands
- Data server (TCP/IP or UDP socket) for sending .
- acquisition data Event server (TCP/IP socket) for sending system events
- (Diagnostics such as temperature, short-circuits ...) Command server Modbus TCP and Modbus (UDP) for

Without synchro: $TS_{Ax} \neq TS_{By}$ With synchro: $TS_{Ay} = TS_{By}$ The combination of synchronisation and time stamp (TS) allows the clear allocation of signals that were captured







Technology

Synchronisation/time stamp

16 analog inputs, differential, 16-bit

Simultaneous acquisition of 4 channels

Voltage or current inputs

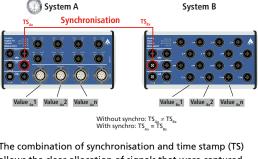
Time stamp

by several systems.

MSX-E3011

with 100 kHz each M12 connector 24 V trigger input

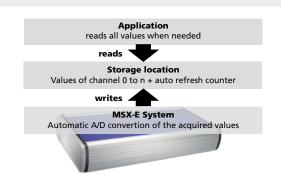
> Several MSX-E systems can be synchronised with one another in the $\boldsymbol{\mu}\boldsymbol{s}$ range through a synchro connection. This allows to start a synchronous data acquisition, to generate trigger events and to synchronise the time on several MSX-E systems. Furthermore, the systems have a time stamp that logs the point in time at which the data was acquired by the system.





Auto-refresh mode

In auto-refresh mode, the measurement values are updated automatically after each acquisition. The acquisition is initialised once and the values of the channels are stored in the memory of the MSX-E Ethernet system. The client (e.g. PC, server, PLC, ...) reads the acquired values asynchronously to the acquisition through socket connection, SOAP or Modbus function. Thereby, the new value is read and the old values are overwritten. In addition to the measurement values, the auto-refresh counter can also be read, which allows to sort the measurement values chronologically. The auto-refresh mode can be combined with a hardware or a synchro trigger and also allows the automatic averaging of values.



Example: 8 channels, each with 10 µs

13 12 13

5

4

0

10 µs

Group IV

Group III

Group II

Group I

S: Sequence

S₂

8

4 5

0

Simultaneous acquisition

Sequence mode

In sequence mode, a list of channels is acquired. Thereby, the single measurement rows are stored one after another. The client receives the acquired values asynchronously to the acquisition through a socket connection. In the sequence mode, the measurement values are read in chronological order, this means the oldest values are read first. The acquisition can be effected continuously, with or without delay or in combination with a hardware or synchro triager.

Horizontal wiring (with 4 gauges/sensors)

25 kHz 25 kHz 25 kHz 25 kHz 100 kHz

Vertical wiring (with 4 gauges/sensors)

	100 kHz	0 kHz	0 kHz	0 kHz	
Group I	٥ 🔘		() ²	() ³	100 kHz
Group II	() 4	●5	6	() 7	100 kHz
Group III	© 8	9	() ¹⁰	() ¹¹	100 kHz
Group IV	() 12	() ¹³	() ¹⁴	() ¹⁵	100 kHz

Sn

12 13

4 5

End of acquisition

8

0 1

Group I **Different wiring** for 25 kHz/channel and 100 kHz/groups Group II Group III

Acquisition triggered through trigger or synchro input

Example: A measurement process is to be started through an external trigger impulse. For each trigger, 10 sequences are to be acquired. After the acquisition of the 10 sequences they are to be sent to the client.

Reading data from a MSX-E system

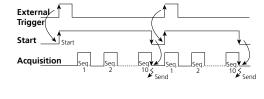
MSX-E systems are multi-client capable, this means several clients (e. g. PC, server, PLC, ...) can read the measurement values of one MSX-E system at the same time. For this, each client establishes a socket connection to the data server of the MSX-E system (port 8989). As soon as the measurement values are available on the data server, the MSX-E system transfers them to the clients.

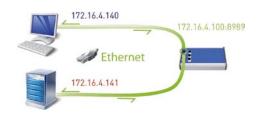
Onboard programming / stand-alone operation

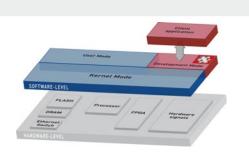
Development mode

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With the Development mode of the MSX-E systems you can customise your measurement, control and regulation applications to fit your requirements. The programs run directly on the MSX-E systems, which has two advantages: external PCs are relieved and you can process data freely according to your requirements. This helps you to improve the efficiency of your processes and to secure your investments.









Acquisition speed

Group IV

Phone: +49 7229 1847-0 info@addi-data.com Fax:

+49 7229 1847-222 www.addi-data.com

The **ConfigTools** program allows an easy administration of the MSX-E systems. These are automatically detected in the network. **ConfigTools** consists of common and specific functions.

In addition, with **ConfigTools**, the complete configuration of a MSX-E system can be saved and transferred to another system of the same type (clone function).

ConfigTools is included in the delivery.

ConfigTools functions for MSX-E3011:

- Change of IP address
- Display of web interface
- Firmware update
- Save/load system configuration
- Save/load channel configuration
- Monitor for analog inputs

Features

Status LEDs



16 analog inputs, differential, 16-bit 5-pin M12 female connector

Sync Out

Trigger Out Trigger In

Sync In

16 analog

inputs - 4 groups - 4 x 16-bit ADC

Gain x1, x2 Differential

current inputs

Optional:

Sync Out

Trigger Out

Trigger In

Sync In

Analog input 0

Analog

Analog input 8

Analog input 12

2 x Ethernet

2 x Trigger/Synchronisation IN/OUT

2 x voltage supply,

24 V IN/OUT, optically isolated

Simplified block diagram

Ethernet Link / ACT LEDs

FLASH

DRAM

Ethernet switch

Processor status LED 🛉

Ethernet Port 0

Ethernet

24 V

supply

Output

Input line

Port 1

Optical isolation 1000 V

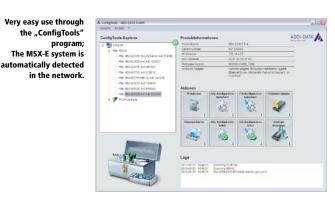
Temperature monitoring

Interface

Power Good LED

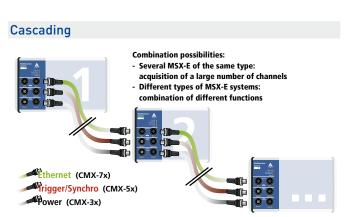
Energy supply

Processo





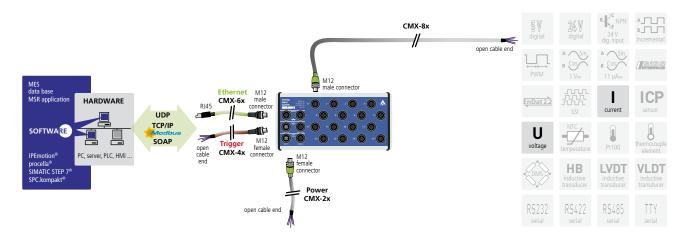
Example of monitor function: Testing the analog inputs.



FPGA

logic

ADDI-DATA connection technology







Specifications	
Analog inputs	
Number/type:	16 differential inputs
Architecture:	4 groups of 4 channels each
	4-port simultaneous converter with one 4-channel
	multiplexer per converter
Resolution:	16-bit, SAR ADC
Accuracy:	± 1.221 mV typ. (± 4 LSB)
	± 2.442 mV max.
Relative Accuracy (INL):	± 3 LSB max (ADC)
Optical isolation:	1000 V
Input ranges:	± 5 V, ± 10 V (16-bit), 0-5 V, 0-10 V (15-bit)
	software-programmable, current inputs optional
Sampling frequency:	25 kHz per channel / 100 kHz max.
Gain:	x1, x2, software-programmable
Common mode rejection:	80 dB min. DC up to 60 Hz (diff. amplifier)
Input impedance (PGA): Bandwidth (-3dB):	$10^9 \Omega$ // 10nF against GND
Bandwidth (-3dB):	160 kHz limited through TP filters 16 Hz version
	with differential filter
Trigger:	digital input, synchro,
inggei.	software-programmable
Offset error:	$\pm 1 \text{ LSB} (\pm 305 \mu\text{V})$
Gain error:	+ 2.5 LSB
Temperature drift:	$2.3 \times V_{in} + 22.5 (\mu V/ °C) typ.$
V _{in} : input voltage in Volts	
$(-10 \text{ V} \le \text{V}_{in} \le +10 \text{ V})$	
In the temperature range:	
from -40°C to +85°C	4.5 ppm/°C FSR
V II	

Voltage supply, Ethernet, Trigger, Synchro

The specifications for the voltage supply, Ethernet, Trigger, Synchronisation and Electromagnetic Compatibility apply to all MSX-E systems. See page 31.

System features	
Interface:	Ethernet acc. to specification IEEE802.3
Dimensions:	215 x 110 mm x 50 mm
Weight:	850 g
Degree of protection:	IP 65
Current consumption at 24 V:	180 mA
Operating temperature:	-40 °C to +85 °C
Connectors for sensors	
For analog inputs	16 x 5-pin M12 female connector

Ordering information

MSX-E3011

Ethernet analog input system, 16 analog inputs, diff., 16-bit. Incl. technical description, software drivers and ConfigTools.

Connection cables

Voltage supply

CMX-2x:	Shielded cable, M12 5-pin female connector/open end, IP 65		
CMX-3x:	For cascading, shielded cable, M12 5-pin		
	female connector/male connector IP 65		
Trigger/S	ynchro		
CMX-4x:	Shielded cable, M12 5-pin female connector/open end, IP 65		
CMX-5x:	-5x: For cascading, shielded cable, M12 5-pin		
	female connector/male connector IP 65		
Ethernet			
CMX-6x:	CAT5E cable, M12 D-coded male connector/RJ45 connector		
CMX-7x:	For cascading, CAT5E cable, 2 x M12 D-coded male connector		

Connection to peripherals

CMX-8x: Shielded cable, M12 5-pin male connector/open end, IP 65 Options

 PC-Diff:
 Current input 0(4)-20 mA for 1 input, diff. (please indicate the number of channels)

 S7 Modbus TCP Client Library for S7: Easy use of the Ethernet systems

MSX-E with PLCs **MSX-E 5V-Trigger:** Level change of the trigger inputs and outputs to 5 V **MX-Clip, MX-Rail** (please specify when ordering!), **MX-Screw, PCMX-1x**



Ethernet force-distance measurement system, 1 counter input, 4 analog inputs, 24-bit, 2 dig. I/O, 24 V





MSX-E3017

1 counter input	ut
-----------------	----

4 analog inputs, diff, 24-bit

2 digital I/0. 24 V

Time stamp

Easy configuration: Easy mode

Synchronisation/time stamp

Synchronisation

was acquired by the system.

🕥 System A

Value 41 Value 42 Value An

rs..=ts.

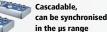
by several systems.

Acauisition with synchro

Syster

Syste





On request: Compare logic for

Several MSX-E systems can be synchronised with one an-

other in the µs range through a synchro connection.

This allows to start a synchronous data acquisition, to

generate trigger events and to synchronise the time on

several MSX-E systems. Furthermore, the systems have a

time stamp that logs the point in time at which the data

Without synchro: $TS_{Ax} \neq TS_{Bx}$ With synchro: $TS_{Ax} = TS_{Bx}$

Acquisition without synchro

System A

System B

The combination of synchronisation and time stamp (TS)

allows the clear allocation of signals that were captured

System B

Value Ry 1 Value Ry 2 Value Ry n

Value "2

Value ",

TS_{A1}

Value _1

Value _{Bx}2

Value "n

TS_{B1}

synchro trigger signal





on request



DatabaseConnect on request, see page 112



www.addi-data.com

Features

- 24 V digital trigger input
- ARM[®]9 32-bit processor
- 64 MB onboard SDRAM for storing data
- Robust standardized metal housing
- Power Save Mode: Reduced power consumption when no acquisition runs

Safety features

- Status LEDs for fast error diagnostics
- Optical isolation Input filters

Counter input

- 1 incremental counter input, 32-bit, (on request: Sin/Cos 1 V_{pp} or Sin/Cos 11 µA_{pp}) 12-pin M23 female connector
- Max. input frequency 5 MHz

Analog inputs

- 4 diff. inputs, 24-bit, 4-pin M12 female connector
- Sampling frequency max. 100 kHz/channel simultaneous on 4 channels

Digital I/O

- 1 x 2 digital lines, 24 V, which can be parameterised as pairs of inputs or outputs
- Shutdown logic
- Watchdog for resetting the outputs to "0"
- At Power-On the outputs are set to "0"
- Electronic fuse
- Dual LED for each 24 V digital I/O with direction • indication

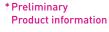
Interfaces

- Fast 24 V trigger input
- Ethernet switch with 2 ports
- Synchronisation/trigger In/Out
- Line in for 24 V supply and cascading ٠

Communication interfaces

- Web server (configuration and monitoring)
- Command server SOAP for transferring commands
- Data server (TCP/IP or UDP socket) for sending • acquisition data
- Event server (TCP/IP socket) for sending system events (Diagnostics such as temperature, short-circuits ...)
- Command server Modbus TCP and Modbus (UDP) for sending commands







With the MSX-E3017 system, you can effect force-distance measurements in 2 different ways: in auto-refresh mode or sequence mode. The acquisition can be done depending on a position or on time.w

Auto-refresh mode

In auto-refresh mode, the measurement values are updated automatically after each acquisition. The acquisition is initialised once and the values of the channels are stored in the memory of the MSX-E Ethernet system. The client (e.g. PC, server, PLC, ...) reads the acquired values asynchronously to the acquisition through socket connection, SOAP or Modbus function. Thereby, the new value is read and the old values are overwritten. In addition to the measurement values, the auto-refresh counter can also be read, which allows to sort the measurement values chronologically. The auto-refresh mode can be combined with a hardware or a synchro trigger and also allows the automatic averaging of values.

Sequence mode

In sequence mode, a list of channels is acquired. Thereby, the single measurement rows are stored one after another. The client receives the acquired values asynchronously to the acquisition through a socket connection. In the sequence mode, the measurement values are read in chronological order, this means the oldest values are read first. The acquisition can be effected continuously, with or without delay or in combination with a hardware or synchro trigger.

Acquisition triggered through trigger or synchro input

Example: A measurement process is to be started through an external trigger impulse. For each trigger, 10 sequences are to be acquired. After the acquisition of the 10 sequences they are to be sent to the client.

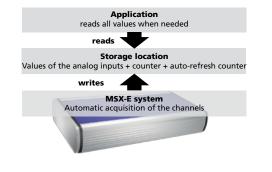
Reading data from a MSX-E system

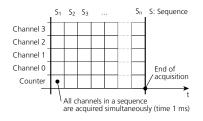
MSX-E systems are multi-client capable, this means several clients (e. g. PC, server, PLC, ...) can read the measurement values of one MSX-E system at the same time. For this, each client establishes a socket connection to the data server of the MSX-E system (port 8989). As soon as the measurement values are available on the data server, the MSX-E system transfers them to the clients.

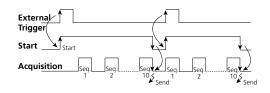
Onboard programming

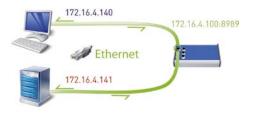
Development mode

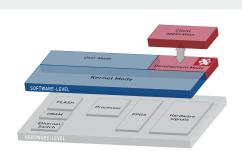
With the Development mode of the MSX-E systems you can customise your measurement, control and regulation applications to fit your requirements. The programs run directly on the MSX-E systems, which has two advantages: external PCs are relieved and you can process data freely according to your requirements. This helps you to improve the efficiency of your processes and to secure your investments.













55

The **ConfigTools** program allows an easy administration of the MSX-E systems. These are automatically detected in the network. **ConfigTools** consists of common and specific functions.

In addition, with **ConfigTools**, the complete configuration of a MSX-E system can be saved and transferred to another system of the same type (clone function).

ConfigTools is included in the delivery.

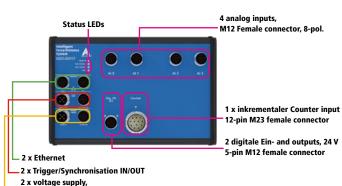
ConfigTools functions for MSX-E3017:

- Change of IP address
- Display of web interface
- Firmware update
- Save/load system configuration
- Save/load channel configuration
- Monitor for analog inputs
- Visualisation of the force-distance measurement (Easy mode)

Very easy use through the "ConfigTools" program; The MSX-E system is automatically detected in the network.

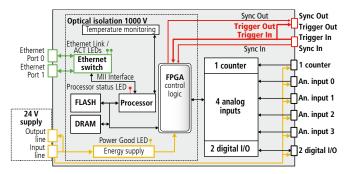


Features

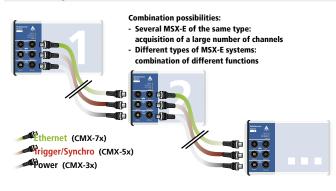


24 V IN/OUT, optically isolated

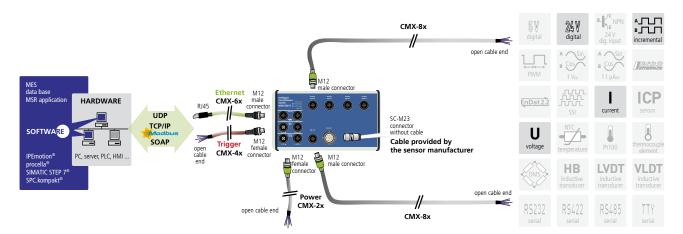
Simplified block diagram



Cascading



ADDI-DATA connection technology



* Preliminary product information

56



2, on 1 M12 female connector

1000 V through opto-couplers

High-side, load to ground acc. to IEC 1131-2



Specifications*

Incremental counter

Number of counter inputs:	1
Input type:	Differential or TTL inputs
Differential inputs:	Complies with the EIA standards RS422A
Common mode range:	+12 / -7 V
Input sensitivity:	± 200 mV
Input hysteresis:	50 mV typ.
Input impedance:	12 k Ω min.
Max. input frequency:	5 MHz at nominal voltage
"Open Circuit Fail Safe	
Receiver Design"	"1" = inputs open
ESD protection:	Up to ±15 kV
Voltage supply	

Incremental encoder:

5 V or 24 V, max. 500 mA

Analog inputs

Number/type:	4 differential inputs, 1 A/D converter per channel
Resolution:	24-bit, SAR ADC
Optical isolation:	1000 V
Input ranges:	± 10 V, ± 5 V (24-bit), 0-10 V, 0-5 V (23-Bit),
	software-programmable,
	current inputs optional
Sampling frequency:	100 kHz per channel
Gain:	x1, x10, x100, x1000, software-programmable
Trigger:	digital input, synchro, software-programmable

Nominal voltage: 24 V 18 V-30 V Voltage supply: 1.85 A typical for 2 channels through PTC Current (max.) Output current / output: 500 mA max. Short-circuit current / output: 1.7 A max Shut-down logic at 24 V, R_{load} =10m Ω RDS ON Resistance: 280 mΩ max. Switch-on time: 100 us max RL=48 Ω von 80 % V Switch-off time: 150 µs max RL=48 Ω von 10 % V Overtemperature (shutdown) 135°C max. (output driver) Temperature hysteresis: 15°C typ. (output driver) Diagnostics: Common diagnostics bit at overtemperature Watchdog:

Number:	1
Resolution:	16-bit
Time base:	μs, ms, s (programmable)
Time value range:	1 to 65535

Voltage supply, Ethernet, Trigger, Synchro

The specifications for the voltage supply, Ethernet, Trigger, Synchronisation and Electromagnetic Compatibility apply to all MSX-E systems. See page 31.

Digital inputs

Number of inputs:	2, on 1 M12 female connector	
	Common ground acc. to IEC 1131-2	
Overvoltage protection:	30 V	
Optical isolation:	1000 V through opto-couplers	
Nominal voltage:	24 VDC	
Input voltage:	0 to 30 V	
Input impedance:	>1 MΩ	
Logic input levels:	UH (max) 30 V typ. UH (min) 18 V typ.	
	UL (max) 16 V typ. UL (min) 0 V typ.	

System features

Digital outputs Number of outputs:

Optical isolation:

Output type:

oyotonnitouturoo	
Interface:	Ethernet acc. to specification IEEE802.3
Dimensions:	220 x 140 x 50 mm
Weight:	ca. 900 g
Degree of protection:	IP 65
Current consumption at 24 V:	150 mA without load
Operating temperature:	-40 °C to +85 °C
Connectors for sensors	
For analog inputs:	4 x 4-pin M12 female connector
For digital I/O:	1 x 5-pin M12 female connector
For the counter input:	1 x 12-pin M23 female connector

Ordering information

MSX-E3017

Ethernet force-distance measurement system, 1 counter input, 4 analog inputs, 24-bit, 2 digital I/O, 24 V. Incl. technical description, software drivers and ConfigTools.

Connection cables

Voltage supply

- CMX-2x: Shielded cable, M12 5-pin female connector/open end, IP 65 CMX-3x: For cascading, shielded cable, M12 5-pin
- female connector/male connector IP 65

Trigger/Synchro

- CMX-4x: Shielded cable, M12 5-pin female connector/open end, IP 65
- **CMX-5x:** For cascading, shielded cable, M12 5-pin female connector/male connector IP 65

Ethernet

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CMX-6x: CAT5E cable, M12 D-coded male connector/RJ45 connector **CMX-7x:** For cascading, CAT5E cable, 2 x M12 D-coded male connector

Connection to peripherals

CMX-8x: Shielded cable, M12 5-pin male connector/open end, IP 65

Options

PC-Diff: Current input 0(4)-20 mA for 1 input, diff. (please indicate the number of channels)

S7 Modbus TCP Client Library for S7: Easy use of the Ethernet systems MSX-E with PLCs

MSX-E 5V-Trigger: Level change of the trigger inputs and outputs to 5 V MX-Clip, MX-Rail (please specify when ordering!), MX-Screw, PCMX-1x



Ethernet force-distance measurement system, 1 counter input, 4 inputs for strain gauges, 24-bit, 2 digital I/O, 24 V





MSX-E3317

1 counter input

4 inputs for strain gauges, diff, 24-bit

2 digital I/O, 24 V

Easy configuration: Easy mode



RoHS 2002/95/EC





on request



DatabaseConnect on request, see page 112



More information on www.addi-data.com

Features

- 24 V digital trigger input
- ARM®9 32-bit processor
- 64 MB onboard SDRAM for storing data
- Robust standardized metal housing
- Power Save Mode: Reduced power consumption when no acquisition runs

Safety features

- Status LEDs for fast error diagnostics
- Optical isolation Input filters

Counter input

- 1 incremental counter input, 32-bit, (on request: Sin/Cos 1 V_{pp} or Sin/Cos 11 μA_{pp}) 12-pin M23 female connector
- Max. input frequency 5 MHz

Inputs for strain gauges

- 4 inputs for strain gauges, 24-bit, M12 female connector, 8-pin
- Sampling frequency max. 788 Hz/channel (max. 2 channels simultaneously)

Digital I/O

- 1 x 2 digital lines, 24 V, which can be parameterised as pairs of inputs or outputs
- Shutdown logic
- At Power-On the outputs are set to "0"
- Electronic fuse
- Dual LED for each 24 V dig. I/O with direction indication

Interfaces

- Fast 24 V trigger input
- Ethernet switch with 2 ports
- Synchronisation/trigger In/Out
- Line in for 24 V supply and cascading

Communication interfaces

- Web server (configuration and monitoring)
- Command server SOAP for transferring commands
- Data server (TCP/IP or UDP socket) for sending acquisition data
- Event server (TCP/IP socket) for sending system events (Diagnostics such as temperature, short-circuits ...)
- Command server Modbus TCP and Modbus (UDP) for sending commands



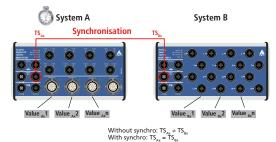
Time stamp

Several MSX-E systems can be synchronised with one another in the µs range through a synchro connection. This allows to start a synchronous data acquisition, to generate trigger events and to synchronise the time on several MSX-E systems. Furthermore, the systems have a time stamp that logs the point in time at which the data was acquired by the system.

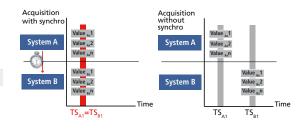
On request:

Compare logic for

synchro trigger signal



The combination of synchronisation and time stamp (TS) allows the clear allocation of signals that were captured by several systems.





* Preliminary Product information

Power
 no acc



With the MSX-E3317 system, you can effect force-distance measurements in 2 different ways: in auto-refresh mode or sequence mode. The acquisition can be done depending on a position or on time.

Auto-refresh mode

In the auto-refresh mode, the measurement values are updated automatically after each acquisition. The acquisition is initialised once and the values of the channels are stored in the memory of the MSX-E Ethernet system. The client (e.g. PC, server, PLC, ...) reads the acquired values asynchronously to the acquisition through socket connection, SOAP or Modbus function. Thereby, the new value is read and the old values are overwritten. In addition to the measurement values, the auto-refresh counter can also be read, which allows to sort the measurement values chronologically. The auto-refresh mode can be combined with a hardware or a synchro trigger and also allows the automatic averaging of values.



Application

Sequence mode

In the sequence mode, a list of channels is acquired. Thereby, the single measurement rows are stored one after another. The client receives the acquired values asynchronously to the acquisition through a socket connection. In the sequence mode, the measurement values are read in chronological order, this means the oldest values are read first. The acquisition can be effected continuously, with or without delay or in combination with a hardware or synchro trigger.

Acquisition triggered through trigger or synchro input

Example: A measurement process is to be started through an external trigger impulse. For each trigger, 10 sequences are to be acquired. After the acquisition of the 10 sequences they are to be sent to the client.

Reading data from a MSX-E system

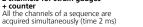
MSX-E systems are multi-client capable, this means several clients (e. g. PC, server, PLC, ...) can read the measurement values of one MSX-E system at the same time. For this, each client establishes a socket connection to the data server of the MSX-E system (port 8989). As soon as the measurement values are available on the data server, the MSX-E system transfers them to the clients.

Onboard programming

Development mode

With the Development mode of the MSX-E systems you can customise your measurement, control and regulation applications to fit your requirements. The programs run directly on the MSX-E systems, which has two advantages: external PCs are relieved and you can process data freely according to your requirements. This helps you to improve the efficiency of your processes and to secure your investments.





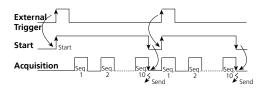


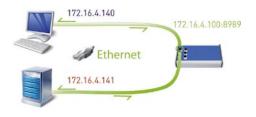


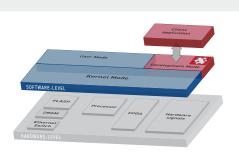
С С С 2

S: Sequence 🔛 Simultan. acquisition C: Counter

End of acquisition









ADDI-DATA

SPIRIT OF EXCELLENCE



The **ConfigTools** program allows an easy administration of the MSX-E systems. These are automatically detected in the network. **ConfigTools** consists of common and specific functions.

In addition, with **ConfigTools**, the complete configuration of a MSX-E system can be saved and transferred to another system of the same type (clone function).

ConfigTools is included in the delivery.

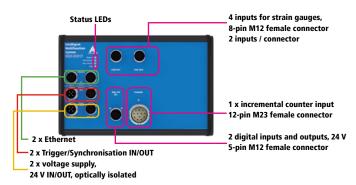
ConfigTools functions for MSX-E3317:

- Change of IP address
- Display of web interface
- Firmware update
- Save/load system configuration
- Save/load channel configuration
- Monitor for Inputs for strain gauges
- Visualisation of the force-distance measurement (Easy mode)

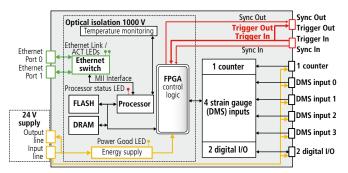
Very easy use through the "ConfigTools" program; The MSX-E system is automatically detected in the network.



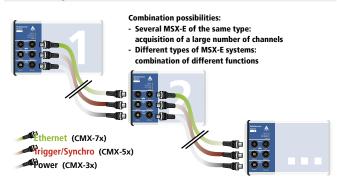
Features



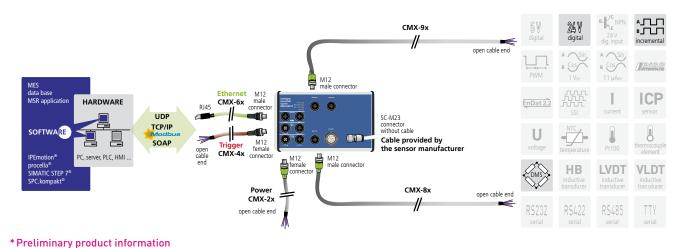
Simplified block diagram



Cascading



ADDI-DATA connection technology





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Specifications*

Incremental counter

Number of counter inputs:	1
Input type:	Differential or TTL inputs
Differential inputs:	Complies with the EIA standards RS422A
Common mode range:	+12 / -7 V
Input sensitivity:	± 200 mV
Input hysteresis:	50 mV typ.
Input impedance:	12 kΩ min.
Max. input frequency:	5 MHz at nominal voltage
"Open Circuit Fail Safe	
Receiver Design"	"1" = inputs open
ESD protection:	Up to ±15 kV
Voltage supply	
Incremental encoder:	5 V or 24 V, max. 500 mA

Inputs for strain gauges

	- J
Number of inputs:	4 differential inputs for strain gauges 2 inputs per M12 connector
Resolution:	24-bit
Optical isolation:	1000 V
Throughput per M12 connector:	max. 788 Hz for 1 channel, max. 528 Hz for 2 channels
Voltage supply	
for the sensors:	10 V, 50 mA
Trigger:	Digital input, synchro, software-programmable
Digital inputs	
Number of inputs:	2, on 1 M12 female connector, Common ground acc. to IEC 1131-2
Overvoltage protection:	30 V
Optical isolation:	1000 V through opto-couplers
	241/06

inputs for strain gauges		
/12 connector	Voltage supply, Eth	herr
for 1 channel, max. 528 Hz for 2 channels	The specifications for the voltage Electromagnetic Compatibility a	
	System features	
synchro, software-programmable	Interface:	Eth
	Dimensions:	220
	Weight:	ca.
	Degree of protection:	IP 6
emale connector,	Current consumption at 24 V:	150
und acc. to IEC 1131-2	Operating temperature:	-40

Number of inputs:	2, on 1 M12 female connector,
	Common ground acc. to IEC 1131-2
Overvoltage protection:	30 V
Optical isolation:	1000 V through opto-couplers
Nominal voltage:	24 VDC
Input voltage:	0 to 30 V
Input impedance:	>1 MΩ
Logic input levels:	UH (max) 30 V typ. UH (min) 18 V typ.
	UL (max) 16 V typ. UL (min) 0 V typ.
1	

Digital outputs	
Number of outputs:	2, on 1 M12 female connector
Optical isolation:	1000 V through opto-couplers
Output type:	High-side, load to ground acc. to IEC 1131-2
Nominal voltage:	24 V
Voltage supply:	18 V-30 V
Current (max.):	1.85 A typical for 2 channels through PTC
Output current / output:	500 mA max.
Short-circuit current / output:	1.7 A max.
	Shut-down logic at 24 V, R _{load} =10mΩ
RDS ON Resistance:	280 mΩ max.
Switch-on time:	100 µs
	max RL=48 Ω von 80 % V _{out}
Switch-off time:	150 µs
	max RL=48 Ω von 10 % V _{out}
Overtemperature (shutdown):	135°C max. (output driver)
Temperature hysteresis:	15°C typ. (output driver)
Diagnostics:	Common diagnostics bit at overtemperature

net, Trigger, Synchro

pply, Ethernet, Trigger, Synchronisation and to all MSX-E systems. See page 31.

Interface:	Ethernet acc. to specification IEEE802.3
Dimensions:	220 x 140 x 50 mm
Weight:	ca. 900 g
Degree of protection:	IP 65
Current consumption at 24 V:	150 mA without load
Operating temperature:	-40 °C to +85 °C
Connectors for sensors	
For inputs for strain gauges:	2 x 8-pin female connector M12
For digital I/O:	1 x 5-pin M12 female connector
For the counter input:	1 x 12-pin M23 female connector

Ordering information

MSX-E3317

Ethernet force-distance measurement system, 1 counter input, 4 inputs for strain gauges, 24-bit, 2 digital I/O, 24 V. Incl. technical description, software drivers and ConfigTools.

Connection cables

Voltage supply

	· · · · · · · · · · · · · · · · · · ·
CMX-2x:	Shielded cable, M12 5-pin female connector/open end, IP 65
CMX-3x:	For cascading, shielded cable, M12 5-pin
	female connector/male connector IP 65

Trigger/Synchro

	,
CMX-4x:	Shielded cable, M12 5-pin female connector/open end, IP 65
CMX-5x:	For cascading, shielded cable, M12 5-pin
	female connector/male connector IP 65
Ethernet	

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CMX-6x: CAT5E cable, M12 D-coded male connector/RJ45 connector CMX-7x: For cascading, CAT5E cable, 2 x M12 D-coded male connector

Connection to peripherals

CMX-8x: Shielded cable, M12 5-pin male connector/open end, IP 65 CMX-9x: Shielded cable, M12 8-pin male connector/open end, IP 65

Options

S7 Modbus TCP Client Library for S7: Easy use of the Ethernet systems MSX-E with PLCs

MSX-E 5V-Trigger: Level change of the trigger inputs and outputs to 5 V MX-Clip, MX-Rail (please specify when ordering!), MX-Screw, PCMX-1x





Ethernet analog input system 16 analog inputs, diff., 16-bit









on request



DatabaseConnect see page 112



www.addi-data.com

Features

- 24 V digital trigger input
- ARM®9 32-bit processor
- 64 MB onboard SDRAM for storing data
- Robust standardized metal housing
- Power Save Mode: Reduced power consumption when
 no acquisition runs

Analog inputs

- 16 diff. inputs, 16-bit, 5-pin M12 female connector
- Sampling frequency max. 100 kHz, up to 4 simultaneous channels
- Input ranges: ±5 V, ±10 V (16-bit), 0-5 V, 0-10 V (15-bit)
- Gain PGA x1, x2, x10, x20, x100, x200, x1000, x2000 software-programmable, signals up to +/-5mV (16-bit) are possible
- Current inputs optional

Safety features

- Status LEDs for fast error diagnostics
- Optical isolation Input filters
- Overvoltage protection ± 40 V
- · Internal temperature monitoring

Interfaces

- Fast 24 V trigger input
- Ethernet switch with 2 ports
- Synchronisation/trigger In/Out
- Line in for 24 V supply and cascading

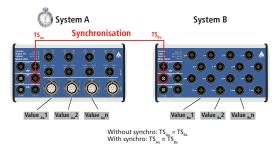
Communication interfaces

- Web server (configuration and monitoring)
- Command server SOAP for transferring commands
- Data server (TCP/IP or UDP socket) for sending
- acquisition data • Event server (TCP/IP socket) for sending system events
- Event server (TCP/IP socket) for sending system events (Diagnostics such as temperature, short-circuits ...)
- Command server Modbus TCP and Modbus (UDP) for sending commands

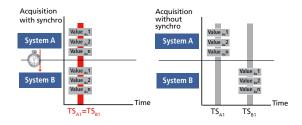
Synchronisation/time stamp

Time stamp

Several MSX-E systems can be synchronised with one another in the µs range through a synchro connection. This allows to start a synchronous data acquisition, to generate trigger events and to synchronise the time on several MSX-E systems. Furthermore, the systems have a time stamp that logs the point in time at which the data was acquired by the system.



The combination of synchronisation and time stamp (TS) allows the clear allocation of signals that were captured by several systems.



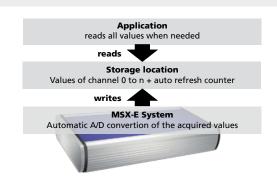


* Preliminary Product information



Auto-refresh mode

In auto-refresh mode, the measurement values are updated automatically after each acquisition. The acquisition is initialised once and the values of the channels are stored in the memory of the MSX-E Ethernet system. The client (e.g. PC, server, PLC, ...) reads the acquired values asynchronously to the acquisition through socket connection, SOAP or Modbus function. Thereby, the new value is read and the old values are overwritten. In addition to the measurement values, the auto-refresh counter can also be read, which allows to sort the measurement values chronologically. The auto-refresh mode can be combined with a hardware or a synchro trigger and also allows the automatic averaging of values.



Example: 8 channels, each with 10 µs

13 12 13

5

4

0

10 µs

Group IV

Group III

Group II

Group I

S: Sequence

100 kHz

Fxternal

Star

Triager

Start

S₂

8

4 5

0

Simultaneous acquisition

Sequence mode

In sequence mode, a list of channels is acquired. Thereby, the single measurement rows are stored one after another. The client receives the acquired values asynchronously to the acquisition through a socket connection. In the sequence mode, the measurement values are read in chronological order, this means the oldest values are read first. The acquisition can be effected continuously, with or without delay or in combination with a hardware or synchro trigger.

Horizontal wiring (with 4 gauges/sensors)

Group IV

25 kHz

25 kHz

25 kHz

Vertical wiring (with 4 gauges/sensors)

	100 kHz	0 kHz	0 kHz	0 kHz	
Group I	٥ 🎑		() ²	() ³	100 kHz
Group II	() 4	●5	٥	() 7	100 kHz
Group III	() 8	() 9	() ¹⁰	() ¹¹	100 kHz
Group IV	() 12	() ¹³	() ¹⁴	() ¹⁵	100 kHz

Sn

12 13

4 5

End of acquisition

8

0

Acquisition speed Different wiring for 25 kHz/channel and 100 kHz/groups Group II Group II Group III

Acquisition triggered through trigger or synchro input

Example: A measurement process is to be started through an external trigger impulse. For each trigger, 10 sequences are to be acquired. After the acquisition of the 10 sequences they are to be sent to the client.

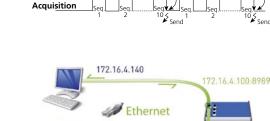
Reading data from a MSX-E system

MSX-E systems are multi-client capable, this means several clients (e. g. PC, server, PLC, ...) can read the measurement values of one MSX-E system at the same time. For this, each client establishes a socket connection to the data server of the MSX-E system (port 8989). As soon as the measurement values are available on the data server, the MSX-E system transfers them to the clients.

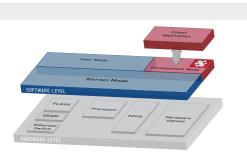
Onboard programming / stand-alone operation

Development mode

With the Development mode of the MSX-E systems you can customise your measurement, control and regulation applications to fit your requirements. The programs run directly on the MSX-E systems, which has two advantages: external PCs are relieved and you can process data freely according to your requirements. This helps you to improve the efficiency of your processes and to secure your investments.



172.16.4.141



* Preliminary product information



The ConfigTools program allows an easy administration of the MSX-E systems. These are automatically detected in the network. ConfigTools consists of common and specific functions.

In addition, with ConfigTools, the complete configuration of a MSX-E system can be saved and transferred to another system of the same type (clone function).

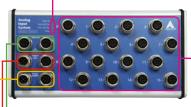
ConfigTools is included in the delivery.

ConfigTools functions for MSX-E3021:

- Change of IP address
- Display of web interface
- Firmware update ٠
- Save/load system configuration ٠
- Save/load channel configuration
- Monitor for analog inputs

Features

Status LEDs



16 analog inputs, differential, 16-bit 5-pin M12 female connector

Sync Out

Trigger Out Trigger In

Sync In

16 analog

Optional:

current inputs

- 4 groups - 4 x 16-bit ADC - Gain x1, x2 (x5, x10 opt.) - Differential - Ontional:

Sync Out

Trigger Out

Trigger In

Sync In

Analog input 0

Analog

Analog input 8

Analog input 12

2 x Ethernet

2 x Trigger/Synchronisation IN/OUT

2 x voltage supply, 24 V IN/OUT, optically isolated

Simplified block diagram

Optical isolation 1000 V

Ethernet switch

MII Interface

rocessor status LED 📍

Extended Flash memory

Processor

Power Good LED

Energy supply

Temperat

Ethernet Link / ACT LEDs ••

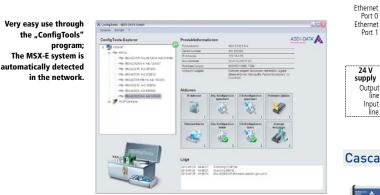
FLASH

DRAM

24 V

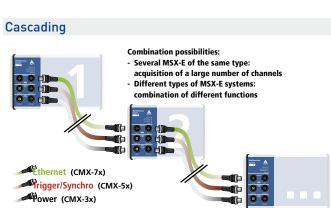
Output line

Input line





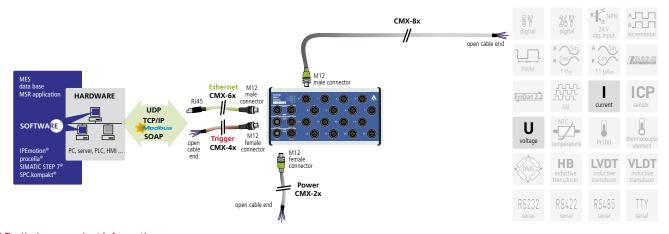
Example of monitor function: Testing the analog inputs.



FPGA

logic

ADDI-DATA connection technology





* Preliminary product information



Specifications*

Number/type:	16 differential inputs
Architecture:	4 groups of 4 channels each
	4-port simultaneous converter with one 4-channel
	multiplexer per converter
Resolution:	16-bit, SAR ADC
Accuracy:	± 1.221 mV typ. (± 4 LSB)
	\pm 2.442 mV max.
Relative Accuracy (INL):	\pm 3 LSB max (ADC)
Optical isolation:	1000 V
Input ranges:	± 5 V, ± 10 V (16-bit), 0-5 V, 0-10 V (15-bit)
	current inputs optional
Sampling frequency:	25 kHz per channel / 100 kHz max.
Gain:	x1, x2, x10, x20, x100, x200, x1000, x2000
	software-programmable
Common mode rejection:	80 dB min. DC up to 60 Hz (diff. amplifier)
Input impedance (PGA):	10 ⁹ Ω // 10nF against GND
Bandwidth (-3dB):	160 kHz limited through TP filters
	16 Hz version
	with differential filter
Trigger:	digital input, synchro,
	software-programmable
Offset error:	± 1 LSB (± 305 μV)
Gain error:	± 2.5 LSB
Temperature drift :	2.3 x V _{in} + 22.5 (μ V/ °C) typ.
	V_{in} : input voltage in Volts (-10 V \leq V _{in} \leq +10 V)
	In the temperature range
	from -40°C to +85°C: 4.5 ppm/°C FSR

RAM:	64 MB	
FLASH:	4 MB for system data	
Extended FLASH memory:	4 GB (3.7 GB for measured data)	
Buffered real-time clock:	approx. 4 weeks at 20 °C	
Voltage supply, Eth	hernet, Trigger, Synchro	
	ge supply, Ethernet, Trigger, Synchronisation and apply to all MSX-E systems. See page 31.	
System features		
Interface:	Ethernet acc. to specification IEEE802.3	
Dimensions:	215 x 110 x 50 mm	
Weight:	850 g	
	IP 65	
Degree of protection:	180 mA	
Current consumption at 24 V:		
	-25 °C to +85 °C	
Current consumption at 24 V: Operating temperature:		
Current consumption at 24 V:	-25 °C to +85 °C -40 °C to +85 °C on request	
Current consumption at 24 V: Operating temperature:	-25 °C to +85 °C	
Current consumption at 24 V: Operating temperature: Connectors for sensors	-25 °C to +85 °C -40 °C to +85 °C on request	

MSX-E3021

Ethernet analog input system, 16 analog inputs, diff., 16-bit. Incl. technical description, software drivers and ConfigTools.

Connection cables

Voltage supply

	Shielded cable, M12 5-pin female connector/open end, IP 65 For cascading, shielded cable, M12 5-pin female connector/male connector IP 65		Current input 0(4)- (please indicate th IS TCP Client Librar
Trigger/S	ynchro	MSX-E wit	
	Shielded cable, M12 5-pin female connector/open end, IP 65 For cascading, shielded cable, M12 5-pin female connector/male connector IP 65	MX-Clip, I	- Trigger: Level chan <u>c</u> MX-Rail (please speci ı, PCMX-1x
Ethernet			
CMX-6x:	CAT5E cable, M12 D-coded male connector/RJ45 connector		
CMX-7x:	For cascading, CAT5E cable, 2 x M12 D-coded male connector		
	on to peripherals Shielded cable, M12 5-pin male connector/open end, IP 65		

Options

 PC-Diff:
 Current input 0(4)-20 mA for 1 input, diff. (please indicate the number of channels)

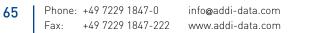
 S7 Modbus TCP Client Library for S7: Easy use of the Ethernet systems

MSX-E with PLCs **MSX-E 5V-Trigger:** Level change of the trigger inputs and outputs to 5 V **MX-Clip, MX-Rail** (please specify when ordering!),



SPIRIT OF EXCELLENCE

Ordering information



Ethernet analog input system 16 analog inputs, differential, 16-bit



MSX-E3027

16 analog inputs, differential, 16-bit	
Voltage or current inputs	
4 GB extended memory	
Buffered real-time clock	
Stainless steel housing – UIP 67	

Fast distributed data acquisition



RoHS 2002/95/EC





on request



DatabaseConnect see page 112



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Features

- 24 V digital trigger input
- ARM[®]9 32-bit processor
- 64 MB onboard SDRAM for storing data
- Robust standardized metal housing
- Power Save Mode: Reduced power consumption when
 no acquisition runs

Analog inputs

- 16 diff. inputs, 16-bit, 5-pin M12 female connector
- Sampling frequency max. 100 kHz, up to 4 simultaneous channels
- Input ranges: ±5 V, ±10 V (16-bit), 0-5 V, 0-10 V (15-bit)
- Gain PGA x1, x2, x10, x20, x100, x200, x1000, x2000 software-programmable,
- signals up to +/-5mV (16-bit) are possible • Current inputs 0(4) to 20 mA optional

Safety features

- Status LEDs for fast error diagnostics
- Optical isolation
- Input filters
- Overvoltage protection ± 40 V
- Internal temperature monitoring

Interfaces

- Fast 24 V trigger input
 - Ethernet switch with 2 ports
 - Synchronisation/trigger In/Out
 - Line in for 24 V supply and cascading

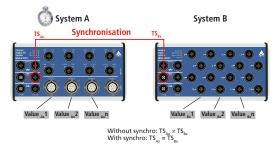
Communication interfaces

- Web server (configuration and monitoring)
- Command server SOAP for transferring commands
- Data server (TCP/IP or UDP socket) for sending acquisition data
- Event server (TCP/IP socket) for sending system events (Diagnostics such as temperature, short-circuits ...)
- Command server Modbus TCP and Modbus (UDP) for sending commands

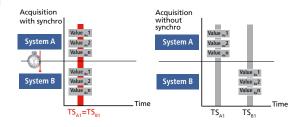


Time stamp

Several MSX-E systems can be synchronised with one another in the µs range through a synchro connection. This allows to start a synchronous data acquisition, to generate trigger events and to synchronise the time on several MSX-E systems. Furthermore, the systems have a time stamp that logs the point in time at which the data was acquired by the system.



The combination of synchronisation and time stamp (TS) allows the clear allocation of signals that were captured by several systems.







Auto-refresh mode

In auto-refresh mode, the measurement values are updated automatically after each acquisition. The acquisition is initialised once and the values of the channels are stored in the memory of the MSX-E Ethernet system. The client (e.g. PC, server, PLC, ...) reads the acquired values asynchronously to the acquisition through socket connection, SOAP or Modbus function. Thereby, the new value is read and the old values are overwritten. In addition to the measurement values, the auto-refresh counter can also be read, which allows to sort the measurement values chronologically. The auto-refresh mode can be combined with a hardware or a synchro trigger and also allows the automatic averaging of values.

Sequence mode

Acquisition speed

for 25 kHz/channel and 100 kHz/groups

Different wiring

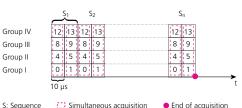
In sequence mode, a list of channels is acquired. Thereby, the single measurement rows are stored one after another. The client receives the acquired values asynchronously to the acquisition through a socket connection. In the sequence mode, the measurement values are read in chronological order, this means the oldest values are read first. The acquisition can be effected continuously, with or without delay or in combination with a hardware or synchro triager.

Horizontal wiring (with 4 gauges/sensors)

25 kHz 25 kHz 25 kHz 25 kHz 100 kHz Group I Group II Group III Group IV



Example: 8 channels, each with 10 µs



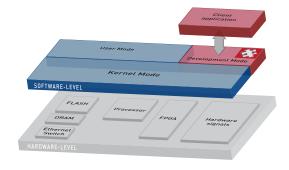
Vertical wiring (with 4 gauges/sensors)

		100 kHz	0 kHz	0 kHz	0 kHz	
Gro	oupl	٥ 🎑	() ¹	() ²	() ³	100 kHz
Gro	oup II	()	0 ⁵	() 6	() 7	100 kHz
Gro	oup III	() 8	() ⁹	() ¹⁰	() ¹¹	100 kHz
Gro	oup IV	() 12	() ¹³	() ¹⁴	() ¹⁵	100 kHz

Onboard programming / stand-alone operation

Development mode

With the Development mode of the MSX-E systems you can customise your measurement, control and regulation applications to fit your requirements. The programs run directly on the MSX-E systems, which has two advantages: external PCs are relieved and you can process data freely according to your requirements. This helps you to improve the efficiency of your processes and to secure your investments.



Buffered real-time clock

The MSX-E3027 system features a buffered real-time clock (SuperCap). This clock keeps on running 4 weeks after the MSX-E system has been switched off. After a new start, the system time is still the current time.



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The **ConfigTools** program allows an easy administration of the MSX-E systems. These are automatically detected in the network. **ConfigTools** consists of common and specific functions.

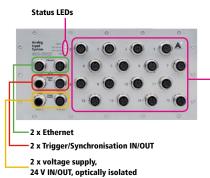
In addition, with **ConfigTools**, the complete configuration of a MSX-E system can be saved and transferred to another system of the same type (clone function).

ConfigTools is included in the delivery.

ConfigTools functions for MSX-E3027:

- Change of IP address
- Display of web interface
- Firmware update
- Save/load system configuration
- Save/load channel configuration
- Monitor for analog inputs

Features



16 analog inputs, differential, 16-bit 5-pin M12 female connector

Sync Out

Trigger Out Trigger In

Sync In

16 analog

inputs - 4 groups - 4 x 16-bit ADC

Gain x1, x2 Differential

current inputs

Optional:

Sync Out

Trigger Out

Trigger In

Sync In

Analog input 0

Analog

Analog input 8

Analog input 12

Simplified block diagram

Ethernet Link / ACT LEDs

FLASH

DRAM

Ethernet switch

Processor status LED 🛉

Ethernet Port 0

Ethernet

24 V

supply

Output

Input line

Port 1

Optical isolation 1000 V

Temperature monitoring

FPGA

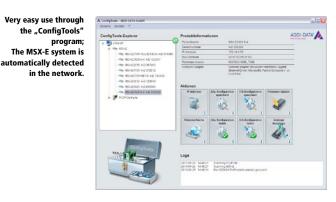
logic

Interface

Power Good LED

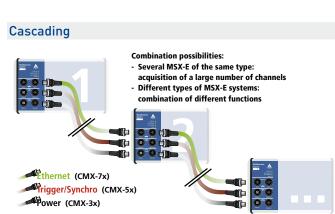
Energy supply

Processo

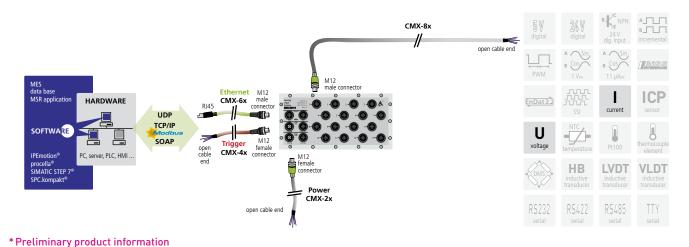




Example of monitor function: Testing the analog inputs.



ADDI-DATA connection technology



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Specifications*	
Analog inputs	
Number/type:	16 differential inputs
Architecture:	4 groups of 4 channels each
Architecture.	4-port simultaneous converter with one 4-channel
	multiplexer per converter
Resolution:	16-bit. SAR ADC
Accuracy:	± 1.221 mV typ. (± 4 LSB)
/iccuracy.	\pm 2.442 mV max.
Relative Accuracy (INL):	\pm 3 LSB max (ADC)
Optical isolation:	1000 V
Input ranges	± 5 V, ± 10 V (16-bit), 0-5 V, 0-10 V (15-bit)
	current inputs optional
Sampling frequency:	25 kHz per channel / 100 kHz max.
Gain:	x1, x2, x10, x20, x100, x200, x1000, x2000
	software-programmable
Common mode rejection:	80 dB min. DC up to 60 Hz (diff. amplifier)
Input impedance (PGA):	$10^{9} \Omega$ // 10nF against GND
Bandwidth (-3dB):	160 kHz limited through TP filters
	16 Hz version
	with differential filter
Trigger:	digital input, synchro,
	software-programmable
Offset error:	\pm 1 LSB (\pm 305 μ V)
Gain error:	± 2.5 LSB
Temperature drift :	2.3 x V _{in} + 22.5 (μ V/ °C) typ.
V _{in} : input voltage in Volts	
$(-10 V \le V_{in} \le +10 V)$	
In the temperature range	
from -40°C to +85°C:	4.5 ppm/°C FSR
Data storage	
RAM:	64 MB

RAM:	64 MB
FLASH:	4 MB for system data
Extended FLASH memory:	4 GB (3.7 GB for measured data)
Buffered real-time clock:	approx. 4 weeks at 20 °C

Voltage supply, Ethernet, Trigger, Synchro

The specifications for the voltage supply, Ethernet, Trigger, Synchronisation and Electromagnetic Compatibility apply to all MSX-E systems. See page 31.

System features	
Interface:	Ethernet acc. to specification IEEE802.3
Degree of protection:	IP 67
Current consumption at 24 V:	160 mA
Operating temperature:	-25 °C to +85 °C
	-40 °C to +85 °C on request
Connectors for sensors	
For analog inputs:	8 x 5-pin M12 female connector

Ordering information

MSX-E3027

Ethernet analog input system, 16 analog inputs, differential, 16-bit. Incl. technical description, software drivers and ConfigTools.

Connection cables

Voltage supply

CMX-2x:	Shielded cable, M12 5-pin female connector/open end, IP 67	PC-DITI:
CMX-3x:	For cascading, shielded cable, M12 5-pin female connector/male connector IP 67	S7 Modbu
Trigger/S	ynchro	MSX-E with
CMX-4x:	Shielded cable, M12 5-pin female connector/open end, IP 67	MSX-E 5V
CMX-5x:	For cascading, shielded cable, M12 5-pin	MX-Rail (p
	female connector/male connector IP 67	
Ethernet		
CMX-6x:	CAT5E cable, M12 D-coded male connector/RJ45 connector	
CMX-7x:	For cascading, CAT5E cable, 2 x M12 D-coded male connector	
	-	

Connection to peripherals

CMX-8x: Shielded cable, M12 5-pin male connector/open end, IP 67

Options

PC-Diff: Current input 0(4)-20 mA for 1 input, diff. (please indicate the number of channels)

S7 Modbus TCP Client Library for S7: Easy use of the Ethernet systems MSX-E with PLCs

MSX-E 5V-Trigger: Level change of the trigger inputs and outputs to 5 V **MX-Rail** (please specify when ordering!), **PCMX-1x**



Ethernet multifunction system 6 diff./SE inputs, 4 analog outputs, 32 digital I/O





MSX-E3121

6 analog inputs, differential/single-ended, 24-bit

Voltage o	r current	inputs
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4 analog outputs, 16-bit

32 digital I/O

24 V digital trigger input







on request



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Features

- 24 V digital trigger input
- ARM[®]9 32-bit processor
- 64 MB onboard SDRAM for storing data
- Robust standardized metal housing
- Power Save Mode: Reduced power consumption when no acquisition runs

Safety features

- Status LEDs for fast error diagnostics
- Optical isolation 1000 V
- Input filters

Analog inputs

- 6 diff./SE inputs, 24-bit, 4-pin M12 female connector
- Sampling frequency max. 100 kHz/channel

Analog outputs

- 4 voltage outputs or
- 2 voltage outputs and 2 current outputs

Digital input and output

- 16 digital inputs, 24 V, optically isolated
- 16 digital outputs, 24 V, optically isolated

Interfaces

- Fast 24 V trigger input
- Ethernet switch with 2 ports
- Synchronisation/trigger In/Out
- Line in for 24 V supply and cascading

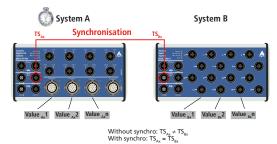
Communication interfaces

- Web server (configuration and monitoring)
- Command server SOAP for transferring commands • Data server (TCP/IP or UDP socket) for sending
- acquisition data
- Event server (TCP/IP socket) for sending system events (Diagnostics such as temperature, short-circuits ...)
- Command server Modbus TCP and Modbus (UDP) for sending commands

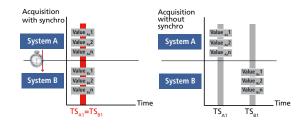
Synchronisation/time stamp

Time stamp

Several MSX-E systems can be synchronised with one another in the µs range through a synchro connection. This allows to start a synchronous data acquisition, to generate trigger events and to synchronise the time on several MSX-E systems. Furthermore, the systems have a time stamp that logs the point in time at which the data was acquired by the system.



The combination of synchronisation and time stamp (TS) allows the clear allocation of signals that were captured by several systems.





* Preliminary product information

Auto-refresh mode

In auto-refresh mode, the measurement values are updated automatically after each acquisition. The acquisition is initialised once and the values of the channels are stored in the memory of the MSX-E Ethernet system. The client (e.g. PC, server, PLC, ...) reads the acquired values asynchronously to the acquisition through socket connection, SOAP or Modbus function. Thereby, the new value is read and the old values are overwritten. In addition to the measurement values, the auto-refresh counter can also be read, which allows to sort the measurement values chronologically. The auto-refresh mode can be combined with a hardware or a synchro trigger and also allows the automatic averaging of values.

Sequence mode

In sequence mode, a list of channels is acquired. Thereby, the single measurement rows are stored one after another. The client receives the acquired values asynchronously to the acquisition through a socket connection. In the sequence mode, the measurement values are read in chronological order, this means the oldest values are read first. The acquisition can be effected continuously, with or without delay or in combination with a hardware or synchro trigger.

Acquisition triggered through trigger or synchro input

Example: A measurement process is to be started through an external trigger impulse. For each trigger, 10 sequences are to be acquired. After the acquisition of the 10 sequences they are to be sent to the client.

Reading data from a MSX-E system

MSX-E systems are multi-client capable, this means several clients (e. g. PC, server, PLC, ...) can read the measurement values of one MSX-E system at the same time. For this, each client establishes a socket connection to the data server of the MSX-E system (port 8989). As soon as the measurement values are available on the data server, the MSX-E system transfers them to the clients.

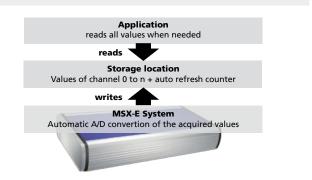
Onboard programming / stand-alone operation

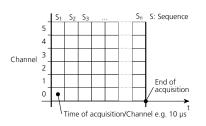
New: Cycle mode

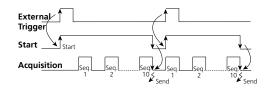
MSX-E systems which are used in stand-alone operation can be controlled through the cycle mode. The cycle mode is a cyclic processing of a program written by the user – the inputs are acquired and the values calculated, if required. At the same time a digital or analog output is possible. Measurement values are read in the auto-refresh mode, thus the current value is always available. A selectable clock pulse (1 ms up to 65535 ms) is available. The programming is executed onboard according to IEC 61131-3.

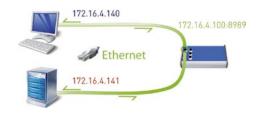
Development mode

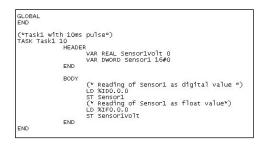
With the Development mode of the MSX-E systems you can customise your measurement, control and regulation applications to fit your requirements. The programs run directly on the MSX-E systems, which has two advantages: external PCs are relieved and you can process data freely according to your requirements. This helps you to improve the efficiency of your processes and to secure your investments.

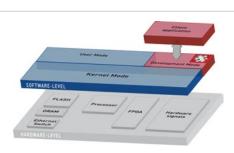












* Preliminary product information



The **ConfigTools** program allows an easy administration of the MSX-E systems. These are recognised automatically in the network. **ConfigTools** consists of common and specific functions.

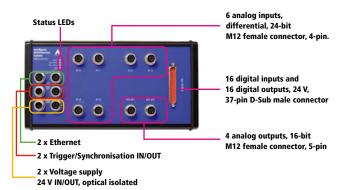
In addition, with **ConfigTools**, the complete configuration of a MSX-E system can be saved and transferred to another system of the same type (clone function).

ConfigTools is included in the delivery.

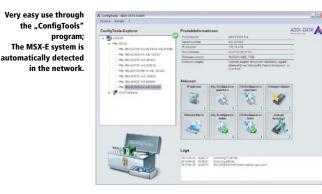
ConfigTools functions for MSX-E3121:

- Change of IP address
- Display of web interface
- Firmware update
- Save/load system configuration
- Save/load channel configuration
- Monitoring the analog inputs/outputs

Features

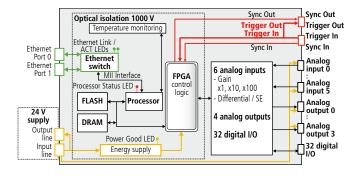


Simplified block diagram

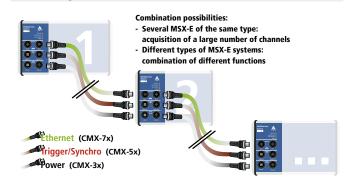




Monitor function example: Check of the analog outputs – value display without programming.



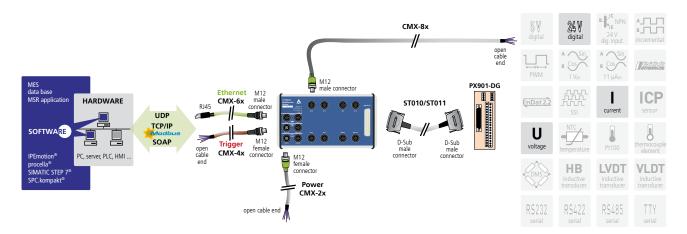
Cascading



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ADDI-DATA connection technology



* Preliminary product information

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Specifications*

Analog inputs

Anatog inputs			
Number/type:	6 differential inputs, 1 A/D converter per channel		
Resolution:	24-bit		
Optical isolation:	1000 V		
Input ranges:	± 10 V, ± 1 V, ± 100 mV, ± 10 mV (24-bit), 0-10 V, 0-1 V,		
	0-100 mV, 0-10 mV (23-bit),		
	software-programmable, current input optional		
Input frequency:	100 kHz per channel		
Gain:	x1, x10, x100 software-programmable		
Trigger:	digital input, synchro, software-programmable		
Analog outputs			
Number of outputs:	4		
Resolution:	16-bit (bipolar) / 15-bit (unipolar)		
Optical isolation:	1000 V		
Output range:	Voltage output: 0-10 V (±10 V)		
	Current output: 0-20 mA		
Output value after reset:	0 V voltage output, not calibrated		
Digital inputs			
Number of inputs:	16, common ground acc. to IEC 1131-2		
Optical isolation:	1000 V through opto-couplers		
Nominal voltage:	24 VDC		
Input voltage:	0 – 30 V		
Logic input levels:	UH (max) 30 V typ. UH (min) 19 V typ.		
	UL (max) 14 V typ. UL (min) 0 V typ.		

Voltage supply, Ethernet, Trigger, Synchro

The specifications for the voltage supply, Ethernet, Trigger, Synchronisation and Electromagnetic Compatibility apply to all MSX-E systems. See page 31.

System features

Interface:	Ethernet acc. to specification IEEE802.3
Dimensions (mm):	270 x 140 x 35
Weight:	approx. 1200 g
Degree of protection:	IP 65
Current consumption at 24 V:	390 mA typ. ± 10 %
Operating temperature:	-40 °C to +85 °C
Connectors for sensors	
For analog inputs:	6 x 4-pin female connector M12
For analog outputs:	2 x 5-pin female connector M12
For digital I/O:	1 x 37-pin D-Sub male connector

Digital outputs

•	
Number of outputs:	16
Optical isolation:	1000 V through opto-couplers
Output type:	High-side, load to ground acc. IEC 1131-2
Nominal voltage:	24 V
Voltage supply:	11 – 36 V
Output current per output:	150 mA max.
Diagnostics:	Common diagnostic bit for 16 channels at
	overtemperature of one channel

Ordering information

MSX-E3121

Ethernet multifunction system, 6 diff./SE inputs, 4 analog outputs, 32 digital I/O. Incl. technical description, software drivers and ConfigTools.

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MSX-E3121-6-4:	4 voltage outputs
MSX-E3121-6-4C:	2 voltage outputs, 2

Connection cables

Voltage supply	1
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	Shielded cable, M12 5-pin female connector/open end, IP 65 For cascading, shielded cable, M12 5-pin female connector/male connector IP 65
Trigger/S	ynchro
CMX-4x:	Shielded cable, M12 5-pin female connector/open end, IP 65
CMX-5x:	For cascading, shielded cable, M12 5-pin
	female connector/male connector IP 65
Ethernet	
CMY GY	CATEE cable M12 D coded male connector/PI/E connector

current outputs

CMX-6x: CAT5E cable, M12 D-coded male connector/RJ45 connector **CMX-7x:** For cascading: CAT5E cable, 2 x M12 D-coded male connector

Connection to peripherals

CMX-8x:Shielded cable, M12 5-pin male connector/open end, IP 65ST010:Standard round cable, shielded, twisted pairs, 2mPX901-DG:Screw terminal panel LED status display for DIN rail

Options

PC-Diff:	Current input 0(4)-20 mA for 1 input, diff.		
	(please indicate the number of channels)		
S7 Modbus TCP Client Library for S7:			
	Easy use of the Ethernet systems MSX-E with PLCs		

Easy use of the Ethernet systems MSX-E with PLCs MSX-E 5V-Trigger: Level change of the trigger inputs and outputs to 5 V MX-Clip, MX-Rail (Please specify when ordering!), MX-Screw, PCMX-1x







MSX-E3122

6 analog inputs, differential/Single-Ended, 24-bit

Voltage or current inputs

4 analog outputs, 16-bit, voltage outputs,

current outputs

64 digital I/O, 24 V

24 V digital trigger input









on request



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Features

- 24 V digital trigger input
- ARM®9 32-bit processor
- 64 MB onboard SDRAM for storing data
- Robust standardized metal housing
- Power Save Mode: Reduced power consumption when no acquisition runs

Safety features

- Status LEDs for fast error diagnostics
- Optical isolation Input filters

Analog inputs

- 6 diff./SE inputs, 24-bit, 4-pin M12 female connector
- Sampling frequency max. 100 kHz/channel

Analog outputs

- 4 voltage outputs or
- 2 voltage and 2 current outputs

Digital I/O

- 32 optically isolated digital inputs, 24 V
- 32 optically isolated digital outputs, 24 V

Interfaces

- Fast 24 V trigger input
- Ethernet switch with 2 ports
- Synchronisation/trigger In/Out
- Line in for 24 V supply and cascading

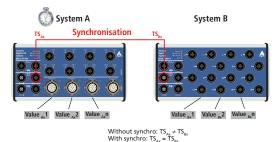
Communication interfaces

- Web server (configuration and monitoring)
- Command server SOAP for transferring commands
- Data server (TCP/IP or UDP socket) for sending
- acquisition data
 Event server (TCP/IP socket) for sending system events (Diagnostics such as temperature, short-circuits ...)
- Command server Modbus TCP and Modbus (UDP) for sending commands

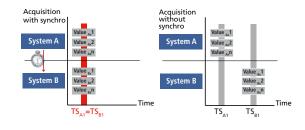
Synchronisation/time stamp

Time stamp

Several MSX-E systems can be synchronised with one another in the μ s range through a synchro connection. This allows to start a synchronous data acquisition, to generate trigger events and to synchronise the time on several MSX-E systems. Furthermore, the systems have a time stamp that logs the point in time at which the data was acquired by the system.



The combination of synchronisation and time stamp (TS) allows the clear allocation of signals that were captured by several systems.





* Preliminary Product information

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Auto-refresh mode

In auto-refresh mode, the measurement values are updated automatically after each acquisition. The acquisition is initialised once and the values of the channels are stored in the memory of the MSX-E Ethernet system. The client (e.g. PC, server, PLC, ...) reads the acquired values asynchronously to the acquisition through socket connection, SOAP or Modbus function. Thereby, the new value is read and the old values are overwritten. In addition to the measurement values, the auto-refresh counter can also be read, which allows to sort the measurement values chronologically. The auto-refresh mode can be combined with a hardware or a synchro trigger and also allows the automatic averaging of values.

Sequence mode

In sequence mode, a list of channels is acquired. Thereby, the single measurement rows are stored one after another. The client receives the acquired values asynchronously to the acquisition through a socket connection. In the sequence mode, the measurement values are read in chronological order, this means the oldest values are read first. The acquisition can be effected continuously, with or without delay or in combination with a hardware or synchro trigger.

Acquisition triggered through trigger or synchro input

Example: A measurement process is to be started through an external trigger impulse. For each trigger, 10 sequences are to be acquired. After the acquisition of the 10 sequences they are to be sent to the client.

Reading data from a MSX-E system

MSX-E systems are multi-client capable, this means several clients (e. g. PC, server, PLC, ...) can read the measurement values of one MSX-E system at the same time. For this, each client establishes a socket connection to the data server of the MSX-E system (port 8989). As soon as the measurement values are available on the data server, the MSX-E system transfers them to the clients.

Onboard programming / stand-alone operation

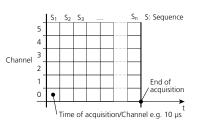
New: Cycle mode

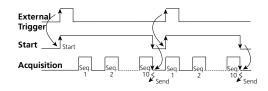
MSX-E systems which are used in stand-alone operation can be controlled through the cycle mode. The cycle mode is a cyclic processing of a program written by the user – the inputs are acquired and the values calculated, if required. At the same time a digital or analog output is possible. Measurement values are read in the auto-refresh mode, thus the current value is always available. A selectable clock pulse (1 ms up to 65535 ms) is available. The programming is executed onboard according to IEC 61131-3.

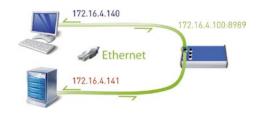
Development mode

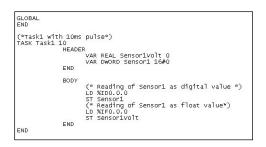
With the Development mode of the MSX-E systems you can customise your measurement, control and regulation applications to fit your requirements. The programs run directly on the MSX-E systems, which has two advantages: external PCs are relieved and you can process data freely according to your requirements. This helps you to improve the efficiency of your processes and to secure your investments.

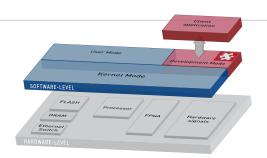












* Preliminary product information



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The **ConfigTools** program allows an easy administration of the MSX-E systems. These are automatically detected in the network. **ConfigTools** consists of common and specific functions.

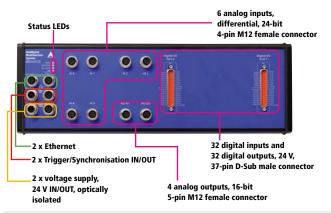
In addition, with **ConfigTools**, the complete configuration of a MSX-E system can be saved and transferred to another system of the same type (clone function).

ConfigTools is included in the delivery.

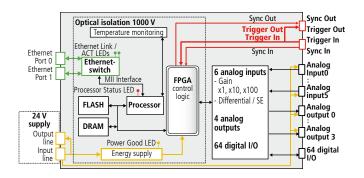
ConfigTools functions for MSX-E3122:

- Change of IP address
- Display of web interface
- Firmware update
- Save/load system configuration
- Save/load channel configuration
- Monitor for analog inputs and outputs

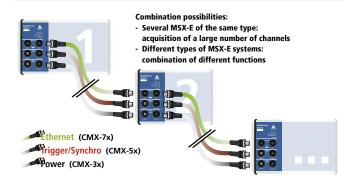
Features



Simplified block diagram



Cascading



program; The MSX-E system is automatically detected in the network.

Very easy use through

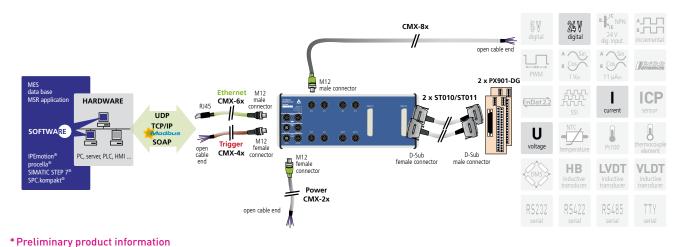
the "ConfigTools"





Example of monitor function: Testing the analog outputs – Value output without programming.

ADDI-DATA connection technology



ADDI-DATA SPIRIT OF EXCELLENCE

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Specifications*

Analog inputs

Analog inputs	
Number/type:	6 differential/Single-Ended inputs (software-configurable), 1 A/D converter per channel
Resolution:	24-bit
Optical isolation:	1000 V
Input ranges:	± 10 V, ± 1 V, ± 100 mV, ± 10 mV (24-bit), 0-10 V, 0-1 V,
	0-100 mV, 0-10 mV (23-Bit),
	software-programmable,
	current inputs 0(4) – 20 mA optional
Sampling frequency:	100 kHz per channel
Gain:	x1, x10, x100, software-programmable
Trigger:	Digital input, synchro, software-programmable
Analog outputs	
Number of outputs:	4
Resolution:	16-bit (bipolar) / 15-bit (unipolar)
Optical isolation:	1000 V
Output range:	Voltage output: 0-10 V (±10 V)
	Current output: 0-20 mA
Output value after reset:	0 V voltage output, not calibrated
Digital inputs	
Number of inputs:	32, common ground acc. to IEC 1131-2
Optical isolation:	1000 V through opto-couplers
Nominal voltage:	24 VDC
Input voltage:	0 – 30 V
Logic input levels:	UH (max) 30 V typ. UH (min) 19 V typ.
	UL (max) 14 V typ. UL (min) 0 V typ.
Digital outputs	
Number of outputs:	32
Optical isolation:	1000 V through opto-couplers
A 1 1 1	

High-side, load to ground acc. to IEC 1131-2

Common diagnostics bit for all 16 channels at

Overtemperature of one channel

24 V 11 – 30 V

2 A

500 mA max.

Voltage supply, Ethernet, Trigger, Synchro

The specifications for the voltage supply, Ethernet, Trigger, Synchronisation and Electromagnetic Compatibility apply to all MSX-E systems. See page 31.

System features

Ethernet acc. to specification IEEE802.3
380 x 130 x 50 mm
in preparation
IP 65
390 mA typ. ± 10 %
-40 °C to +85 °C
6 x 4-pin M12 female connector
2 x 5-pin M12 female connector
2 x 37-pin D-Sub male connector

Ordering information

MSX-E3122

Output type:

Diagnostics:

Nominal voltage:

Max. total current of 16 outputs:

Voltage supply: Output current per channel:

Ethernet multifunction system, 6 diff./SE inputs, 4 analog outputs, 64 digital I/O. Incl. technical description, software drivers and ConfigTools.

Versions

MSX-E3122:4 voltage outputsMSX-E3122-C:2 voltage outputs, 2 current outputs

Connection cables

Voltage supply

CMX-2x: Shielded cable, M12 5-pin female connector/open end, IP 65 **CMX-3x:** For cascading, shielded cable, M12 5-pin

female connector/male connector IP 65

Trigger/Synchro

CMX-4x: Shielded cable, M12 5-pin female connector/open end, IP 65 **CMX-5x:** For cascading, shielded cable, M12 5-pin

female connector/male connector IP 65

Ethernet

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CMX-6x: CAT5E cable, M12 D-coded male connector/RJ45 connector **CMX-7x:** For cascading, CAT5E cable, 2 x M12 D-coded male connector

Connection to peripherals

CMX-8x:Shielded cable, M12 5-pin male connector/open end, IP 65ST010:Standard round cable, shielded, twisted pairs, 2 mPX901-DG:Screw terminal board, LED Status display, for DIN rail

Options

PC-Diff: Current input 0(4)-20 mA for 1 input, diff. (please indicate the number of channels)

S7 Modbus TCP Client Library for S7: Easy use of the Ethernet systems MSX-E with PLCs

MSX-E 5V-Trigger: Level change of the trigger inputs and outputs to 5 V MX-Clip, MX-Rail (please specify when ordering!), MX-Screw, PCMX-1x



info@addi-data.com www.addi-data.com

Ethernet analog output system 8 analog outputs (voltage and/or current), 16-bit



MSX-E3511 / MSX-E3511-C

8 analog outputs, 16-bit

Voltage or current outputs

Function generator for the output of any signal type,

e.g. sine curves, sawtooth curves etc. - can be

configurated separately for each channel

M12 connector

24 V trigger input

Cascadable,

in the µs range

can be synchronised



on request



More information on www.addi-data.com

Features

- 24 V digital trigger input
- ARM®9 32-bit processor
- 64 MB onboard SDRAM for storing data
- Robust standardized metal housing
- Power Save Mode: Reduced power consumption when no acquisition runs

Safety features

- Status LEDs for fast error diagnostics
- Optical isolation
- Diagnostics in case of short-circuits (voltage mode) or line break (current mode)
- Internal temperature monitoring

Analog outputs

- 8 analog outputs, 16-bit: MSX-E3511: voltage outputs 0-10 V, ± 10 V MSX-E3511-C: each output can be configured as voltage or current output 0-20 mA
- Output voltage after reset 0 V
- 5-pin M12 female connector
- Output mode/operation mode: Data output per software function, trigger input or synchro output

Function generator

- Output of any signal type, e.g. sine curves, sawtooth curves etc.
- Maximal 8 channels (each channel independently)
- Can be used without programming skills, e. g. via .csvfile or mathematic functions such as f(x) = sin(x)

Interfaces

- Fast 24 V trigger input
- Ethernet switch with 2 ports
- Synchronisation/trigger In/Out
- Line in for 24 V supply and cascading

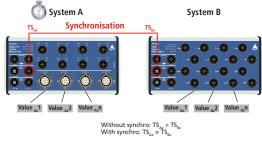
Communication interfaces

- Web server (configuration and monitoring)
- Command server SOAP for transferring commands
- Data server (TCP/IP or UDP socket) for sending acquisition data
- Event server (TCP/IP socket) for sending system events (Diagnostics such as temperature, short-circuits ...)
- Command server Modbus TCP and Modbus (UDP) for sending commands

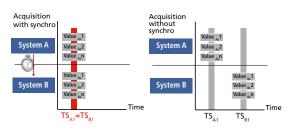
Synchronisation/time stamp

Time stamp

Several MSX-E systems can be synchronised with one another in the µs range through a synchro connection. This allows to start a synchronous data acquisition, to generate trigger events and to synchronise the time on several MSX-E systems. Furthermore, the systems have a time stamp that logs the point in time at which the data was acquired by the system.



The combination of synchronisation and time stamp (TS) allows the clear allocation of signals that were captured by several systems.











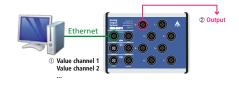
Output modes

Output modes – Analog output values can be generated either via direct access or through function generator:

1. Direct access

In direct access, the values of the outputs are transmitted via SOAP or Modbus function.

For each function call, 1 value (for one or several outputs) is sent. The value output is triggered per software, 24V hardware or synchro trigger.

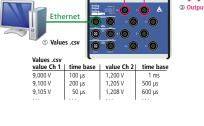


2. Function generator

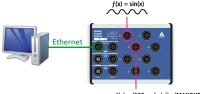
The MSX-E3511 system features a function generator for each analog output. The values are sent either automatically at a defined rate or using a trigger. The 24V trigger input or the synchro trigger can be used, included those issued by another MSX-E system.

Generator with CSV file

The value are transmitted to the function generator via a CSV file which contains both the values and the number of cycles for each value.

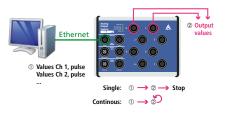


2 Start



 $f(\mathbf{x}) = (100 - \operatorname{abs}(\mathbf{x})) \cdot (MAXOUT/100)$

J



"Single Mode" Generator

In "Single Mode", an array with values is sent to the respective generator via SOAP function. After all values have been transmitted, the generator stops automatically (outputs = 0V).

"Continuous Mode" Generator

Generator with mathematical formula

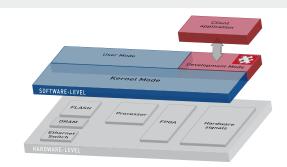
The values are generated through a formula, e.g. f(x) = sin(x).

In "Continuous Mode", an array with values is sent to the respective generator via SOAP function. After all values have been transmitted, the generator automatically starts again at the first value.

Onboard programming / stand-alone operation

Development mode

With the Development mode of the MSX-E systems you can customise your measurement, control and regulation applications to fit your requirements. The programs run directly on the MSX-E systems, which has two advantages: external PCs are relieved and you can process data freely according to your requirements. This helps you to improve the efficiency of your processes and to secure your investments.



Safety

Watchdog

The MSX-E3511 Ethernet system has a 16-bit watchdog which can be programmed in 3 time units (μ s, ms, s). The watchdog is used for automatically resetting the digital outputs to 0 V after a defined time in order e. g. to switch off actuators if an error has occurred.



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The **ConfigTools** program allows an easy administration of the MSX-E systems. These are automatically detected in the network. **ConfigTools** consists of common and specific functions.

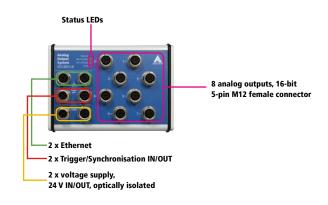
In addition, with **ConfigTools**, the complete configuration of a MSX-E system can be saved and transferred to another system of the same type (clone function).

ConfigTools is included in the delivery.

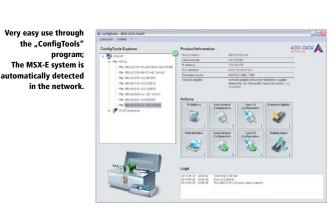
ConfigTools functions for MSX-E3511 / MSX-E3511-C:

- Change of IP address
- Display of web interface
- Firmware update
- Save/load system configuration

Features

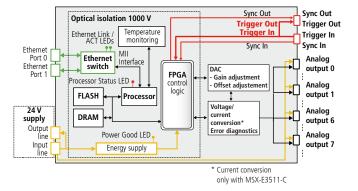


Simplified block diagram

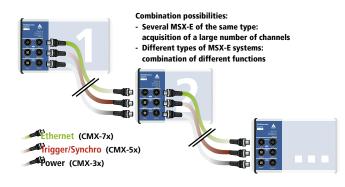




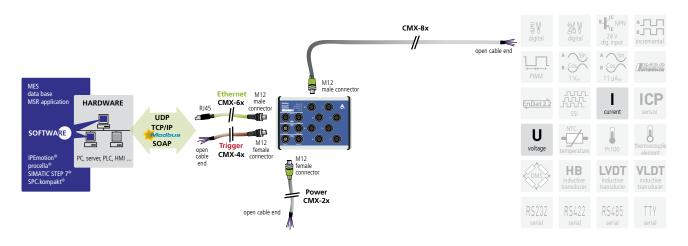
Example of monitor function: Testing the analog outputs – Value output without programming.



Cascading



ADDI-DATA connection technology







Specifications			
Analog outputs			
Number of outputs:	8		
Resolution:	16-bit (bipolar)		
	15-bit (unipolar)		
Optical isolation:	1000 V		
Output range:	voltage output:	0-10 V (±10 V)	
	current output:	0-20 mA	
Output velocity:	Voltage version: ma	x. 40 kHz	
	Voltage version: max. 25 kHz FSR (Full Scale Range) Current version: depending on load		
Overvoltage protection:	±14 V		
Output current/Last:	voltage output:	15 mA, min. 680 Ω	
	current output:	20 mA, max. 550 Ω	
Short-circuit current:	Voltage output:	± 20 mA	
	Current output:	± 32 mA	
Output value after reset	0 V voltage output, not calibrated		
1 watchdog (programmable):	16-bit, 1 µs to 6553	5 s	

Voltage supply, Ethernet, Trigger, Synchro

The specifications for the voltage supply, Ethernet, Trigger, Synchronisation and Electromagnetic Compatibility apply to all MSX-E systems. See page 31.

System features			
Interface:	Ethernet acc. to specification IEEE802.3		
Dimensions:	154 mm x 110 mm x 50 mm		
Weight:	620 g		
Degree of protection:	IP 65		
Current consumption at 24 V:	150 mA	without load	
	310 mA	current outputs switched on	
	410 mA	voltage outputs switched on	
Operating temperature:	- 40°C to + 85°C		
Connectors for sensors			
Analog outputs	8 x 5-pin M12 female connector		

Ordering information

MSX-E3511 / MSX-E3511-C

Ethernet analog output system, 8 analog outputs (voltage and/or current), 16-bit. Incl. technical description, software drivers and ConfigTools.

	Versions				
	MSX-E3511-C:	Ethernet analog output system, 8 analog outputs	Ethernet		
		(voltage and/or current), 16-bit	CMX-6x: CAT5E cable, M12 D-coded male connector/RJ45 connector		
	MSX-E3511:	Ethernet analog output system, 8 analog outputs,	CMX-7x: For cascading, CAT5E cable, 2 x M12 D-coded male connector		
		only voltage for fast signal output, 16-bit	Connection to peripherals		
	, , , , ,		CMX-8x: Shielded cable, M12 5-pin male connector/open end, IP 65		
Connection cables Voltage supply CMX-2x: Shielded cable, M12 5-pin female connector/open end, IP 65		ables	Options		
		1	S7 Modbus TCP Client Library for S7: Easy use of the Ethernet system MSX-E with PLCs		
		ed cable, M12 5-pin female connector/open end, IP 65			
	CMX-3x: For ca	scading, shielded cable, M12 5-pin	MSX-E 5V-Trigger: Level change of the trigger inputs and outputs to 5 V		
female connector/ma		e connector/male connector IP 65	MX-Clip , MX-Rail (please specify when ordering!),		
	Trigger/Synchro	0	MX-Crip, MX-Kall (please specify when ordering!), MX-Screw, PCMX-1x		
	CMX-4x: Shield	ed cable, M12 5-pin female connector/open end, IP 65			
	CMX-5x: For ca	scading, shielded cable, M12 5-pin			

or cascading, shielded cable, IV female connector/male connector IP 65

ADDI-DATA° SPIRIT OF EXCELLENCE

Ethernet system for temperature measurement 16/8/4 channels for thermocouples or RTD, 24-bit



MSX-E3211

16/8/4 differential inputs, 24-bit

For thermocouples or RTD (Pt 100, Pt 1000)

Simultaneous data acquisition up to 8 channels

On request:

Compare logic for

synchro trigger signal

24 V digital trigger input















DatabaseConnect see page 112



Features

Operating temperature

- 24 V digital trigger input
- ARM®9 32-bit processor
- 64 MB onboard SDRAM for storing data

IP 65

- Robust standardized metal housing
- Power Save Mode: Reduced power consumption when
 no acquisition runs

ARM[®]9

Technoloav

Safety features

- Status LEDs for fast error diagnostics
- Optical isolation
- Input filters

Temperature inputs

- 16/8/4 differential inputs for thermocouples or RTD, 24-bit
- Sampling frequency max. 788 Hz/channel (max. 8/4/2 channels simultaneously)
- Auto gain: Optimal adjustment of the gain to the measuring range
- Integrated cold junction compensation (CJC) for thermocouples
- NTC sensors optional

Interfaces

- Fast 24 V trigger input
- Ethernet switch with 2 ports
- Synchronisation/trigger In/Out

24 V supply and cascading

- Communication interface
- Web server (configuration and monitoring)
- Command server SOAP for transferring commands
 Data server (TCP/IP or UDP socket) for sending
- acquisition data Event server (TCP/IP socket) for sending system events
- (Diagnostics such as temperature, short-circuits ...)
- Command server Modbus TCP and Modbus (UDP) for sending commands

Synchronisation/time stamp

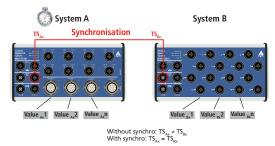
Time stamp

Cascadable, can be

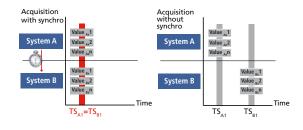
synchronised

in the µs range

Several MSX-E systems can be synchronised with one another in the μ s range through a synchro connection. This allows to start a synchronous data acquisition, to generate trigger events and to synchronise the time on several MSX-E systems. Furthermore, the systems have a time stamp that logs the point in time at which the data was acquired by the system.



The combination of synchronisation and time stamp (TS) allows the clear allocation of signals that were captured by several systems.







Auto-refresh mode

In auto-refresh mode, the measurement values are updated automatically after each acquisition. The acquisition is initialised once and the values of the channels are stored in the memory of the MSX-E Ethernet system. The client (e.g. PC, server, PLC, ...) reads the acquired values asynchronously to the acquisition through socket connection, SOAP or Modbus function. Thereby, the new value is read and the old values are overwritten. In addition to the measurement values, the auto-refresh counter can also be read, which allows to sort the measurement values chronologically. The auto-refresh mode can be combined with a hardware or a synchro trigger and also allows the automatic averaging of values.



Sequence mode

In sequence mode, a list of channels is acquired. Thereby, the single measurement rows are stored one after another. The client receives the acquired values asynchronously to the acquisition through a socket connection. In the sequence mode, the measurement values are read in chronological order, this means the oldest values are read first. The acquisition can be effected continuously, with or without delay or in combination with a hardware or synchro trigger.

Acquisition triggered through trigger or synchro input

Example: A measurement process is to be started through an external trigger impulse. For each trigger, 10 sequences are to be acquired. After the acquisition of the 10 sequences they are to be sent to the client.

Reading data from a MSX-E system

MSX-E systems are multi-client capable, this means several clients (e. g. PC, server, PLC, ...) can read the measurement values of one MSX-E system at the same time. For this, each client establishes a socket connection to the data server of the MSX-E system (port 8989). As soon as the measurement values are available on the data server, the MSX-E system transfers them to the clients.

Onboard programming / stand-alone operation

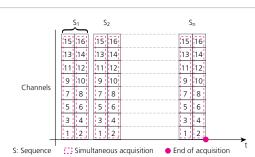
Development mode

With the Development mode of the MSX-E systems you can customise your measurement, control and regulation applications to fit your requirements. The programs run directly on the MSX-E systems, which has two advantages: external PCs are relieved and you can process data freely according to your requirements. This helps you to improve the efficiency of your processes and to secure your investments.

NOTE

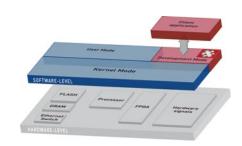
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With thermocouples, it is only possible to capture the relative temperature between the terminal to which the thermocouple is connected and the welding spot. For this reason, in order to calculate the actual temperature at the measuring point, it is necessary to capture an absolute temperature on a second sensor (PTC). This is called cold junction compensation (CJC) and is included on the MSX-E3211-TC system.



External Trigger Start Acquisition seq seq seq seq seq seq seq seq 105 sen 105 sen





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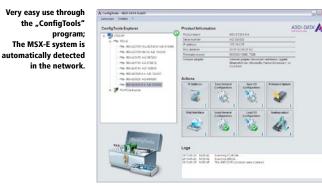
The ConfigTools program allows an easy administration of the MSX-E systems. These are recognised automatically in the network. ConfigTools consists of common and specific functions.

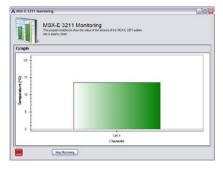
In addition, with ConfigTools, the complete configuration of a MSX-E system can be saved and transferred to another system of the same type (clone function).

ConfigTools is included in the delivery.

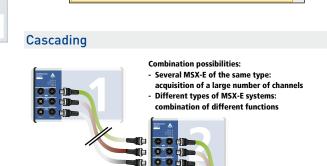
ConfigTools functions for MSX-E3211:

- Change of IP address
- Display of web interface
- Firmware update ٠
- Save/load system configuration •
- Save/load channel configuration •
- Monitoring the temperature inputs





Monitor function example for temperature inputs



Ethernet (CMX-7x) Power (CMX-3x)

Features

Status LEDs

0.0

0 0

2 x Ethernet

Ethernet

Port 0 Ethernet Port 1

24 V

Supply Output line

Input line

2 x Voltage supply,

0

2 x Trigger/Synchronisation IN/OUT

Optical isolation 1000 V

MII

Interface

Processor

Power Good LED

Energy supply

Temperature monitoring

FPGA

loaic

24 V IN/OUT, optical isolated

Simplified block diagram

Ethernet Link / ACT LEDs

Ethernet switch

FLASH

DRAM

Processor Status LED

0

 \sim C 16/8/4 differential inputs for

temperature measurement,

2 inputs / connector

Sync Out

Trigger Out Trigger In

Sync In

16/8 temperature inputs

ADCO

ADC7

Sync Out

Trigger Out

Temperature

Temperature

input 14/15

Trigger In

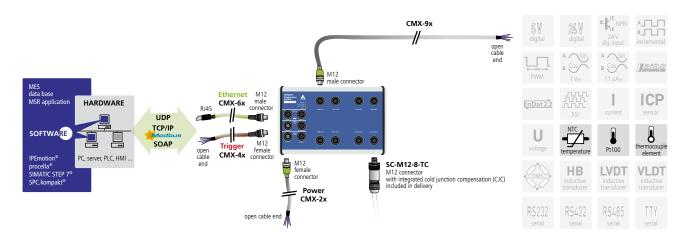
Sync In

input 0/1

8-pin M12 female connector,

rigger/Synchro (CMX-5x)

ADDI-DATA connection technology







Analog inputs			
Number of inputs:		lifferential input	S
		couples or RTD	
		or each M12 co	nnector
Resolution:	24-bit		
Optical isolation:	1000 V	- f 1	
Throughput: Current source:			max. 528 Hz for 2 channels
	~200 µA (I	ntern kalibriert)	
Real acquisition frequency:	at 1 channel	at 2 channels	Sampling frequency (software programmable)
	2.37 Hz	1.585 Hz	5 Hz
	4.73 Hz	3.154 Hz	10 Hz
	9.37 Hz	6.243 Hz	20 Hz
	18.9 Hz	12.6 Hz	40 Hz
	37.35 Hz	24.89 Hz	80 Hz
	73 Hz	48.65 Hz	160 Hz
	145 Hz	96.8 Hz	320 Hz
	276.4 Hz	184.26 Hz	640 Hz
	407.83 Hz	271.96 Hz	1 kHz
	788 Hz	525.48 Hz	2 kHz
Voltage supply, I	Ethernet. 1	riaaer. S	vnchro
The specifications for the vo			
Electromagnetic Compatibil	5 11 2	,	
	ity apply to all M.	JA E Systems. Se	te page 51.
System features			
Interface:	Ethernet ac	c. to specification	on IEEE802.3
Dimensions (mm):	220 x 140 x	k 50	
Weight:	620 g		
Degree of protection:	IP 65		

150 mA \pm 10 % typ. in idle/power save

-40 °C to +85 °C

Current consumption: Operating temperature: **Connectors for sensors**

Degree of protection:

Analog inputs: 8, 4 or 2 x 8-pin M12 female connector

Ordering information

MSX-E3211

Ethernet system for temperature measurement, 16/8/4 channels for thermocouples or RTD, 24-bit. Incl. technical description and software drivers. Trigger/Synchro

Versions

MSX-E3211-TC-16: for 16 thermocouples MSX-E3211-TC-8: for 8 thermocouples for 4 thermocouples MSX-E3211-TC-16: MSX-E3211-RTD-16: for 16 RTD MSX-E3211-RTD-8:

for 8 RTD MSX-E3211-RTD-4: for 4 RTD

Connection cables

Voltage supply

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CMX-2x: Shielded cable, M12 5-pin female connector/open end, IP 65 CMX-3x: For cascading, shielded cable, M12 5-pin female connector/male connector IP 65

CMX-4x: Shielded cable, M12 5-pin female connector/open end, IP 65 CMX-5x: For cascading, shielded cable, M12 5-pin female connector/male connector IP 65

Ethernet

CMX-6x: CAT5E cable, M12 D-coded male connector/RJ45 connector CMX-7x: For cascading: CAT5E cable, 2 x M12 D-coded male connector

Options

S7 Modbus TCP Client Library for S7: Easy use of the Ethernet systems MSX-E with PLCs MSX-E 5V-Trigger: Level change of the trigger inputs and outputs to 5 V

MSX-E-Filter-20Hz: 20 Hz input filter **MSX-E-NTC-100µA:** for NTC sensors with 0-20 k Ω **MSX-E-NTC-50µA:** for NTC sensors with 0-40 k Ω

MX-Rail (Please specify when ordering!),

MX-Screw, PCMX-1x

Ethernet system for pressure/force measurement 16/8 channels for strain gauges, 24-bit





ernet 🥢

Operating temperature













DatabaseConnect see page 112



Features

- 24 V digital trigger input
- ARM®9 32-bit processor
- 64 MB onboard SDRAM for storing data

IP 65

- Robust standardized metal housing
- Power Save Mode: Reduced power consumption when
 no acquisition runs

ARM[®]9

Technoloav

Safety features

- Status LEDs for fast error diagnostics
- Optical isolation
- Input filters

Inputs for strain gauges

- 16/8 differential inputs for strain gauges, 24-bit
- Sampling frequency max. 788 Hz/channel (max. 8/4 channels simultaneously)
- Autogain: Optimal adjustment of the gain to the measuring range
- Integrated sensor supply: 10 V, 50 mA (5 V optional)

Interfaces

- Fast 24 V trigger input
- Ethernet switch with 2 ports
- Synchronisation/trigger In/Out
- Line in for 24 V supply and cascading

Communication interfaces

- Web server (configuration and monitoring)
- Command server SOAP for transferring commands
- Data server (TCP/IP or UDP socket) for sending
- acquisition data • Event server (TCP/IP socket) for sending system events
- (Diagnostics such as temperature, short-circuits ...) • Command server Modbus TCP and Modbus (UDP) for
- sending commands

MSX-E3311

16/8 differential inputs, 24-bit

For strain gauges

Simultaneous acquisition of up to 8 channels

On request:

Compare logic for

synchro trigger signal

24 V digital trigger input



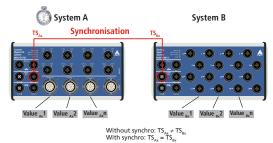
Time stamp

Cascadable,

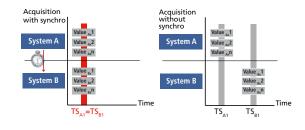
in the µs range

can be synchronised

Several MSX-E systems can be synchronised with one another in the μ s range through a synchro connection. This allows to start a synchronous data acquisition, to generate trigger events and to synchronise the time on several MSX-E systems. Furthermore, the systems have a time stamp that logs the point in time at which the data was acquired by the system.



The combination of synchronisation and time stamp (TS) allows the clear allocation of signals that were captured by several systems.





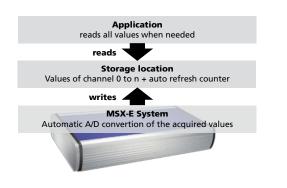
* Preliminary Product information

info@addi-data.com www.addi-data.com



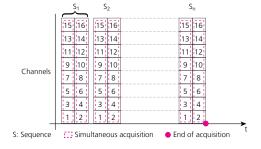
Auto-refresh mode

In auto-refresh mode, the measurement values are updated automatically after each acquisition. The acquisition is initialised once and the values of the channels are stored in the memory of the MSX-E Ethernet system. The client (e.g. PC, server, PLC, ...) reads the acquired values asynchronously to the acquisition through socket connection, SOAP or Modbus function. Thereby, the new value is read and the old values are overwritten. In addition to the measurement values, the auto-refresh counter can also be read, which allows to sort the measurement values chronologically. The auto-refresh mode can be combined with a hardware or a synchro trigger and also allows the automatic averaging of values.



Sequence mode

In sequence mode, a list of channels is acquired. Thereby, the single measurement rows are stored one after another. The client receives the acquired values asynchronously to the acquisition through a socket connection. In the sequence mode, the measurement values are read in chronological order, this means the oldest values are read first. The acquisition can be effected continuously, with or without delay or in combination with a hardware or synchro trigger.



Acquisition triggered through trigger or synchro input

Example: A measurement process is to be started through an external trigger impulse. For each trigger, 10 sequences are to be acquired. After the acquisition of the 10 sequences they are to be sent to the client.

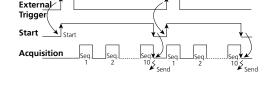
Reading data from a MSX-E system

MSX-E systems are multi-client capable, this means several clients (e. g. PC, server, PLC, ...) can read the measurement values of one MSX-E system at the same time. For this, each client establishes a socket connection to the data server of the MSX-E system (port 8989). As soon as the measurement values are available on the data server, the MSX-E system transfers them to the clients.

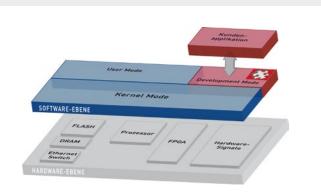
Onboard programming / stand-alone operation

Development mode

With the Development mode of the MSX-E systems you can customise your measurement, control and regulation applications to fit your requirements. The programs run directly on the MSX-E systems, which has two advantages: external PCs are relieved and you can process data freely according to your requirements. This helps you to improve the efficiency of your processes and to secure your investments.









The ConfigTools program allows an easy administration of the MSX-E systems. These are automatically detected in the network. ConfigTools consists of common and specific functions.

In addition, with ConfigTools, the complete configuration of a MSX-E system can be saved and transferred to another system of the same type (clone function).

ConfigTools is included in the delivery.

ConfigTools functions for MSX-E3311:

- Change of IP address
- Display of web interface
- Firmware update
- Save/load system configuration ٠
- Save/load channel configuration ٠
- Monitor for Inputs for strain gauges

Features

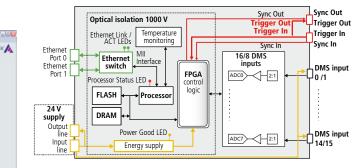


16/8 differential inputs for pres-

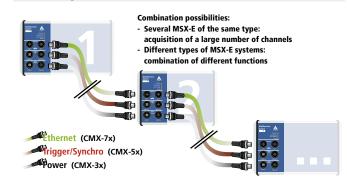
sure/force measurement. 8-pin M12 female connector,

2 inputs / connector

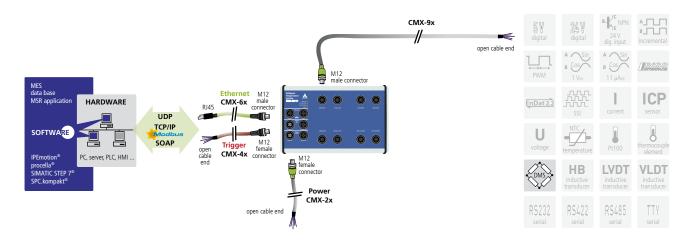
Simplified block diagram



Cascading



ADDI-DATA connection technology





Very easy use through the "ConfigTools" program; The MSX-E system is

automatically detected in the network.

ADDI-DATA

(brend)	Senability	Other	Channel	Senability	Other
Charrel 1	0.001	4	C Oane 9		1
Oarrel 2	1	0	🗋 Olevei 10		8
Ownel 3	1	8	Convert 11		8
Chanald	1	8	Currei 12		8
OarelS			Correct 12		8
Channel &		0	Channel 18		0
Ownel?		9	Charve 15		
Ourrela	1		Corvei 16		1.1

Example of monitor function for Inputs for strain gauges



Inputs for strain	gauges		
Number of inputs:	16 or 8 diff	ferential inputs	
	for strain g		
		er M12 connector	
Resolution:	24-bit		
Optical isolation:	1000 V		<u> </u>
Throughput per M12 connect	or: max. 788 H	Iz for 1 channel, i	max. 528 Hz for 2 channels
Voltage supply for the sensors:	101/ 50	•	
Voltage supply:	10 V, 50 m		rd calibration) 100 mA max.
Real Sampling frequency:	on 1 channel	on 2 channels	Sampling frequency (software-configurable)
	2.37 Hz	1.585 Hz	5 Hz
	4.73 Hz	3.154 Hz	10 Hz
	9.37 Hz	6.243 Hz	20 Hz
	18.9 Hz	12.6 Hz	40 Hz
	37.35 Hz	24.89 Hz	80 Hz
	73 Hz	48.65 Hz	160 Hz
	145 Hz	96.8 Hz	320 Hz
	276.4 Hz	184.26 Hz	640 Hz
	407.83 Hz	271.96 Hz	1 kHz
	788 Hz	525.48 Hz	2 kHz
Voltage supply, E	thernet, 1	Frigger, Sy	'nchro
The specifications for the volt Electromagnetic Compatibility			
System features			
Interface:	Ethernet a	cc. to specification	n IEEE802.3
Dimensions:		140 mm x 50 mm	1
Weight:	620 g		

weight:	620 g
Degree of protection:	IP 65
Current consumption:	150 mA \pm 10 % typ. in idle/power save mode
Operating temperature:	-40 °C to +85 °C
Connectors for sensors	
Analog inputs:	8 or 4 x 8-pin female connector, M12

Ordering information

MSX-E3311

Ethernet system for pressure/force measurement, 16/8 channels for strain gauges, 24-bit. Incl. technical description, software drivers and ConfigTools.

Versions

MSX-E3311-16: for 16 strain gauges MSX-E3311-8: for 8 strain gauges Opt.MSX-E-5Vss: Bridge supply voltage adjustment to 5 V (optional)

Connection cables

Voltage supply

- **CMX-2x:** Shielded cable, M12 5-pin female connector/open end, IP 65
- **CMX-3x:** For cascading, shielded cable, M12 5-pin female connector/male connector IP 65

Trigger/Synchro

CMX-4x: Shielded cable, M12 5-pin female connector/open end, IP 65
 CMX-5x: For cascading, shielded cable, M12 5-pin female connector/male connector IP 65

Ethernet

CMX-6x: CAT5E cable, M12 D-coded male connector/RJ45 connector **CMX-7x:** For cascading, CAT5E cable, 2 x M12 D-coded male connector

Options

 ${\bf S7}$ Modbus TCP Client Library for ${\bf S7:}$ Easy use of the Ethernet systems MSX-E with PLCs

MSX-E 5V-Trigger: Level change of the trigger inputs and outputs to 5 V **MX-Rail** (please specify when ordering!), **MX-Screw, PCMX-1x**

* Preliminary product information



Ethernet system for the acquisition of dynamic signals 8 SE/diff. inputs, 24-bit, simultaneous acquisition



MSX-E3601 / MSX-E3601-2

1

2 or 8 SE/diff. inputs, 24 V		
Simultaneous acquisition		
2 or 8 current sources for ICP sensors		
Onboard calibration		
24 V digital trigger input		

~ / \/

*Operating temperature





on request



see page 112



Features

- 24 V digital trigger input
- ARM[®]9 32-bit processor
- 64 MB onboard SDRAM for storing data
- Robust standardized metal housing
- Power Save Mode: Reduced power consumption when
 no acquisition runs

Analog inputs

- 8 SE or diff. inputs, 24-bit, AC/DC coupling
- One A/D converter per channel: simultaneous acquisition on all analog inputs
- Sampling rate up to 128 kHz
- Antialiasing filter
- BNC female connector:
- inner conductor for positive input
 outer conductor for negative input (diff) or GND (SE)
- Gain x1, x10, x100

Current sources

- 8 current sources for the direct connection of
- ICP[™] sensors (integrated circuit piezoelectric) • 4 mA typ., 24 V max.
- + mA (yp., 2+ V m
- Safety features
 Status LEDs for fast error diagnostics
- Optical isolation
- Input filters
- Overvoltage protection ± 40 V
- Internal temperature monitoring

Interfaces

- Fast 24 V trigger input
- Ethernet switch with 2 ports
- Synchronisation/trigger In/Out
- Line in for 24 V supply and cascading

Communication interfaces

- Web server (configuration and monitoring)
- Command server SOAP for transferring commands
- Data server (TCP/IP or UDP socket) for sending acquisition data
- Event server (TCP/IP socket) for sending system events (Diagnostics such as temperature, short-circuits ...)
- Command server Modbus TCP and Modbus (UDP) for sending commands

Synchronisation/time stamp

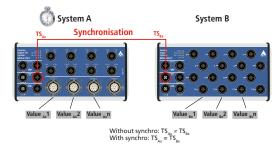
Time stamp

Cascadable,

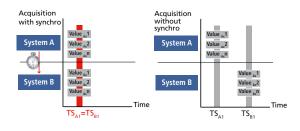
in the µs range

can be synchronised

Several MSX-E systems can be synchronised with one another in the μ s range through a synchro connection. This allows to start a synchronous data acquisition, to generate trigger events and to synchronise the time on several MSX-E systems. Furthermore, the systems have a time stamp that logs the point in time at which the data was acquired by the system.



The combination of synchronisation and time stamp (TS) allows the clear allocation of signals that were captured by several systems.



MSX-E3601-2



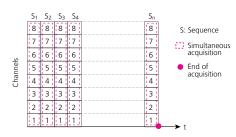


90



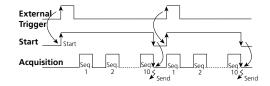
Sequence mode

In sequence mode, a list of channels is acquired. Thereby, the single measurement rows are stored one after another. The client receives the acquired values asynchronously to the acquisition through a socket connection. In the sequence mode, the measurement values are read in chronological order, this means the oldest values are read first. The acquisition can be effected continuously, with or without delay or in combination with a hardware or synchro trigger.



Acquisition triggered through trigger or synchro input

Example: A measurement process is to be started through an external trigger impulse. For each trigger, 10 sequences are to be acquired. After the acquisition of the 10 sequences they are to be sent to the client.





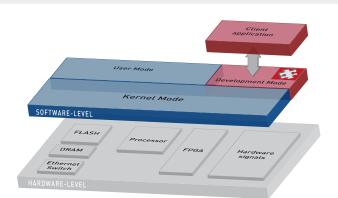


MSX-E systems are multi-client capable, this means several clients (e. g. PC, server, PLC, ...) can read the measurement values of one MSX-E system at the same time. For this, each client establishes a socket connection to the data server of the MSX-E system (port 8989). As soon as the measurement values are available on the data server, the MSX-E system transfers them to the clients.

Onboard programming / stand-alone operation

Development mode

With the Development mode of the MSX-E systems you can customise your measurement, control and regulation applications to fit your requirements. The programs run directly on the MSX-E systems, which has two advantages: external PCs are relieved and you can process data freely according to your requirements. This helps you to improve the efficiency of your processes and to secure your investments.



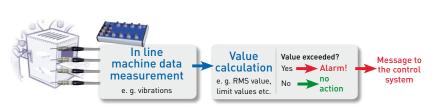
Anti-aliasing filter

Low-pass filters are used before or during digitising in order to remove all frequency components which are higher than the Nyquist frequency. This is to make sure that the digitised value or result does not contain any unwanted frequencies (aliasing frequencies). According to the Nyquist criterion, in order to obtain the full signal information, the sampling rate must be at least 2 x the signal band width.

Firmware and software adaptation

Because MSX-E systems are very flexible, the MSX-E3601 firmware can be easily extended. Thus, calculations such as RMS or limit values etc. can be integrated.

Using the Development Mode, it is possible to create self-sufficient intelligent nodes.





The ConfigTools program allows an easy administration of the MSX-E systems. These are automatically detected in the network. ConfigTools consists of common and specific functions.

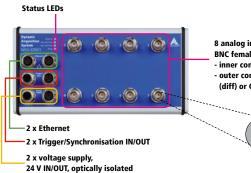
In addition, with ConfigTools, the complete configuration of a MSX-E system can be saved and transferred to another system of the same type (clone function).

ConfigTools is included in the delivery.

ConfigTools functions for MSX-E3601 / MSX-E3601-2:

- Change of IP address
- Display of web interface
- Firmware update
- Save/load system configuration ٠
- Save/load channel configuration ٠

Features



Simplified block diagram

- 8 analog inputs,
- BNC female connector:
- inner conductor for pos. input - outer conductor for neg. input
- (diff) or GND (SE)

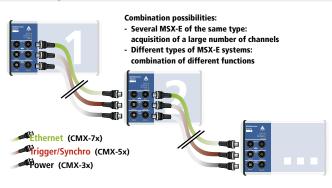


Very easy use through the "ConfigTools" program; The MSX-E system is automatically detected in the network.

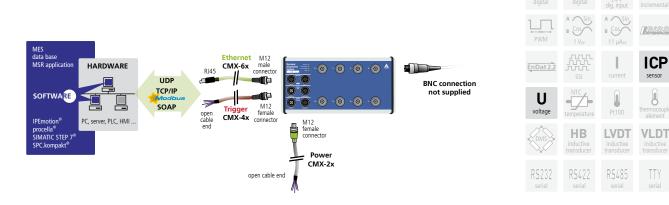


Sync Out Sync Out Optical isolation 1000 V Trigger Out Trigger In Frigger Out Trigger In Ethernet Link / ACT LEDs Temperature monitoring Sync In Sync In Ethernet Port 0 Ethernet Port 1 мл Ethernet switch Interfac -⊡∓v⊲_____ Input 0 FPGA ADC 0 ocessor Status LED • ontro logic FLASH Processor 24 V DRAM supply Output line Power Good LED ADC 7 Input 7 Input line Energy supply

Cascading



ADDI-DATA connection technology





B-K

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Specifications				
Analog inputs				
	o : I:	,.		
Number of inputs:		us acquisitio	n (IVISX-E360	1), 1 AD converte
	per channel			1 2)
Course line and	2, simultaneou			1-2)
Coupling:	DC, AC (softw			C 11.
Input type:	single-ended of	or differentia		onfigurable)
Input ranges	SE		Diff	
Gain x1:	± 10 V single-			differential
Gain x10:	± 1 V single-e			V differential
Gain x100:	± 0.1 V single		± 0.0	5 V differential
ADC-Typ:	Oversampled :			1.01.
	with linear ph	ase FIR antia	iliasing digita	l filter
Resolution:	24-bit			
Sampling rate f _s :	up to 128 kHz			
Selectable frequencies f _s :		100000.00	80000.00	66666.67
	64000.00	50000.00	40000.00	
	32000.00	25000.00	20000.00	16666.67
	16000.00	13333.33	12800.00	
	10000.00	8000.00	6666.67	6400.00
	6250.00	5000.00	4000.00	3333.33
	3200.00	3125.00	2500.00	2000.00
	1666.67	1600.00	1562.50	1280.00
	1000.00			
Oversampling:	8 x f _s			
Frequency accuracy:	± 50 ppm			
Input stage characteristics				
Input impedance:	1 MΩ // 300 p	oF typ., DC co	oupled	
AC cutoff frequency (-3dB):	0.48 Hz typ.			
Overvoltage protection:	Positive input		/-14 V, ± 100	
			continuous cu	irrent
	Negative inpu		, ± 100mA	
-11.		Max. d	continuous cu	irrent
Filter response				
Passband:	DC up to 0.45			
Passband ripple:	+/-0.01 dB ma	ax. DC up to	0.453 x f _s	
Bandwidth (-3dB):	0.49 x f _s typ.			
Stop band:	0.547 x f _s typ.			
Stop band attenuation:	100 dB min.			
Group delay:	37/f _s (µs) typ.			
Settling time (latency):	74/f _s (µs) com	plete settling	J	
Dynamic characteristics				
Signal-to-noise ration (SNR):	FSR, f _{in} =1kHz			
	≥ 95 ďB	Gain x		
	≥ 94 dB	Gain x		
	2 J4 UD		100	
	≥ 75 dB	Gain x	100	
Total Harmonic Distortion (THD):	≥ 75 dB		.100	
Total Harmonic Distortion (THD):	≥ 75 dB			
Total Harmonic Distortion (THD):	\geq 75 dB FSR, fin=1kHz	1	:1	
Total Harmonic Distortion (THD):	≥ 75 dB FSR, fin=1kHz ≥ 100 dB ≥ 100 dB	Gain x Gain x	:1 :10	
	≥ 75 dB FSR, fin=1kHz ≥ 100 dB ≥ 100 dB ≥ 90 dB	Gain x Gain x Gain x	:1 :10	
Total Harmonic Distortion (THD): Dynamic range:	$\geq 75 \text{ dB}$ FSR, fin=1kHz $\geq 100 \text{ dB}$ $\geq 100 \text{ dB}$ $\geq 90 \text{ dB}$ Shorted inputs	Gain x Gain x Gain x	1 10 100	
	≥ 75 dB FSR, fin=1kHz ≥ 100 dB ≥ 100 dB ≥ 90 dB	Gain x Gain x Gain x	1 10 100	

Crosstalk:	Between channels 0-1, 2-3, 4-5, 6-7, with gain x1 \geq 104 dB short input, $f_{in} = 100$ Hz			
	\geq 100 dB short input, f _{in} = 1 kHz			
	\geq 104 dB 50 Ω input, $f_{in} = 100$ Hz			
	\geq 100 dB 50 Ω input, $f_{in} = 1 \text{ kHz}$			
Phase mismatch:	Between channels 1-2, 3-4, 5-6, 7-8, with gain x1			
	± 0.001° fin < 100 Hz			
	$\pm 0.01^{\circ}$ f _{in} < 1 kHz			
	$\pm 0.1^{\circ}$ $f_{in} < 10 \text{ kHz}$			
A multitude a sum su				
Amplitude accuracy:	\pm 0.009 dB max. at f _{in} = 1 kHz sine signal,			
	Gain x1, x10, x100			
CMRR:	> 110 dB typ. at DC			
	$>$ 90 dB typ. at f_{in} < 1000 Hz			
Offset error:	± 90 μV after calibration at 25 °C			
Onboard DC calibration:	Software-configurable			
Calibration voltage:	5 V typ. Gain x1			
	900 mV typ. Gain x10			
	90 mV typ. Gain x100			
Temperature drift:	± 8 ppm/°C typ.			
Sensor supply voltage				
Number of channels:	2 (MSXE-3601-2) or 8 (MSX-E3601)			
	positive input			
Current source:	4 mA typ. to 24 V max.			
Coupling:	AC (positive input)			
	GND (negative input)			
Recording duration:	TBD max. at 128 kHz sampling rate on 8 channels			
Current sources				

Number:

2 or 8 constant current sources for the supply of the ICP[™] sensors, 4 mA typ., 24 V max.

Voltage supply, Ethernet, Trigger, Synchro

The specifications for the voltage supply, Ethernet, Trigger, Synchronisation and Electromagnetic Compatibility apply to all MSX-E systems. See page 31.

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

System features

Ethernet acc. to specification IEEE802.3
215 x 110 x 52 mm
860 g
IP 65
350 mA typ.
-40 °C to +85 °C
8 x BNC female connector (MSX-E3601) 2 x BNC female connector (MSX-E3601-2)

Ordering information

MSX-E3601 / MSX-E3601-2

Ethernet system for the acquisition of dynamic signals, 2 or 8 SE/diff. inputs, 24-bit, simultaneous acquisition. Incl. technical description, software drivers and ConfigTools.

Connection cables

Voltage supply

CMX-2x: Shielded cable, M12 5-pin female connector/open end, IP 65 CMX-3x: For cascading, shielded cable, M12 5-pin female connector/male connector IP 65

Trigger/Synchro

CMX-4x: Shielded cable, M12 5-pin female connector/open end, IP 65 CMX-5x: For cascading, shielded cable, M12 5-pin female connector/male connector IP 65

Ethernet

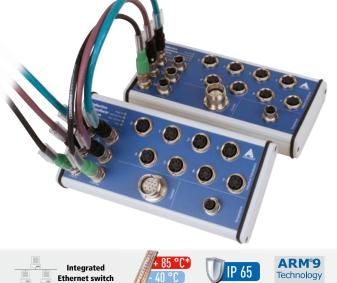
CMX-6x: CAT5E cable, M12 D-coded male connector/RJ45 connector CMX-7x: For cascading, CAT5E cable, 2 x M12 D-coded male connector **Options**

S7 Modbus TCP Client Library for S7: Easy use of the Ethernet systems MSX-E with PLCs

MSX-E 5V-Trigger: Level change of the trigger inputs and outputs to 5 V MX-Clip, MX-Rail (please specify when ordering!), MX-Screw, PCMX-1x



Ethernet system for length measurement, 24-bit, simultaneous, 8 transducers, counter and temperature inputs



MSX-E3711

Simultaneous acquisition of

up to 8 inductive displacement transducers

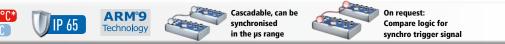
For half-bridge, LVDT, Mahr or Knaebel transducers

1 incremental counter input (32-bit)

1 input for temperature measurement (Pt100)

or for thermocouples (TC)

24 V digital trigger input



Operating temperature





on reauest



DatabaseConnect see page 112



More information at www.addi-data.com

Features

- ARM®9 32-bit processor
- Robust standardized metal housing
- Power Save Mode: Reduced power consumption when no acquisition runs

Transducer inputs

- 8 transducer inputs, 24-bit, 5-pin M18 female connector
- Half-bridge (HB), LVDT, Mahr-compatible, Knaebel
- Simultaneous acquisition
- Diagnostic option (short-circuit, line break)

Counter input

- 1 x 32-bit incremental counter input, max. 5 MHz Voltage supply of sensors with M23 female connector
- (24 V or 5 V)
- Single, double, quadruple edge analysis
- Compare logic

Temperature input

- 1 x RTD input, 16-bit, 5-pin M12 female connector
- +/- 0,01 °C resolution
- Thermocouple input, optional (MSX-E3711-TC)

Safety features

- Status LEDs for fast error diagnostics
- Optical isolation
- Input filters
- Overvoltage protection ± 40 V
- Internal temperature monitoring

Interfaces

- Fast 24 V trigger input
- Ethernet switch with 2 ports
- Synchronisation/trigger In/Out ٠
- Line in for 24 V supply and cascading

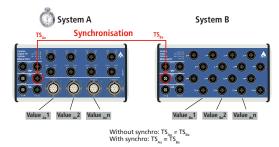
Communication interfaces

- Web server (configuration and monitoring)
- Command server SOAP for transferring commands
- Data server (TCP/IP or UDP socket) for sending acquisition data
- Event server (TCP/IP socket) for sending system events (Diagnostics such as temperature, short-circuits ...)
- Command server Modbus TCP and Modbus (UDP) for sending commands

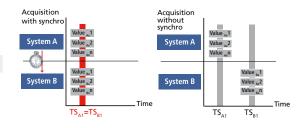


Time stamp

Several MSX-E systems can be synchronised with one another in the $\boldsymbol{\mu}\boldsymbol{s}$ range through a synchro connection. This allows to start a synchronous data acquisition, to generate trigger events and to synchronise the time on several MSX-E systems. Furthermore, the systems have a time stamp that logs the point in time at which the data was acquired by the system.



The combination of synchronisation and time stamp (TS) allows the clear allocation of signals that were captured by several systems.

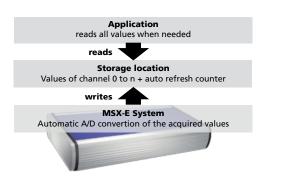






Auto-refresh mode

In the auto-refresh mode, the measurement values are updated automatically after each acquisition. The acquisition is initialised once and the values of the channels are stored in the memory of the MSX-E Ethernet system. The client (e.g. PC, server, PLC, ...) reads the acquired values asynchronously to the acquisition through socket connection, SOAP or Modbus function. Thereby, the new value is read and the old values are overwritten. In addition to the measurement values, the auto-refresh counter can also be read, which allows to sort the measurement values chronologically. The auto-refresh mode can be combined with a hardware or a synchro trigger and also allows the automatic averaging of values.



Pt100

Cn

S: Sequence

Simultaneous

Sequence mode

In the sequence mode, a list of channels is acquired. Thereby, the single measurement rows are stored one after another. The client receives the acquired values asynchronously to the acquisition through a socket connection. In the sequence mode, the measurement values are read in chronological order, this means the oldest values are read first. The acquisition can be effected continuously, with or without delay or in combination with a hardware or synchro trigger.

Compare logic

With the compare logic of the incremental counter, a synchro-trigger signal can be generated in order to latch the counter value, the transducers and the temperature input as soon as the counter value is equal to the compare value.

Thus all the inputs of the MSX-E3711 system can be acquired simultaneously.

With the additional "Modulo-Mode" (Modulo Compare), a trigger can also be generated at the n value of the compare value.

Thus it is possible, e. g. when using an encoder with 3,600 steps / revolution to obtain each degree of a measurement value (Modulo Compare = 10). The thus generated synchro-trigger can also be used for data acquisition on further MSX-E systems.

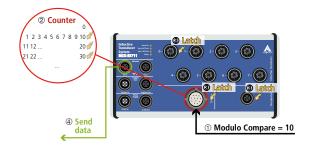
T8 Τ8 Τ8 acquisition End of acquisition T7 T7 Τ7 Channels T6 Τ6 Τ6 Tx: Transducer T5 T5 T5 Cn: Counter T4 Τ4 Τ4 Pt100: Thermo-T3 T3 T3 couple T2 T2 T2 Τ1 T1 T1

Pt100

Cn

Pt100

Cn

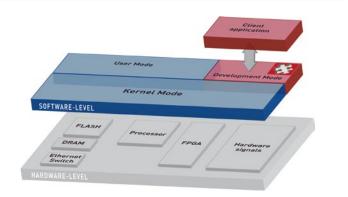


Onboard programming / stand-alone operation

Development mode

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With the Development mode of the MSX-E systems you can customise your measurement, control and regulation applications to fit your requirements. The programs run directly on the MSX-E systems, which has two advantages: external PCs are relieved and you can process data freely according to your requirements. This helps you to improve the efficiency of your processes and to secure your investments.





The ConfigTools program allows an easy administration of the MSX-E systems. These are recognised automatically in the network. ConfigTools consists of common and specific functions.

In addition, with ConfigTools, the complete configuration of a MSX-E system can be saved and transferred to another system of the same type (clone function).

ConfigTools is included in the delivery.

ConfigTools functions for MSX-E3711:

- Change of IP address
- Display of web interface •
- Firmware update ٠
- Save/load system configuration •
- Save/load channel configuration ٠
- Calibration of transducers
- Data base of transducers
- Monitoring of transducers
- Diagnostics of transducers

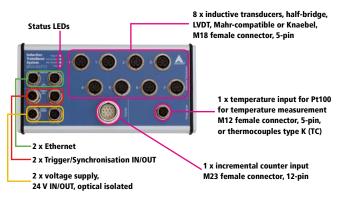
Very easy use through the "ConfigTools" program; The MSX-E system is automatically detected in the network.



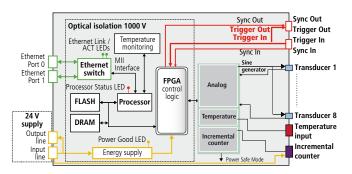


Monitor function example: Check of the analog inputs.

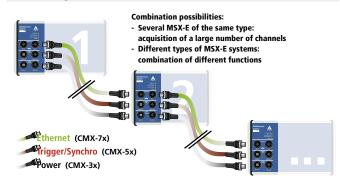
Features



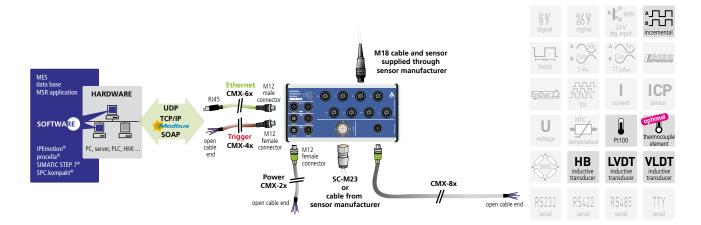
Simplified block diagram



Cascading



ADDI-DATA connection technology





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Specifications

Connection of inductive transducers

connection of mu	uctive transu	ucers	
Inputs for inductive trans	ducers		
Channel features:			
Number:	8 x ADC (not multiplexed)		
Input type:	Single-ended		
Coupling:	DC		
Resolution:	24-bit		
Sampling rate <i>f</i> _s :	On 8 channels	At primary frequency $f_{ ho}$ of 5 kHz 7.69 kHz	
	$f_{\rm s} = f_{\rm P}$	10 kHz	
		12.5 kHz	
		20 kHz	
		50 kHz	
Example with TESA GT21:	$f_{\rm s} = f_{\rm P} = 12.5 \text{ kHZ}$	on all 8 channels	
Input level			
Input impedance:	2 kΩ	software-programmable	
	10 kΩ		
	100 kΩ		
	10 MΩ		
Transducer accuracy:	± 61 nm (Tesa GT2	21)	
Sensor supply (Sine gene			
Туре:	Sine differential (1	80° phase-shift)	
Coupling:	AC		
Programmed signals:			
Output frequency f_{P}		ng on the transducer	
(primary frequency)	(50 kHz Knaebel)		
Output impedance:	< 0.1 Ω typ.		
	$>$ 30 k Ω typ. in sh		
Short-circuit current:	0.7 A typ. at 25 °C	with thermal protection	

Nominal voltage: Max. input frequency:	24 V _{DC} 1 MHz at nominal voltage			
Input impedance:	1 M Ω typ.			
Logic input levels:	i mae typ.			
UH (max.)	30 V typ.			
UH (min)	18 V typical (on request)			
UL (max.)	16 V typical (on request)			
UL (min)	0 V typical			
Temperature meas	surement			
Number of inputs:	1			
Туре:	RTD Pt100 or TC type K (optional)			
Connection:	4-wire			
Temperature range:	-200 to 850 °C			
Resolution:	± 0.01 °C			
System features				
Interface:	Ethernet acc. to specification IEEE802.3			
Dimensions (mm):	215 x 110 x 54			
Weight:	760 g			
Degree of protection:	IP 65			
Operating temperature:	- 40 to + 85°C			
Current consumption at 24 V:	400 mA			
Voltage Supply				
24 VDC IN	1 x 5-pin M12 male connector			
24 VDC OUT	1 x 5-pin M12 female connector			
Connectors for sensors				
For inductive transducers:	8 x 5-pin M18 female connector			
For temperature sensors:	1 x 5-pin M12 female connector			
	1-, 2-, 4-wire Pt100			
For the counter function:	1 x 12-pin M23 female connector			

Counter

Number of counter inputs:	1
Input type:	Differential inputs or TTL
Differential inputs:	Comply with the EIA standards RS422A
Common mode range:	+12 V / - 7 V
Input sensitivity:	± 200 mV
Input hysteresis:	50 mV typ.
Input impedance:	12 k Ω min.
Max. input frequency:	5 MHz at nominal voltage
"Open Circuit Fail Safe	
Receiver Design"	"1" = inputs open
ESD protection:	Up to ± 15 kV
Voltage supply of the encoder:	5 or 24 V/500 mA max.
24 V version	
This constant is destanted for the s	

This version is designed for the connection of 24 V encoders. Only 24 V signals can be connected to the inputs.

Ordering information

MSX-E3711

Ethernet system for length measurement, 24-bit, simultaneous, 8 transducers, counter and temperature input. Incl. technical description, software drivers and ConfigTools.

Versions

MSX-E3711-HB: MSX-E3711-LVDT: MSX-E3711-M: MSX-E3711-K: MSX-E3711-HB-24V:	for 8 HB inductive transducers, 5 V counter input for 8 LVDT inductive transducers, 5 V counter input for 8 Mahr-comp. transducers, 5 V counter input for 8 Knaebel transducers, 5 V counter input for 8 HB inductive transducers, 24 V counter input
MSX-E3711-LVDT-24V:	for 8 LVDT inductive transducers,
	24 V counter input
MSX-E3711-TC:	Type K thermocouple in place of the Pt100 input, other types on request

Connection cables

Voltage supply

CMX-2x: Shielded cable, M12 5-pin female connector/open end, IP 65
 CMX-3x: For cascading, shielded cable, M12 5-pin female connector/male connector IP 65

Trigger/Synchro

CMX-4x: Shielded cable, M12 5-pin female connector/open end, IP 65
 CMX-5x: For cascading, shielded cable, M12 5-pin female connector/male connector IP 65

Ethernet

CMX-6x: CAT5E cable, M12 D-coded male connector/RJ45 connector **CMX-7x:** For cascading: CAT5E cable, 2 x M12 D-coded male connector

Options

S7 Modbus TCP Client Library for S7: Easy use of the Ethernet systems MSX-E with PLCs

MSX-E 5V-Trigger: Level change of the trigger inputs and outputs to 5 V MX-Clip, MX-Rail (Please specify when ordering!),MX-Screw, PCMX-1x





Timer function for

synchro trigger signal



Operating temperature



- ARM®9 32-bit processor
- Robust standardized metal housing
- Power Save Mode: Reduced power consumption when no acquisition runs

Inputs for transducers

- 4, 8 or 16 inputs for transducers, 24-bit, 5-pin M18 female connector
- Half-bridge (HB), LVDT, Mahr compatible, Knaebel
- Diagnostics (short-circuits, line break)
- 16-bit accuracy, example of a measurement: Typ TESA GT21, range \pm 2 mm (\triangle 4 mm),

<u>4 mm</u> _ ± 61 nm = 0.061 μm 216

Safety features

- Status LEDs for fast error diagnostics
- Optical isolation
- Input filters
- Overvoltage protection ± 40 V •
- Internal temperature monitoring

Interfaces

- Fast 24 V trigger input
- Ethernet switch with 2 ports •
- Synchronisation/trigger In/Out •
- Line in for 24 V supply and cascading

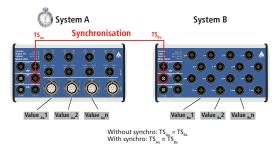
Communication interfaces

- Web server (configuration and monitoring)
- Command server SOAP for transferring commands
- Data server (TCP/IP or UDP socket) for sending acquisition data
- Event server (TCP/IP socket) for sending system events • (Diagnostics such as temperature, short-circuits ...)
- Command server Modbus TCP and Modbus (UDP) for sending commands

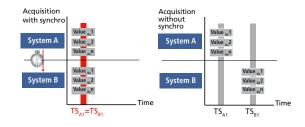
Synchronisation/time stamp

Time stamp

Several MSX-E systems can be synchronised with one another in the $\boldsymbol{\mu}s$ range through a synchro connection. This allows to start a synchronous data acquisition, to generate trigger events and to synchronise the time on several MSX-E systems. Furthermore, the systems have a time stamp that logs the point in time at which the data was acquired by the system.



The combination of synchronisation and time stamp (TS) allows the clear allocation of signals that were captured by several systems.





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DatabaseConnect

see page 112

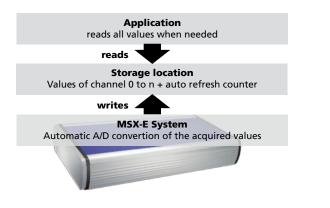






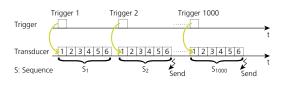
Auto-refresh mode

In the auto-refresh mode, the measurement values are updated automatically after each acquisition. The acquisition is initialised once and the values of the channels are stored in the memory of the MSX-E Ethernet system. The client (e.g. PC, server, PLC, ...) reads the acquired values asynchronously to the acquisition through socket connection, SOAP or Modbus function. Thereby, the new value is read and the old values are overwritten. In addition to the measurement values, the auto-refresh counter can also be read, which allows to sort the measurement values chronologically. The auto-refresh mode can be combined with a hardware or a synchro trigger and also allows the automatic averaging of values.



Sequence Mode

In the sequence mode, a list of channels is acquired. Thereby, the single measurement rows are stored one after another. The client receives the acquired values asynchronously to the acquisition through a socket connection. In the sequence mode, the measurement values are read in chronological order, this means the oldest values are read first. The acquisition can be effected continuously, with or without delay or in combination with a hardware or synchro trigger. Example: Sequence acquisition of 6 channels, 1 Trigger for each sequence sending data after 2 sequences – a total of 1000 sequences

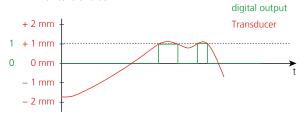


Digtal output with Compare logic

The MSX-3701-x-4 system can optionally be equipped with a digital 24 V output with Compare logic (OPT. MSX-E Dig. Out). This output can be set either manually or through a transducer.

This allows easy threshold value monitoring, e.g. probing a workpiece and then automatically sending an message to a PLC.

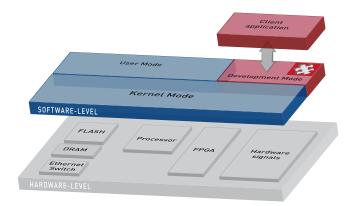
Example: Transducer channel 0, TESA GT21, range ± 2 mm, threshold value: + 1 mm



Onboard programming / stand-alone operation

Development mode

With the Development mode of the MSX-E systems you can customise your measurement, control and regulation applications to fit your requirements. The programs run directly on the MSX-E systems, which has two advantages: external PCs are relieved and you can process data freely according to your requirements. This helps you to improve the efficiency of your processes and to secure your investments.



* Preliminary product information



Phone: +49 7229 1847-0 info@addi-data.com Fax: +49 7229 1847-222 www.addi-data.com

The **ConfigTools** program allows an easy administration of the MSX-E systems. These are automatically detected in the network. **ConfigTools** consists of common and specific functions.

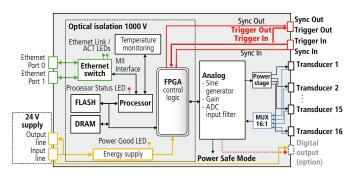
In addition, with **ConfigTools**, the complete configuration of a MSX-E system can be saved and transferred to another system of the same type (clone function).

ConfigTools is included in the delivery.

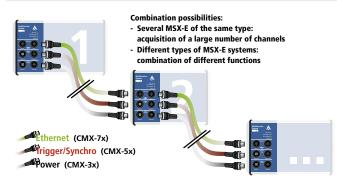
ConfigTools functions for MSX-E3701 / MSX-E3701-x / MSX-E3700:

- Change of IP address
- Display of web interface
- Firmware update
- Save/load system configuration
- Save/load channel configuration
- Transducer calibration
- Transducer database
- Transducer monitoring
- Transducer diagnostics

Simplified block diagram



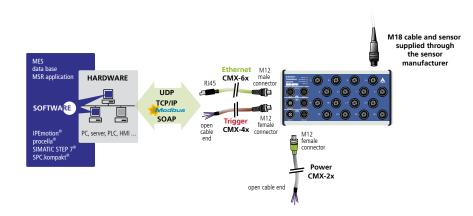
Cascading

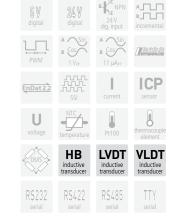


Features



ADDI-DATA connection technology







Specifications

	Inputs	for inductive	transducers
--	--------	---------------	-------------

Channel features					
Number:	-4/-8/-16/ multiplexed				
Input type:	single-ended				
Coupling:	DC				
Resolution:	24-bit				
Sampling frequency <i>f</i> _s :	On 1 channel At primary frequency f_p of 5 kHz				
	7.69 kHz $f_{\rm s}$ = $f_{ ho}$ 10 kHz 12.5 kHz 20 kHz 50 kHz				
	Ab n ≥ 2 channels $f_p =$ primary frequency				
	$f_{\rm S} = \frac{f_{\rm P}}{{}_{\rm SP \times n}} \qquad \qquad$				
Example with TESA GT21:	On 1 channel $f_s = f_p$ = 12.5 kHz				
	From $n \ge 2$ channels $f_s = \frac{12.5 \text{ kHz}}{5 \times 4} = 625 \text{ Hz}$ for 4 channels				
	$f_{\rm s} = \frac{12.5 \text{ kHz}}{5 \times 8} = 312.5 \text{ Hz}$ for 8 channels				
	$f_{\rm s} = \frac{12.5 \text{ Hz}}{5 \times 16} = \frac{156.25 \text{ Hz}}{\text{channels}}$				
Input level					
Input impedance:	2 k Ω software-programmable				
	10 kΩ				
	100 kΩ				
	10 MO				

	100 kΩ
	10 MΩ
Sensor supply (sine ge	nerator)
Туре:	Sine differential (180° phase-shift)
Coupling:	AC
Programmed signals:	
output frequency f_{P}	2-20 kHz depending on the transducer
(primary frequency)	(50 kHz Knaebel)
Output impedance:	< 0.1 Ω typ.
	$>$ 30 k Ω typ. in shutdown mode
Short-circuit current:	0.7 A typ. at 25 °C with thermal protection

Voltage supply

5 1 5		
Nominal voltage:	24 V	
Voltage supply:	18-30 V	
Optical isolation:	1000 V	
Current consumption at 24 V:	90 mA	typ. in power safe mode / idle
	120 mA	Power on
	150 mA	DAC init, sine on, Buffer off
	200 mA	typ. without load (transducers) at \pm 9 V power (Buffer on)
	320 mA	typ. with 16 Solartron AX1S transducers
		at \pm 7 V power, 5 kHz and 3 V _{ms}
	330 mA	typ. with 8 Knaebel IET0200 transducers
		at 5 V power, 50 kHz and 1V

Reverse voltage protection

Digital output (option for MSX-E3701-x-4)					
Number of outputs: 1, M12 female connector					
Optical isolation:	1000 V through opto-couplers				
Output type:	High Side, load to ground acc. to IEC 1131-2				
Nominal voltage:	24 V				
Voltage supply:	18 V-30 V				
Output current: 0.8 A					
Short-circuit current / output:	0.8 A max.				
RDS ON resistance:	1 mΩ max.				
Switch-on time:	21 µs				
	typ. RL = 270 Ω				
Switch-off time:	11 μs				
	typ. RL = 270 Ω				
Overtemperature (shutdown):					
Temperature hysteresis:	10°C typ. (output driver)				

Ethernet					
Number of ports:	2				
Cable length:	150 m max. at CAT5E UTP				
Bandwidth:	10 Mbps	auto-negotiation			
bulumuti	100 Mbps	auto-negotiation			
Protocol:	10Base-T IEEE802.3 compliant				
	100Base-TX IEEE802.3 compliant				
Optical isolation:	1000 V				
MAC address:	00:0F:6C:##:	##:##, unique for each device			
		•			
Triggor					
Trigger					
Number of inputs:	1 trigger ir				
Number of outputs:	1 trigger o				
Filters/protective circuit:	Low-pass/transorb diode				
Optical isolation:	1000 V				
Nominal voltage:	24 V external				
Input voltage:	0 to 30 V				
Input current:	11 mA at 24 VDC, typical				
Input frequency (max.):	2 MHz at 24 V				
Connector, common with					
Trigger input:		nale connector M12			
Trigger output:	1 x 5-pin f	emale connector M12			
Synchro					
Number of inputs:	1				
Number of outputs:	1				
Max. cable length:	20 m				
Optical isolation:	1000 V				
Signal type:	R\$485				
Connector, common with	Trigger				
Trigger input:		nale connector M12			
Trigger output:	1 x 5-pin female connector M12				
	•				

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

System features

Ethernet acc. to specification IEEE802.3		
MSX-E3700-16	215 x 110 x 39 mm	
MSX-E3700-4/8	154 x 110 x 39 mm	
MSX-E3701-16	215 x 110 x 50 mm	
MSX-E3701-4/8	154 x 110 x 50 mm	
MSX-E370x-16:	760 g	
MSX-E370x-8:	560 g	
MSX-E370x-4:	530 g	
MSX-E3701-4/-8/-16:	IP 65	
MSX-E3700-4/-8/-16:	IP 40	
MSX-E370x:	-40 °C to + 85°C	
	MSX-E3700-16 MSX-E3700-4/8 MSX-E3701-16 MSX-E3701-4/8 MSX-E370x-16: MSX-E370x-4: MSX-E370x-4: MSX-E3701-4/-8/-16: MSX-E3700-4/-8/-16:	

MSX-E3701 interface connectors

Ethernet:	2 x 4-pin M12 female connector, D-coded for port 0 and port 1
Trigger/Synchro IN:	1 x 5-pin male connector M12
Trigger/Synchro OUT:	1 x 5-pin female connector M12
Voltage supply	
24 VDC IN:	1 x 5-pin male connector M12
24 VDC OUT:	1 x 5-pin female connector M12

MSX-E3700 interface connectors

Ethernet:	RJ45 for Port 0 and 1
External trigger:	1 x 3-pin binder, 3.81 mm grid
Synchro signal:	1x 3-pin binder, 3.81 mm grid
Voltage supply	
24 VDC:	3-pin binder, 5.08 mm grid

Connectors for connecting inductive transducers

	Jan San Jan Sa	
MSX-E370x-4:	4 x 5-pin M18 female connector	
MSX-E370x-8:	8 x 5-pin M18 female connector	
MSX-E370x-16:	16 x 5-pin M18 female connector	



Versions	Temperature range	Number of	Type of transducer	Digital output 24 V	Degrees of protection
	- 40 °C to + 85 °C	transducers		(option)	
MSX-E3701-HB-16		16			MSX-E3701: Degree of protection IP 65
MSX-E3701-HB-8	1	8	Half-Bridge		Protection against a water jet directed at the housing from any direction. Protection against the penetration of dust.
MSX-E3701-HB-4		4		1	Total protection against contact (dust-proof).
MSX-E3701-LVDT-16		16			
MSX-E3701-LVDT-8	1	8	LVDT		As the set
MSX-E3701-LVDT-4		4		1	
MSX-E3701-K-8	1		Knaebel		
MSX-E3701-M-8		8	Maha ana shila		
MSX-E3701-M-4		4	Mahr compatible	1	
MSX-E3700-HB-16		16			MSX-E3700: Degree of protection IP 40
MSX-E3700-HB-8	 ✓ 	8			Protection against the penetration of foreign bodies with a diameter greater than 1 mm.
MSX-E3700-HB-4		4		2	
MSX-E3700-LVDT-16		16			
MSX-E3700-LVDT-8	1	8	LVDT		
MSX-E3700-LVDT-4		4			

Ordering information

MSX-E3701 / MSX-E3701-EXT / MSX-E3700

Ethernet system for length measurement, 24-bit, 16/8/4 inductive displacement transducers, LVDT, half-bridge, Mahr-compatible, Knaebel. Incl. technical description, software drivers and ConfigTools.

MSX-E3701: IP 65, s MSX-E3701-HB-16: MSX-E3701-LVDT-16: MSX-E3701-HB-8: MSX-E3701-K-8: MSX-E3701-LVDT-8: MSX-E3701-HB-4: MSX-E3701-HB-4: MSX-E3701-LVDT-4: MSX-E3701-M-4:	itandard system For 16 HB inductive displacement transducers For 16 LVDT inductive displacement transducers For 8 HB inductive displacement transducers For 8 Knaebel induct. displacement transducers For 8 LVDT inductive displacement transducers For 4 HB inductive displacement transducers for 8 Mahr-compatible displacement transducers For 4 LVDT inductive displacement transducers for 4 Mahr-compatible displacement transducers for 4 Mahr-compatible displacement transducers	Options MSX-E 5V-Trigger: Level change of the trigger inputs and outputs to 5 V Opt. MSX-E Dig. Out: additional dig. ouptut with compare logic for transducer 0 (only available for MSX-E3701-x-4)	
MSX-E3700 (degree Incl. standard binders MSX-E3700-HB-16: MSX-E3700-LVDT-16: MSX-E3700-HB-8: MSX-E3700-LVDT-8: MSX-E3700-HB-4: MSX-E3700-LVDT-4:		Binders i Power Su SMX-10: SMX-11: SMX-12: Trigger SMX-20:	for MSX-E3700: pply Standard 3-pin binder, 5.08 mm grid, screw connector (included in delivery) 3-pin binder, 5.08 mm grid, 2-row screw connector 3-pin binder, 5.08 mm grid, 2-row spring-cage connector Standard 3-pin binder, 5.08 mm grid
Options for MSX-E3701 and MSX-E3700 S7 Modbus TCP Client Library for S7: Easy use of the Ethernet systems MSX-E with PLCs Connection cables Voltage supply CMX-2x: Shielded cable, M12 5-pin female connector/open end, IP 65 CMX-3x: For cascading, shielded cable, M12 5-pin female connector/male connector IP 65		Trigger/Synchro CMX-4x: Shielded cable, M12 5-pin female connector/open end, IP 65 CMX-5x: For cascading, shielded cable, M12 5-pin female connector/male connector IP 65 Ethernet CMX-6x: CAT5E cable, M12 D-coded male connector/RJ45 connector CMX-7x: For cascading: CAT5E cable, 2 x M12 D-coded male connector MSX-E 5V-Trigger: Level change of the trigger inputs and outputs to 5 V MX-Clip, MX-Rail (Please specify when ordering!),MX-Screw, PCMX-1x	



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Applications

Practical Examples



Machinerv

Precise and error-free diameter detection of gear wheels

Challenge

A manual test station shall be replaced by a modern and easy-to-use measuring system. The goal is to improve the accuracy of the measurement and to avoid errors which occur when measurement values are entered manually.

Solution

The diameter between the gear teeth shall be measured. Therefore the gear wheel is put on a measurement table with a ball for stopping. On the opposite side, a spring-loaded ball and an inductive measurement sensor are installed. The diameter between the fixed ball and the sensor is detected by the Ethernet length measurement system MSX-E3701. Then the measured values are calculated through an integrated logic and classified in 4 predefined categories (tolerance range). The measurement result is displayed with an LED on the digital Ethernet system MSX-E1516.

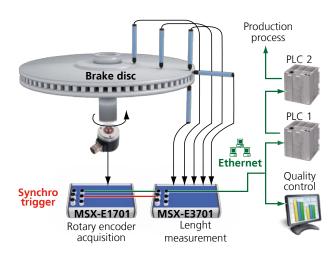
Consistent data for parameter corrections and precise rework of brake discs

Challenge

A manufacturer of car parts wants to test the surface of his produced brake discs as exactly as possible for roundness, radius and surface quality. In order to allow precise rework and corrections of the process parameters, the measurement device must be able to transfer a consistent data set of the position value and the measured value to the control unit.

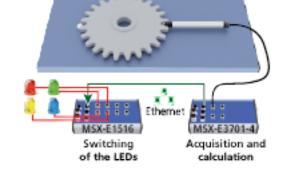
Solution

For this application, two intelligent Ethernet systems are used: The MSX-E1701 counter system for the position acquisition and the MSX-E3701 length measurement system for the detection of the measurement values through the connected displacement transducers. For each measurement point, the MSX-E1701 system triggers the MSX-E3701 system angle-dependently. For a precise matching of position and measurement values at each measurement point, both systems are synchronised and have a time stamp. The acquired data is then transferred through Ethernet to a PC or a PLC for evaluation and regulation purposes. Exceeded tolerance values are forwarded to the super-ordinate machine in order to adjust process parameters or to effect corrections on the measured brake disc.









Ethernet system for length measurement, 24-bit 16 inductive transducers, LVDT, half-bridge





MSX-E3701-DI0

Acquisition of 16 inductive transducers

For half-bridge and LVDT transducers

24 V digital trigger input

32 digital I/O, 24 V



Operating temperature



Integrated

Ethernet

switch





on request



DatabaseConnect see page 112



* Preliminary Product information

Features

- ARM[®]9 32-bit processor
- Robust standardized metal housing
- Power Save Mode: Reduced power consumption when
 no acquisition runs

Inputs for transducers

- 16 inputs for transducers, 24-bit, 5-pin M18 female connector
- Half-bridge (HB), LVDT
- Diagnostics at short-circuits or line break
- 16-bit accuracy

Transducer precision: example of a measurement Typ TESA GT21, range \pm 2 mm (Δ 4 mm), 16-bit accuracy

Digital I/O

2¹⁶

- 16 inputs for transducers, 24-bit, 5-pin M18 female connector
- 32 digital I/O, 24 V:
- 16 opt. isolated inputs, 24 V, optional filter 16 opt. isolated outputs, 11 V to 36 V, output current per channel 150 mA

Safety features

- Status LEDs for fast error diagnostics
- Optical isolation Input filters
- Overvoltage protection ± 40 V
- Internal temperature monitoring

Interfaces

- Fast 24 V trigger input
- Ethernet switch with 2 ports
- Synchronisation/trigger In/Out
- Line in for 24 V supply and cascading

Communication interfaces

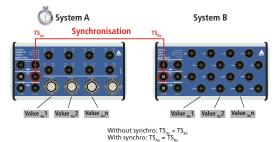
- Web server (configuration and monitoring)
- Command server SOAP for transferring commands
- Data server (TCP/IP or UDP socket) for sending acquisition data
- Event server (TCP/IP socket) for sending system events (Diagnostics such as temperature, short-circuits ...)
- Command server Modbus TCP and Modbus (UDP) for sending commands

Synchronisation/time stamp

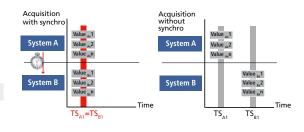
Time stamp

by several systems.

Several MSX-E systems can be synchronised with one another in the μ s range through a synchro connection. This allows to start a synchronous data acquisition, to generate trigger events and to synchronise the time on several MSX-E systems. Furthermore, the systems have a time stamp that logs the point in time at which the data was acquired by the system.



The combination of synchronisation and time stamp (TS) allows the clear allocation of signals that were captured

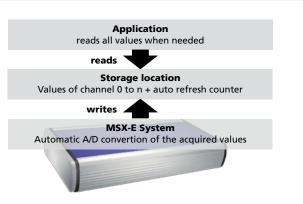




info@addi-data.com www.addi-data.com

Auto-refresh mode

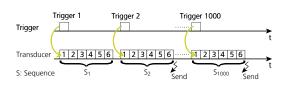
In the auto-refresh mode, the measurement values are updated automatically after each acquisition. The acquisition is initialised once and the values of the channels are stored in the memory of the MSX-E Ethernet system. The client (e.g. PC, server, PLC, ...) reads the acquired values asynchronously to the acquisition through socket connection, SOAP or Modbus function. Thereby, the new value is read and the old values are overwritten. In addition to the measurement values, the auto-refresh counter can also be read, which allows to sort the measurement values chronologically. The auto-refresh mode can be combined with a hardware or a synchro trigger and also allows the automatic averaging of values.



lew

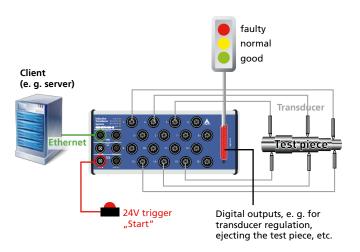
Sequence Mode

In the sequence mode, a list of channels is acquired. Thereby, the single measurement rows are stored one after another. The client receives the acquired values asynchronously to the acquisition through a socket connection. In the sequence mode, the measurement values are read in chronological order, this means the oldest values are read first. The acquisition can be effected continuously, with or without delay or in combination with a hardware or synchro trigger. Example: Sequence acquisition of 6 channels, 1 trigger per sequence Send data after 2 sequences – 1000 sequences in total



Digital I/O

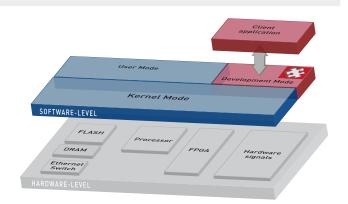
In addition to the transducer channels, the MSX-3701-DIO system has 32 digital 24 V I/O channels (16 inputs, 16 outputs). The system is therefore very flexible and can manage complete measurement and test applications. Example: Test bench for cylindrical parts, probing the workpieces, automatic regulation of the transducers and visualisation of the results via LED trafic light. The measurement data is at the same time stored in a database.



Onboard programming / stand-alone operation

Development mode

With the Development mode of the MSX-E systems you can customise your measurement, control and regulation applications to fit your requirements. The programs run directly on the MSX-E systems, which has two advantages: external PCs are relieved and you can process data freely according to your requirements. This helps you to improve the efficiency of your processes and to secure your investments.



* Preliminary product information





32 digital I/O, 24 V

Degree of protection IP 65

Connection of up to 16 transducers, half-bridge, LVDT ,

5-pin M18 female connector

ConfigTools

The **ConfigTools** program allows an easy administration of the MSX-E systems. These are automatically detected in the network. **ConfigTools** consists of common and specific functions.

In addition, with **ConfigTools**, the complete configuration of a MSX-E system can be saved and transferred to another system of the same type (clone function).

ConfigTools is included in the delivery.

ConfigTools functions for MSX-E3701-DIO:

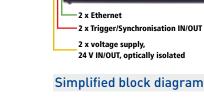
- Change of IP address
- Display of web interface
- Firmware update
- Save/load system configuration
- Save/load channel configuration
- Transducer calibration
- Transducer database
- Transducer monitoring
- Transducer diagnostics

Very easy use through the "ConfigTools" program; The MSX-E system is automatically detected in the network.



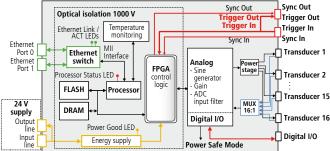


... and can then be calibrated.

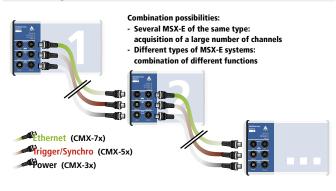


Features

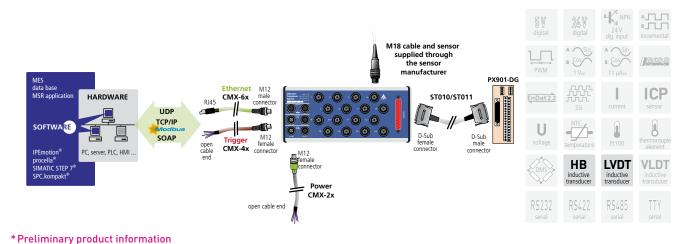
Status LEDs



Cascading



ADDI-DATA connection technology





106 Phone: +49 7229 1847-0 Fax: +49 7229 1847-222

info@addi-data.com 2 www.addi-data.com

New!*

Specifications*

Specifications		
Inputs for inductiv	e transduce	rs
Channel features		
Number:	16 multiplexed	
Input type:	Single-Ended	
Coupling:	DC	
Resolution:	24-bit	
Sampling frequency f_{s} :	on 1 channel	at primary frequency $f_{\rm P}$ of
Sampling nequency J_s .		5 kHz
		7.69 kHz
	$f_{s} = f_{p}$	10 kHz
	JS JP	12.5 kHz
		20 kHz
		50 kHz
	From $n > 2$ channel	els $f_p = primary frequency$
		$SP = settling period (5 \le SP \le 255)$
	$f_{\rm s} = \frac{f_{\rm P}}{{\rm sP} \times {\rm p}}$	<i>fs</i> concerns here all n channels
	Js – SP x n	Js concerns here an in channels
Example with TECA CT21:	On one channel	$f_{\rm s} = f_{\rm p}$ = 12.5 kHz
Example with TESA GT21:		
	From n > 2 charge	els $f_s = \frac{12.5 \text{ kHz}}{5 \times 4} = 625 \text{ Hz}$ for 4 channels
		$f_{\rm s} = \frac{12.5 \text{ kHz}}{5 \times 8} = 312.5 \text{ Hz for 8 channels}$
		$J_s = \frac{1}{5 \times 8} = 512.5$ Hz for 8 challes
		$f_{\rm s} = \frac{12.5 \text{ kHz}}{5 \times 16} = 156.25 \text{ Hz}$ for 16 channels
		$J_s = \frac{150.25 \text{ Hz}}{5 \times 16} = 150.25 \text{ Hz}$ for 10 challes
Innut loval		
Input level Input impedance:	2 kΩ software-con	figurable
input impedance.	$10 \text{ k}\Omega$, $100 \text{ k}\Omega$, $100 \text{ k}\Omega$	
Sensor supply (sine gener		
Type:	Differential sine (1)	80° phase shift)
Coupling:	AC	
Programmed signals: f		0 kHz; 12.5 kHz; 20 kHz, 50 kHz,
Output frequency f_{P}	depending on the	transducer
Output impedance:	< 0,1 Ω typ.	
Short-circuit current:	$> 30 \text{ k}\Omega$ typ. In sn	utdown mode with thermal protection
Short-circuit current.	0.7 A typ. at 25 C	with thermal protection
Power Supply		
Nominal voltage:	24 V	
Voltage supply:	18-30 V	
Optical isolation:	1000 V	
Current consumption at 24 V:		in power safe mode / idle
		er on
		init, sinus on, buffer off
		without load (transducer) at \pm 9 V
		er (buffer on)
		with 16 Solartron AX1S transducers
		7 V power, 5 kHz and 3 V _{me}
Reverse voltage protection		rms
Digital inputs		
Number of inputs:	16 common group	nd acc. to IEC 1131-2
Optical isolation:	1000 V through op	
Nominal voltage:	24 VDC	no coupiers
Input voltage:	0 to 30 V	
Logic input levels:	UH (max) 30 V typ.	. UH (min) 19 V typ.
Logic input icveis.	511 (110A) 50 v typ.	

input voitage.	010501		
Logic input levels:	UH (max) 30 V typ.	UH (min) 19 V typ.	_
	UL (max) 14 V typ.	UL (min) 0 V typ.	
Digital autouto			
Digital outputs			
Number of outputs:	16		
Optical isolation:	1000 V through opto	o-couplers	
Output type:	High-side, load to gr	ound acc. to IEC 1131-2	
Nominal voltage:	24 V		

Voltage supply:	11 V-36 V
Output current per channel:	150 mA max.
Diagnostics:	Common diagnostics bit for all 16 channels at
	overtemperature of one channel
Ethernet	
Number of ports:	2
Cable length:	150 m max. at CAT5E UTP
Bandwidth:	10 Mbps auto-negotiation
	100 Mbps auto-negotiation
Protocol:	10Base-T IEEE802.3 compliant
	100Base-TX IEEE802.3 compliant
Optical isolation:	1000 V
MAC address:	00:0F:6C:##:##:##, unique for each device
Triggor	
Trigger	
Number of inputs:	1 trigger input
Number of outputs:	1 trigger output
Filters/protective circuit:	Low-pass/transorb diode
Optical isolation:	1000 V
Nominal voltage:	24 V external
Input voltage:	0 to 30 V
Input current:	11 mA at 24 VDC, typical
Input frequency (max.):	2 MHz at 24 V
Connector, common with	
Trigger input:	1 x 5-pin male connector M12
Trigger output:	1 x 5-pin female connector M12
Synchro	
Number of inputs:	1
Number of outputs:	1
Max. cable length:	20 m
Optical isolation:	1000 V
Signal type:	R\$485
Connector, common with	
Trigger input:	1 x 5-pin male connector M12
Trigger output:	1 x 5-pin female connector M12
EMC - Electroma	gnetic compatibility
certified EMC laboratory in ac The limit values as set out by	European EMC directive. The tests were carried out by a cordance with the norm from the EN 61326 series (IEC 61326) the European EMC directive for an industrial environment are EMC test report is available on request.
System features	
Interface:	Ethernet acc. to specification IEEE802.3
Dimensions:	260 x 110 x 50 mm

System reatures	
Interface:	Ethernet acc. to specification IEEE802.3
Dimensions:	260 x 110 x 50 mm
Weight:	965 g
Degree of protection:	IP 65
Operating temperature:	-40 °C to + 85°C
Interface conne	ctors
Ethernet:	2 x 4-pin female connector, D-coded M12 for port 0 and port1
Trigger/Synchro input:	1 x 5-nin M12 male connector

Ethernet:	2 x 4-pin female connector, D-coded M12 for port 0 a
Trigger/Synchro input:	1 x 5-pin M12 male connector
Trigger/Synchro output:	1 x 5-pin M12 female connector
Voltage supply	
24 VDC input:	1 x 5-pin M12 male connector
24 VDC output:	1 x 5-pin M12 female connector

MSX-E3701-DI0

Ethernet system for length measurement, 24-bit, 16 inductive	transducers, LVDT, half-bridge. Incl. technical description, software drivers and ConfigTools.
Versions	connector IP 65
	Triager/Synchro

MSX-E3701-DIO-HB-16: for 16 HB inductive transducers MSX-E3701-DIO-LVDT-16: for 16 LVDT inductive transducers

Connection cables for 32 dig. I/O, 24 V auf 37-pol. D-Sub-Connector ST010: Standard round cable, shielded twisted pairs, 2m

PX901-DG: Screw terminal board with Schraubklemmen, LED Status display,

for DIN rail

Voltage supply

CMX-2x: Shielded cable, M12 5-pin female connector/open end, IP 65 CMX-3x: For cascading, shielded cable, M12 5-pin female connector/male

CMX-4x: Shielded cable, M12 5-pin female connector/open end, IP 65 CMX-5x: For cascading, shielded cable, M12 5-pin female connector/male connector IP 65

Ethernet

CMX-6x: CAT5E cable, M12 D-coded male connector/RJ45 connector CMX-7x: For cascading, CAT5E cable, 2 x M12 D-coded male connector

Options

MX-Clip, MX-Rail (please specify when ordering!), MX-Screw, PCMX-1x

* Preliminary product information

Ordering information



info@addi-data.com www.addi-data.com

Ethernet system for serial interfaces 4 ports for RS232, RS422, RS485 or 20 mA CL





4 serial interfaces

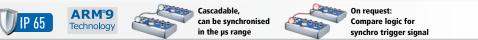
RS232, RS422, RS485, 20 mA Current Loop

128-byte FIFO buffer for each port

16C950 compatible UART

Optical isolation

Onboard evaluation of user data



*Operating temperature



Integrated

Ethernet

switch





on request



www.addi-data.com

Features

- 24 V digital trigger input
- ARM[®]9 32-bit processor
- 64 MB onboard SDRAM for storing data
- Robust standardized metal housing
- Power Save Mode: Reduced power consumption when no acquisition runs

Safety features

- Status LEDs for fast error diagnostics
- Optical isolation Input filters •

Serial interfaces

- 4 serial interfaces
- RS232, RS422, RS485 and TTY (20 mA Current Loop)

Interfaces

- Fast 24 V trigger input
- Ethernet switch with 2 ports
- Synchronisation/trigger In/Out ٠
- Line in for 24 V supply and cascading

Communication interfaces

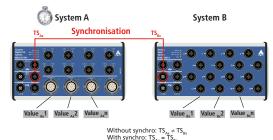
- Web server (configuration and monitoring)
- Command server SOAP for transferring commands • Data server (TCP/IP or UDP socket) for sending acquisition data
- Event server (TCP/IP socket) for sending system events (Diagnostics such as temperature, short-circuits ...)
- Command server Modbus TCP and Modbus (UDP) for sending commands

Synchronisation/time stamp

Time stamp

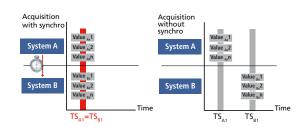
by several systems.

Several MSX-E systems can be synchronised with one another in the µs range through a synchro connection. This allows to start a synchronous data acquisition, to generate trigger events and to synchronise the time on several MSX-E systems. Furthermore, the systems have a time stamp that logs the point in time at which the data was acquired by the system.



The combination of synchronisation and time stamp (TS)

allows the clear allocation of signals that were captured





• The port modes can be mixed

• The channels are optically isolated from each other



Onboard programming / stand-alone operation

Development mode

With the Development mode of the MSX-E systems you can customise your measurement, control and regulation applications to fit your requirements. The programs run directly on the MSX-E systems, which has two advantages: external PCs are relieved and you can process data freely according to your requirements. This helps you to improve the efficiency of your processes and to secure your investments.

Flexibility through firmware and software adaptation

Thanks to the flexibility of the MSX-E product range, the MSX-E7511 system can be easily extended via firmware, e. g. to install additional protocols on the serial interfaces.

Fields of application

The MSX-E7511 Ethernet system features 4 serial interfaces.

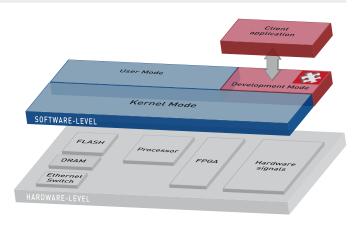
Depending on the version, the interface standards are RS232, RS422, RS485 and TTY (20 mA Current Loop).

Any application can be programmed either through a firmware adaptation or using the Development mode.

Data collector

Via the RS485 interface, the Modbus RTU protocol can be used. For example, temperature sensors featuring an integrated Modbus RTU interface can be read and the complete protocol interpretation can be made on the MSX-E7511 system.

It is thus possible to filter the actual user data - in this case temperature data - and to transmit only this data or to store it in a database.



Onboard calculation

As the system can be programmed, it is possible to effect calculations with the different ports.

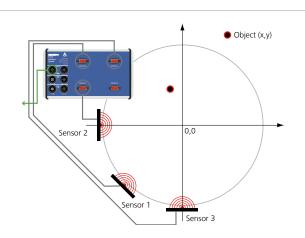
Example:

Using a radar sensor, the position of an object is to be displayed. The sensors indicate the distance via R5422.

Three sensors are used:

- Sensor 1 as a reference to control whether there is an object or not.
- Sensor 2 and 3 for establishing the position of the object.

On the MSX-E7511 system, the interface data is read and interpreted. The position of the object is obtained through calculation of the values of sensor 2 and 3 (e.g. as x/y value). Sensor 1 is used to validate the result. The MSX-E7511 system transmits then only the position data to the Client.



Scanne

Database

serve

. RS232

Selft-sufficient unit

Using further systems from the MSX-E product range, the MSX-E7511 system can also be used as a self-sufficient unit.

Example:

Components are acquired via a scanner (RS232). The MSX-E7511 system reads the barcode and sends the corresponding commands for the processing of the components to a MSX-E1516 Ethernet digital I/O system (handling) and a printer (marking).

There is thus no need for an additional PLC or PC.



Digital I/O: Handling

ConfigTools

The **ConfigTools** program allows an easy administration of the MSX-E systems. These are automatically detected in the network. **ConfigTools** consists of common and specific functions.

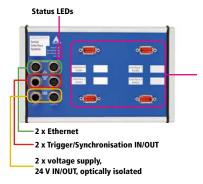
In addition, with **ConfigTools**, the complete configuration of a MSX-E system can be saved and transferred to another system of the same type (clone function).

ConfigTools is included in the delivery.

ConfigTools functions for MSX-E7511:

- Change of IP address
- Display of web interface
- Firmware update
- Save/load system configuration

Features

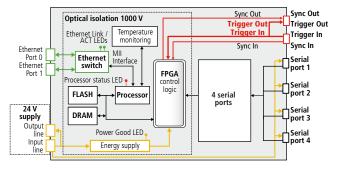


4 serial interfaces, RS232, RS422, RS485, 20 mA CL 9-pin D-Sub male connector

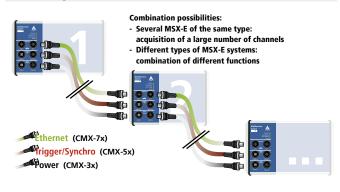
Simplified block diagram

Very easy use through the "ConfigTools" program; The MSX-E system is automatically detected in the network.

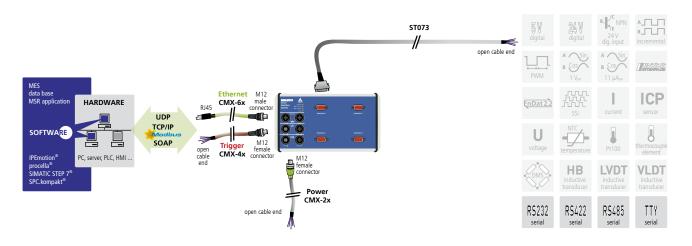




Cascading

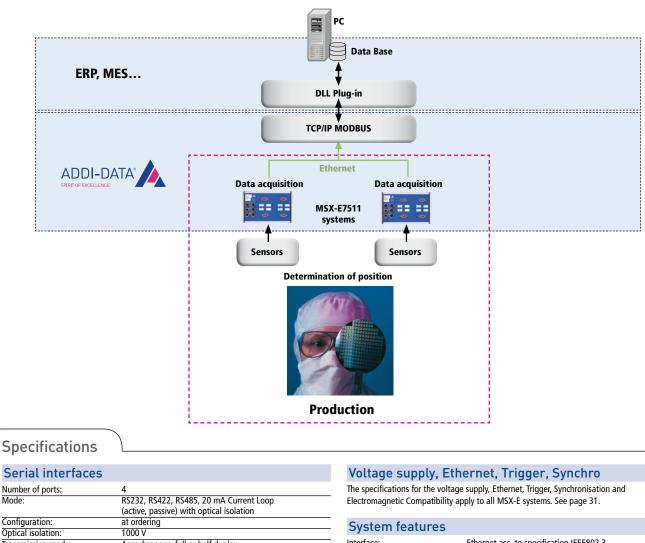


ADDI-DATA connection technology









Connectors:	4 x 9-pin D-Sub male connector
Parity:	Even, odd, none, mark, space
Protocol:	5-, 6-,7- or 8-bit character 1,1½ or 2 stop bits
	Any Baud rate up to 1 MBaud on request
Transfer rate: Programmable up to 115.2 kBaud	
Memory:	128-byte FIFO memory for sender and receiver
Addressing: Automatic	
Transmission mode: Asynchronous, full or half duplex	
	1000 V

Ethernet acc. to specification IEEE802.3
220 mm x 140 mm x 50 mm
620 g
IP 65
150 mA ± 10 % typ. in idle/power save
-40 °C to +85 °C

Ordering information

MSX-E7511

Ethernet system for serial interfaces, 4 ports for RS232, RS422, RS485 or 20 mA CL. Incl. technical description, software drivers and ConfigTools.

Versions

MS.	X-E7	511	-XX	ΧХ

- A: RS232, optically isolated B: RS422, optically isolated C: RS485, optically isolated
- D: 20mA CL
- Example:
 - MSX-E7511-AACC Port 1 = RS232, Port 2 = RS232, Port 3 = RS485, Port 4 = RS485

Connection cables

For serial interfaces Open cable end, 9-pin D-Sub female connector ST073-RS232 (A): RS232 cable ST073-RS422 (B): RS422 cable ST073-RS485 (C): RS485 cable ST073-CL (D): 20mA CL cable

Voltage supply

CMX-2x: Shielded cable, M12 5-pin female connector/open end, IP 65 CMX-3x: For cascading, shielded cable, M12 5-pin female connector/male connector IP 65 Trigger/Synchro CMX-4x: Shielded cable, M12 5-pin female connector/open end, IP 65 CMX-5x: For cascading, shielded cable, M12 5-pin female connector/male connector IP 65 Ethernet CMX-6x: CAT5E cable, M12 D-coded male connector/RJ45 connector CMX-7x: For cascading, CAT5E cable, 2 x M12 D-coded male connector Options S7 Modbus TCP Client Library for S7: Easy use of the Ethernet systems MSX-E with PLCs MSX-E 5V-Trigger: Level change of the trigger inputs and outputs to 5 V MX-Rail (please specify when ordering!), MX-Screw



Database interface software Ethernet-based, no programming needed





Features

Description

- Program for storing measurement data in databases
- Easy to use
- Based on the latest technology (XML, SOAP, etc.)
- Ideal addition to the MSX-E systems as interface
- between field and IT levels
- Data transfer via standard Ethernet
- No programming needed
- Raw data is converted into real values
- Optional filtering of values

Functions

- First Steps DatabaseConnect First Steps are easy-to-follow instructions for creating a project
- Storage function for storing DatabaseConnect projects
- Parameterisation function Measurement parameterisation and system configuration via the website of the MSX-E systems
- Calculation function

Measurement data is converted into "real values," e.g. a 0..10 V standardised signal into fill level [%], pressure [bar], etc.

- Scan function
 Easy detection and selection of MSX-E systems present
 in the network
- Database function
 - for creating and configuring databases
- Data monitoring (Live values)
- Error analysis (Log files)
- Export as .txt, .csv or .xml files
- Direct connection of databases: MS SQL Server[®], mySQL, MS Access[®]

Licence conditions

- 1 PC
- a maximum of 20 MSX-E systems

Extras

- Language versions on request
- Multi-user/server licence on request

DatabaseConnect

Measurement data stored in databases
Standard Ethernet for easy integration
Ease of use: No programming needed
Processing of measurement data
Filtering of measurement values
Easy parameterising of MSX-E systems
via website

Examples of use

- Acquisition of machine data, e.g. number of produced pieces, downtime, etc.
- Temperature data logger, e.g. in server rooms
- Documentation of production data and quality parameters in databases
- Company-wide availability of measurement data

Scope of delivery

The program is supplied on a CD-ROM including a quick installation manual and an online tutorial.

First Aid Functions

Online help, tutorial, First Steps

Read further information about the current software version on the internet at www.addi-data.com



Specifications

Program features

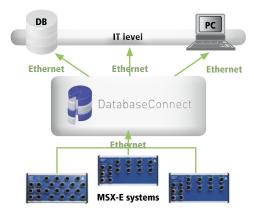
.	
DatabaseConnect: Data processing: Language:	Single-user licence Measurement data is read, converted, calculated and stored English Other languages on request
Database connectio	n
File format: Databases:	.txt, .csv, .xml Microsoft SQL-Server, mySQL, MS Access® Other databases on request
System features	
Memory space: Processor: Hard drive:	min. RAM of 512 MB, 1 GB recommended min. CPU of 700 MHz, 2 GHz recommended min. HDD of 350 MB (300 MB for .Net and 50 MB for DatabaseConnect)
Operating system:	Microsoft Windows 2000 Microsoft Windows XP (32-bit) Microsoft Vista (32-bit) Microsoft Windows 7 (32-bit) Linux on request
Monitor screen resolution:	min. 1024 x 768 pixels

MSX-E system compatibility

Ethernet system	DatabaseConnect compatible	
MSX-E1516:	Digital I/O system, 16 digital I/O	yes
MSX-E1516-NPN:	Digital I/O system, 16 digital I/O	on request
MSX-E1516-5V-Inpu	t: Digital I/O system, 16 digital I/O:	
MSX-E1701:	Multifunction counter system, digital I/O	on request
MSX-E1711:	Multifunction counter system, sin/cos, digital I/O	on request
MSX-E1721:	Multifunction counter system, sin/cos, digital I/O	on request
MSX-E1731:	Multifunction counter system, digital	on request
MSX-E1741-1VPP:	Multifunction counter system, digital	on request
MSX-E3121:	Analog input system	yes, max. 1 kHz/channel
MSX-E3122:	Multifunction system, analog I/O	on request
MSX-E3011:	Analog input system	yes, max. 1 kHz/channel
MSX-E3021:	Analog input system	yes, max. 1 kHz/channel
MSX-E3027:	Analog input system	yes, max. 1 kHz/channel
MSX-E3017:	Force distance measurement system	on request
MSX-E3317:	Force distance measurement system	on request
MSX-E3211:	System for temperature acquisition	on request
MSX-E3311:	System for pressure acquisition	on request
MSX-E3601:	System for the acquisition of dynamic signals	on request
MSX-E3601-2:	System for the acquisition of dynamic signals	on request
MSX-E3711:	System for length measurement, 24-bit, simultaneous	on request
MSX-E3701-x:	System for length measurement	on request
MSX-E3700:	System for length measurement	on request
MSX-E7511:	System for length measurement	on request

Use cases Use Case 1: 🕨 If no database is used and none is to be generated, DatabaseConnect can store the measurement data in files like for example .txt , .csv or .xml format. If a database which already exists is to be used (SQL[®], Use Case 2: 🕨 mySQL, etc.), DatabaseConnect stores the measurement data directly into it. If a database is not yet available, but the data is to be Use Case 3: 🕨 stored in one, DatabaseConnect can generate an (open source) mySQL database and use it.

Data flow with DatabaseConnect



How DatabaseConnect works

Field level MSX-E systems	DatabaseConnect 🚏	IT level
Raw data (on data server) for example Channel 1: 010 V Channel 2: ± 10 V	Parameterisation of the acquisition Monitoring	Time stamp Channel 1: Fill level in % Channel 2: Pressure in bar Channel 20: Fan 1 off
Channel 20: off Channel 21: on Channel n: 420 mA	Database management Processing the measure- ment data Storing the configuration (backup)	Channel 21: Door A open Channel n: humidity in %

Ordering information

ADDI-DATA

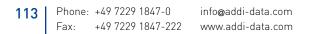
DatabaseConnect

Database interface software, Ethernet-based, no programming needed

Versions

DatabaseConnect

Version 1.1: CD-ROM incl. Quick Installation and online manual







DatabaseConnect

Performance and reliability in the industrial environment



With the sophisticated and reliable ADDI-DATA PC boards, your measurement and automation tasks will be a success! High quality products, well thought-out design concepts and robust constitution guarantee a reliable function of ADDI-DATA PC boards in a harsh industrial environment.

For more than 25 years ADDI-DATA has been developing interference-free PC boards for industrial measurement and automation and offers a wide range of solutions for PCI-Express, PCI and CompactPCI-bus:

- Digital I/O • Analog I/O or multifunction boards
- Serial interfaces

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Fax:

- Multifunction counter boards
- Motion control boards

For a safe and reliable use in your application, ADDI-DATA PC boards are protected by numerous protective circuits like optical isolation, filters, protection against short-circuits etc.

Unique applications due to FPGA technology

A FPGA component has a programmable logic on which you can save your own algorithms in order to adapt the functionality of the PC board to your requirements. This adaptation makes your PC board unique and improves the performance of your applications. The onboard algorithms reduce the cycle time of signal acquisition and of regulation tasks.

Many ADDI-DATA boards come with a FPGA component. Use the full potential of your PC board hardware and software resources and thus accelerate your processes.

Bespoke solutions

Benefit from our know-how and our experience and improve the efficiency of your application with our customised solutions. We will be glad to help you - from a small adaptation of a standard product to the complete development of a new product.

Examples:

- Adaptation of the signal type, for example 12 V instead of 24 V
- Firmware adaptation
- Drivers for specific operating systems

MORE PERFORMANCE WITH ADDI-DATA PC BOARDS

- Simplified application design
- Faster processes through FPGA technology
- High-precision measurement results
- Individual customisation
- Safe investments through long-term availability





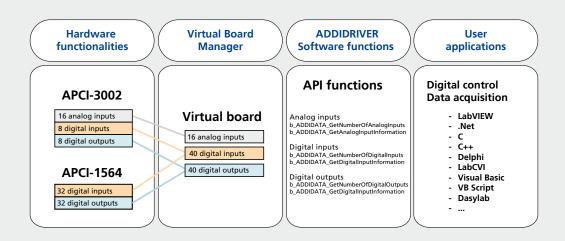
Fast and easy application design

ADDI-DATA PC boards are supplied with an extensive software package for a fast and convenient integration into your application: the clever driver concept ADDIPACK, a wide range of drivers and samples and a configuration tool assist you from the beginning.

ADDIPACK: welcome to the virtual world

For an easy administration of the boards installed in the PC ADDI-DATA has developed a convenient driver system that lists the functionalities of all boards inside your PC on a virtual board. This means that you do not administrate PC boards but functionalities, in principle like a resource.

Thus you can easily exchange or add new boards. Changes in the functionalities due to exchanges are immediately visible. An installation of new drivers, for example in case of replacement of PCI boards by PCI-Express boards, is not necessary.



The ADDIPACK concept

The ADDIPACK software is organised in two parts: ADDIDRIVER (ADDIDATA.DLL): The library contains all API functions for the control of ADDI-DATA boards. ADDEVICE MAPPER and ADDEVICE MANAGER: With these programs, you can administrate the functionalities of the virtual board. The programs help you by showing a clear visualisation of the virtual board.

These two principles are the interface between ADDI-DATA boards and your application.

Supported functionalities

- Digital inputs and outputs
- Analog inputs and outputs
- Temperature measurement
- Pressure measurement
- Resistance measurement
- Inductive sensors
- Timer/ watchdog/ counter

Numerous drivers and samples

ADDI-DATA boards come with drivers and samples, software packages and compilers for the most common operating systems like Linux, Windows 7/ Vista/XP/2000, LabVIEW, etc.

For time-critical tasks ADDI-DATA offers real-time drivers for Linux and Windows (RTX, VxWorks). They allow an easy integration of our boards into real-time systems.

Linux drivers

In the sector of automation, Linux allows an easy and cost-effective realisation of highly-efficient systems. Depending on the board type, our Linux drivers are available for Kernel 2.4, 2.6 or 3.0.

The driver model is based on 2 levels: the user level and the Kernel level. The source code of all drivers is included, allowing you to adapt the drivers to your personal requirements.

Practical simulation

In order to simplify the initiation of our boards the drivers come with numerous programming examples. They allow you to parameterise the inputs and outputs of your process and to simulate it practically. Complete measurement, control and regulation processes can be developed fast and easily.

Fast integration in LabVIEW

LabVIEW drivers are available for numerous ADDI-DATA PC boards. They enable a fast and convenient parameterisation of your measurement boards with LabVIEW.

Individual driver adaptations

You cannot find the drivers you need on our website? You need a driver adaptation for your application? Our experts will be glad to advise you. Just call us!



PCI EXPRESS BOARDS



YOUR BENEFITS

- Higher data transmission rate
- Shorter cycle times through FPGA technology
- Simplified application design
- Safe operation in an industrial environment
- Long term availability
- Fast upgrade from PCI to PCI Express
- Free loan period.

Prepared for the future

Realise your future PC-based applications with efficient and robust PCI Express boards by ADDI-DATA.

The boards of the APCle-xxxx series are intended for use in an industrial environment: They are equipped with numerous protective circuits like filters, optical isolation, protection against overvoltage and short-circuits, etc. Thus they ensure a reliable and smooth operation, particularly in case of dangerous interferences like voltage peaks or high currents.

With the ADDI-DATA PCI Express boards, you secure your investments in the long term.

Simplified application design

ADDI-DATA PCI Express boards come with useful software tools for a fast and convenient integration into your application: The ingenious driver concept ADDIPACK (see page 75), a wide range of drivers and samples and a configuration tool assist you from the beginning.

Shorter cycle times through FPGA technology

PCI Express boards with FPGA components reduce the cycle time of signal acquisition and regulation tasks. Use the full hardware and software capacity of your board and accelerate your processes. In the product overview, PCI Express boards with FPGA component are indicated.

Upgrade from PCI to PCI Express

Your application is running with ADDI-DATA PCI boards and you would like to upgrade to PCI Express boards? Our PCI Express follow-up models are functionally compatible with our PCI boards. Benefit from the new technologies and the new efficient FPGA components that we have added to our PCI Express boards.

Any questions on compatibility?

Contact us! +49 7229 1847-0 or per e-mail at info@addi-data.com.

HIGH LEVEL OF PROTECTION

- Optical isolation from 500 V to 1000 V
- Separation of analog and digital signals
- Protection against short-circuits, overtemperature, overvoltage
- Filters for the inputs and outputs
- Industry-standard D-Sub connectors





New!

FPGA Filter and protective circuits Optical isolation 100 Digital, 24 V / 12 V / 5 V 100 Input channels, incl. interruptible 24 V / 12 V / 5 V 24 V / 12 V / 5 V 22 Output channels, 24 V / 5 V 16, Output current per channel 500 m Relays 1 wa tim	Light of the second sec	APCIe-1532 APCIe-1533 APCIe-1533 APCIe-1533 APCIe-1533 APCIe-1533 APCIE-1533	r 0 V 5	A 20001		APCIe-1564-5V / APCIe-1564-5V-HS	APCIe-2200	APCIe-1711	APCle-3121 / APCle-3123	APCIe-3021	APCIe-3521	APCIe-040	-7ххх
FPGA Filter and protective circuits Optical isolation 100 Digital, 24 V / 12 V / 5 V 100 Input channels, incl. interruptible 24 V / 12 V / 5 V 24 V / 12 V / 5 V 22 Output channels, 24 V / 5 V 16, Output current per channel 500 m Relays 11 wa tim 1 time Counter 2 x 16-bit	✓ ✓ 1000 V 16 15 24 V 16, 24 V 0 mA (typ.) watchdog/	1000 1000 16 19 24 V	r 0 V 5	<i>s</i>	✓ ✓		1	4	AF	AF	AP(APC	APCIe-7xxx
Filter and protective circuits Optical isolation 10 Digital, 24 V / 12 V / 5 V 10 Input channels, incl. interruptible 2 24 V / 12 V / 5 V 2 Output channels, 24 V / 5 V 16, Output channel 500 m Relays 1 wa tin 1 time Counter 2 x 16-bit	✓ 1000 V 16 15 24 V 16, 24 V 0 mA (typ.) watchdog/	1000 1000 100 100 100 100 100 100 100 1	r 0 V 5	1	1	1	•	1	1	1	1	1	1
Optical isolation 10 Digital, 24 V / 12 V / 5 V 10 Input channels, incl. interruptible 2 24 V / 12 V / 5 V 2 Output channels, 24 V / 5 V 16, Output current per channel 500 m Relays 1 wa tim 1 time Counter 2 x 16-bit	1000 V 16 15 24 V 16, 24 V 0 mA (typ.) watchdog/	100 16 12 24 V	0 V 5				1	1	1	1	1	1	
Digital, 24 V / 12 V / 5 V Input channels, incl. interruptible 24 V / 12 V / 5 V 0utput channels, 24 V / 5 V 0utput channels, 24 V / 5 V 16, 0utput current per channel 8 Watchdog / Timer 1 Counter 2 x 16-bit	16 15 24 V 16, 24 V 0 mA (typ.) watchdog/	16 15 24 V	5	1000 V		1	1	1	1	1		1	1
Input channels, incl. interruptible 2 24 V / 12 V / 5 V 2 Output channels, 24 V / 5 V 16, Output current per channel 500 m Relays 1 wa tim 1 time Counter 2 x 16-bit	15 24 V 16, 24 V 0 mA (typ.) watchdog/	15 24 V			1000 V	1000 V	1000 V	1000 V	500 V	500 V	500 V	1000 V	optional
incl. interruptible 24 V / 12 V / 5 V 2 Output channels, 24 V / 5 V 16, Output current per channel 500 m Relays 1 wa tim 1 time Counter 2 x 16-bit	15 24 V 16, 24 V 0 mA (typ.) watchdog/	15 24 V		1									
24 V / 12 V / 5 V 2 Output channels, 24 V / 5 V 16, Output current per channel 500 m Relays 1 Watchdog / Timer 1 wa tim 1 time Counter 2 x 16-bit	16, 24 V 0 mA (typ.) watchdog/			8	32 16	32 16	16 15		4 1	4 1	4 1	8	
Output current per channel 500 m Relays 1 wa tin 1 time Watchdog / Timer 1 wa tin 1 time Counter 2 x 16-bit	0 mA (typ.) watchdog/	16, 2	12 V	24 V	24 V	5 V	24 V	24 V	24 V	24 V	24 V	24 V	
per channel 500 m Relays 1 wa Watchdog / Timer 1 wa Counter 2 x 16-bit	watchdog/		4 V	8, 24 V	32, 24 V	32, 5 V			4, 24 V	4, 24 V	4, 24 V		
Watchdog / Timer 1 wa tin 1 time Counter 2 x 16-bit		500 mA	(typ.)	500 mA (typ.)	500 mA (typ.)	50 mA	Relays 2A		65 mA (typ.)	65 mA (typ.)	65 mA (typ.)	Relays 2 A	
Watchdog / Timer tim 1 time 1 time Counter 2 x 16-bit							16					8	
Counter 16-bit	imer (12-bit)	1 watc time 1 timer (r+	1 watchdog	1 watchdog/ timer + 1 timer (12-bit)	1 watchdog/ timer + 1 timer (12-bit)	1 watchdog	-	1 watchdog/ timer + 1 timer (16-bit)	1 timer (16-bit)	1 watchdog/ timer + 1 timer (16-bit)	7	-
Reprogrammable function modules		2 x 16	6-bit	-	3 x 32-bit	3 x 32-bit	_	_	-	_	-	-	-
	es												
 Incremental counter, SSI synchronous serial interface, Counter/timer, Pulse acquisition, Frequency, pulse width, period duration measure- ment, PWM dig. in- and outputs BiSS-B, BiSS-C Parallel interface New: EnDat 2.2 New: Sin/Cos 								4					
Input frequency								Up to 5 MHz New: 10 MHz					
Signals								TTL, RS422, 24 V					
Analog													
Analog inputs, 16-bit									16 / 8 SE or 8 / 4 diff.	16 / 8 SE or 8 / 4 diff.			
Throughput (kHz)									100	100			
Voltage range									0-10 V ± 10 V	0-10 V ± 10 V			
Current inputs (option)									0(4)-20 mA	0(4)-20 mA			
Gain 1, 2, 5, 10									1, 2, 5, 10	1, 2, 5, 10			
Trigger (software / 24 V)									1	1			
Analog outputs, 16-bit									8 or 4		8 or 4		
0-10 V / ± 10 V									1		1		
Current outputs									0-20 mA		0-20 mA		
Serial interfaces (base boards)													1 / 2 / 4 / 8 ports
Operating mode configuration through SI modules													RS232, RS422, RS485, 20 mA CL
Software Curre		list on											20 MA CL
Page 1	rrent driver		the we	eb: www.a	ddi-data.co	m							



PCI >>



SPIRIT OF EXCELLENCE









Also for **PC** See APCI-1500, page 154



Also for *CompactPCI*TM See CPCI-1500, page 232



Also for PC/104-PLUS see PC104-PLUS1500 page 214



64/32-bit drivers





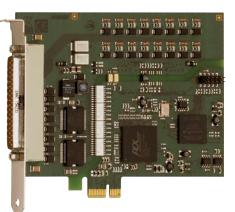
I abVIFW



* Preliminary product information

Fax:

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Features

Inputs

- 16 optically isolated inputs, 24 V (APCIe-1532 / APCIe-1502 / APCIe-1501) or 12 V (APCIe-1532-12V)
- incl. 15 interruptible inputs
- Channel 0 can be used as a 16-bit counter input (up to 100 kHz)
- Reverse voltage protection
- All inputs are filtered

Outputs

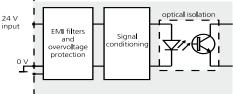
- 16 optically isolated outputs, 11 to 36 V
- Output current per channel 500 mA
- Total current: 3 A typ. (fused through PTC resistor)
- Watchdog for resetting the outputs to "0"
- At Power-On, reset of the outputs to "0"
- Current limit: ~1.5 A per 8 channels (through PTC)
- Short-circuit current per output ~1.5 A typ.
- Self-resetting fuse (electronic fuse)
- Overtemperature and overvoltage protection
- 24 V power outputs with protection diodes and filters
- Ext. 24 V voltage supply screened and filtered
- Shutdown logic, when the external supply voltage drops below 7 V
- Timer / Counter
- 2 timers (12-bit resolution)
 - 1 timer can be used as watchdog
- 1 counter (APCle-1502: 2 counters)

Safety features

- Optical isolation 1000 V
- Creeping distance IEC 61010-1
- Protection against fast transients (burst), overvoltage, electrostatic discharge and high-frequency EMI
- · Separate ground line for inputs and outputs

Protective circuit for the input channels

Peripherals | Board



Phone: +49 7229 1847-0 info@addi-data.com +49 7229 1847-222 www.addi-data.com

APCIe-1532 / APCIe-1532-12V / APCIe-1502 / APCIe-1501

PCI Express interface

16 digital inputs, 24 V / 12 V,

including 15 interruptible inputs

16 digital outputs, 24 V, 500 mA/channel

Optical isolation 1000 V

Input and output filters

Connection through industry-standard

D-Sub connector

Applications

- Industrial I/O control
- PLC coupling
- Reading of encoder values for process control
- Signal switching
- Interface to electromechanical relays
- ON/OFF monitoring of motors, lights...
- Watchdog timer
- Interface to machines

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

- .NET
 - Microsoft VC++ Borland C++
- Visual Basic Delphi
- LabVIEW LabWindows/CVI

ADDIPACK functions:

- Digital input
 Digital output
 Watchdog
- Timer Counter

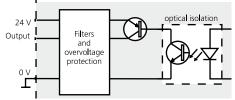
On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads

Protective circuit for the output channels

Peripherals Board







Digital inputs			
Number of inputs:	16 digital inputs,		
(common ground	channel 0 can be used as a 16-bit counter input		
acc. to IEC 1131-2)	(up to 100 kHz) / APCIe-1502: channel 0 and 1		
Interruptible inputs:	15 channels (channel 1 to 15)		
Optical isolation:	1000 V through opto-couplers, from PC to peripheral		
Nominal voltage:	24 V (APCle-1532, -15	02, -1501 / 12 V (APCIe-1532-12V)	
Input current:	at 24 V	at 12 V (APCIe-1532-12V)	
Channel 0 or 0-1:	6.6 mA typ.	3.2 mA typ.	
Channel 1-15 or 2-16:	2 mA typ.	1.5 mA typ.	
Input frequency (max.):	at 24 V	at 12 V (APCIe-1532-12V)	
Channel 0 or 0-1:	100 kHz	100 kHz	
Channel 1-15 or 2-16:	5 kHz	5 kHz	
Logic input levels:	at 24 V	at 12 V (APCIe-1532-12V)	
UH (max.):	30 V	16 V	
UH (min.):	19 V	9 V	
UL (max.):	14 V	6 V	
UL (min.):	0 V	0 V	
Filters/protective circuit:	Input filters, transil dio	de, RC filters, Z diode, opto-couplers	

Filters/protective circuit:

Digital outputs

Number of outputs:	16 digital outputs
Output type:	High-side (load to ground) acc. to IEC 1131-2
Optical isolation:	1000 V (through opto-couplers), from PC to peripheral
Nominal voltage:	24 V
Supply voltage range:	11 to 36 V
Current limit:	1.5 A per 8 channels (through PTC)
Output current per output:	500 mA (typical)
Short-circuit current per output:	1.5 A (typ.) pulse current
	shutdown at 24 V, R_{load} < 0.1 Ω
RDS ON resistance:	max. 0.2 Ω at 25 °C
Switch-on time:	l _{out} =0.5 A, load = resistance: 50 μs
Switch-off time:	l _{out} =0.5 A, load = resistance: 75 μs
Overtemperature (shutdown):	135 °C (output driver)
Temperature hysteresis:	15 °C (output driver)

Timer/watchdog

Timer:	2 x 12-bit timers, 1 up to 4095 µs, ms, s 1 timer can be used as watchdog.
Safety	
Shutdown logic (V _{CC} diagnostic):	When the ext. 24 V voltage drops below 7 V:

The outputs are switched off. Watchdog: For resetting the outputs to "0 Common diagnostics: For all 16 channels at overtemperature of one channel

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

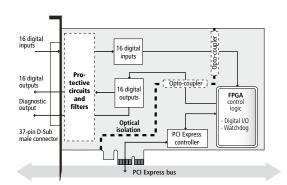
Physical and environmental conditions

Dimensions:	149 x 99 mm		
System bus:	Acc. to PCI Express base specification,		
	Revision 1.0a (PCI Express 1.0a)		
Space required:	1-/4-lane PCI Express slot		
Operating voltage:	+ 3.3 V from PC		
Current consumption:	Inputs and outputs inactive 320 mA \pm 10 %, typical		
	8 inputs and outputs active 400 mA \pm 10 %, typical		
	16 inputs and outputs active 470 mA \pm 10 %, typical		
Front connector:	37-pin D-Sub male connector		
Temperature range:	0 to 60 °C (with forced cooling)		

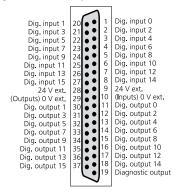
APCIe-1532 / APCIe-1532-12V / APCIe-1502 / APCIe-1501 Digital I/O board, optically isolated, 32 digital inputs and outputs, 24 V / 12 V, for PCI Express. Incl. technical description and software drivers.

Digital no board, optically isolated, 52 digital inputs and outputs, 24 v / 12 v, for r cl express. Incl. technical description and software drivers.					
APCIe-1532	16 inputs, 24 V, 16 outputs, 11-36 V, 1 counter	ST010:	Standard round cable, shielded, twisted pairs, 2 m		
APCIe-1532-12V: 16 inputs, 12 V, 16 outputs, 11-36 V, 1 counter		ST011:	Standard round cable, shielded, twisted pairs, 5 m		
APCIe-1502: 16 inputs, 24 V, 16 outputs, 11-36 V, 2 counters		ST010-S:	Same as ST010, for high currents		
APCIe-1501	APCIe-1501: 16 inputs, 24 V, 16 outputs, 11-36 V, 1 counter		Round cable between APCIe-15x2 and PX8500-G,		
(APCI-1501 firmware compatible with APCI-1500, direct replacement possible)			shielded, twisted pairs, 2 m		
Accessories		ST022:	Round cable between PX8500-G and PX901		
PX901-D: Screw terminal panel, LED status display			or PX9000, shielded, 2 m		
PX901-DG: Screw terminal panel, LED status display, for DIN rail		ST8500:	Ribbon cable for cascading two PX8500-G		
PX9000: 3-row screw terminal panel for DIN rail,					
with LED status display					
PX8500-G: Relay output board for DIN rail, cascadable					

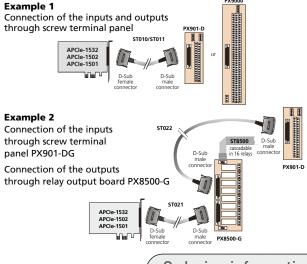
Simplified block diagram



Pin assignment – 37-pin D-Sub male connector



ADDI-DATA connection



Ordering information

* Preliminary product information



Digital I/O board, optically isolated, 16 digital inputs and outputs, 24 V, for PCI Express





Also for **PCI** See APCI-1516, page 148

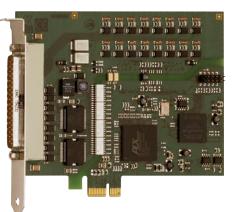








LabWindows/CVI™



Features

Inputs

- 8 optically isolated inputs, 24 V
- Reverse voltage protection
- All inputs are filtered

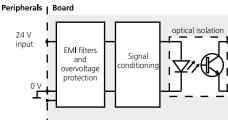
Outputs

- 8 optically isolated outputs, 11 to 36 V
- Output current per channel 500 mA
- Watchdog for resetting the outputs to"0"
- At Power-On, reset of the outputs to "0"
- Current limit: ~ 1.5 A per 8 channels (through PTC) ٠
- Short-circuit current per output ~1.5 A typ.
- Self-resetting fuse (electronic fuse)
 - Overtemperature and overvoltage protection
- 24 V power outputs with protection diodes and filters
- External 24 V voltage supply screened and filtered
 - Shutdown logic, when the external supply voltage drops below 7 V

Safety features

- Optical isolation 1000 V
- Creeping distance IEC 61010-1
- · Separate ground line for inputs and outputs
- Protection against fast transients (burst), overvoltage, electrostatic discharge and high-frequency EMI
- Watchdog for the outputs

Protective circuit for the input channels



APCIe-1516

PCI Express interface

8 digital inputs, 24 V

8 digital outputs, 24 V, 500 mA/channel

Optical isolation 1000 V

Input and output filters

Connection through industry standard

D-Sub connector

Applications

- Industrial I/O control
- PLC coupling
- Signal switching
- Interface to electromechanical relays

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000 ٠
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

- .NET
- Microsoft VC++ Borland C++
- Visual Basic Delphi
- LabVIEW LabWindows/CVI

ADDIPACK functions:

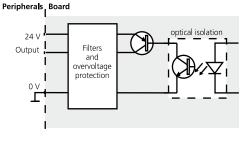
• Digital input • Digital output • Watchdog

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads

Protective circuit for the output channels





info@addi-data.com www.addi-data.com

- Total current: 1.5 A typ.
 - (fused through PTC resistor)

 - •

Specifications

Digital inputs		
Number of inputs: (common ground acc. to IEC 1131-2)	8 digital inputs	
Optical isolation:	1000 V through	opto-couplers, from PC to peripheral
Nominal voltage:	24 V	
Input current:	Channel 0-7:	2 mA at 24 V, typical
Input frequency (max.):	Channel 0-7:	5 kHz at 24 V
Logic input levels:	UH (max.):	30 V
	UH (min.):	19 V
	UL (max.):	14 V
	UL (min.):	0 V
Filters/protective circuit:	Input filters, transil diode, RC filters, Z diode, opto-couplers	

Digital outputs

J	
Number of outputs:	8 digital outputs
Output type:	High-side (load to ground) acc. to IEC 1131-2
Optical isolation:	1000 V (through opto-couplers), from PC to peripheral
Nominal voltage:	24 V
Supply voltage range:	11 to 36 V
Current limit:	1.5 A for all channels (through PTC)
Output current per output:	500 mA (typical)
Short-circuit current per output	1.5 A (typ.) pulse current
	shutdown at 24 V, R_{load} < 0.1 Ω
RDS ON resistance:	max. 0.2 Ω at 25 °C
Switch-on time:	l _{out} =0.5 A, load = resistance: 50 μs
Switch-off time:	l _{out} =0.5 A, load = resistance: 75 μs
Overtemperature (shutdown):	135 °C (output driver)
Temperature hysteresis:	15 °C (output driver)

Safety

Shutdown logic (V _{CC} diagnostic):	When the ext. 24 V voltage drops below 7 V: The outputs are switched off.
Watchdog:	For resetting the outputs to "0"
	Time units: 1 up to 4095 µs, ms, s
Diagnostics:	Common Diagnostics for all 8 channels at
	overtemperature of one channel

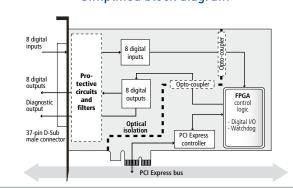
EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

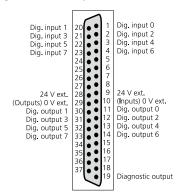
Physical and environmental conditions

Dimensions:	149 x 99 mm		
System bus:	Acc. to PCI Express base specification,		
	Revision 1.0a (PCI Express 1.0a)		
Space required:	1-/4-lane PCI Express slot		
Operating voltage:	+ 3.3 V from PC		
Current consumption:	Inputs and outputs inactive 320 mA ± 10 %, typical		
	8 inputs and outputs active 400 mA \pm 10 %, typical		
Front connector:	37-pin D-Sub male connector		
Temperature range:	0 to 60 °C (with forced cooling)		

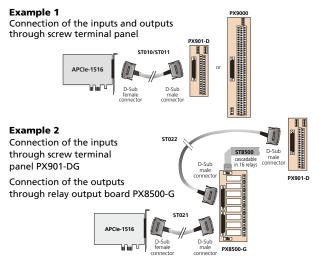
Simplified block diagram



Pin assignment – 37-pin D-Sub male connector



ADDI-DATA connection



Ordering information

APCIe-1516

Digital I/O board, optically isolated, 16 digital inputs and outputs, 24 V, for PCI Express. Incl. technical description and software drivers.

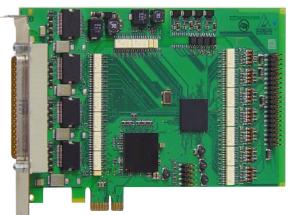
PX901-DG: Scree PX9000: 3-ro with	ew terminal panel, LED status display ew terminal panel, LED status display, for DIN rail ow screw terminal panel for DIN rail, h LED status display ay output board for DIN rail, cascadable	ST010: ST011: ST010-S: ST021: ST022: ST8500:	Standard round cable, shielded, twisted pairs, 2 m Standard round cable, shielded, twisted pairs, 5 m Same as ST010, for high currents Round cable between APCle-1516 and PX8500-G, shielded, twisted pairs, 2 m Round cable between PX8500-G and PX901 or PX9000, shielded, 2 m Ribbon cable for cascading two PX8500-G
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PCI >

Digital I/O board, optically isolated, 64 digital inputs and outputs, 24 V, for PCI Express











Also for **PC** See APCI-1564, page 150







Features

Inputs

- 32 optically isolated inputs, 24 V, incl. 16 interruptible inputs
- Channels 0-2 can be used as 32-bit counter inputs (up to 500 kHz)
- Reverse voltage protection
- All inputs are filtered

Outputs

- 32 optically isolated outputs, 11 to 36 V
- Output current per channel 500 mA
- Total current: 3 A typ. (fused through PTC resistor)
- Watchdog for resetting the outputs to "0"
- At Power-On, reset of the outputs to "0"
- Current limit: ~1.5 A per 8 channels (through PTC)
- Short-circuit current per output ~1.5 A typ.
- Self-resetting fuse (electronic fuse)
- Overtemperature and overvoltage protection
- 24 V power outputs with protection diodes and filters
- Ext. 24 V voltage supply screened and filtered
- Shutdown logic, when the external supply voltage drops below 7 V

Timer / Watchdog / Counter

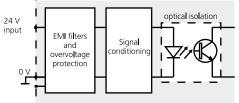
- 2 timers (12-bit), of which one can be used as a watchdog
- 3 counter (32-bit)

Safety features

- Optical isolation 1000 V
- Creeping distance IEC 61010-1
- Protection against fast transients (burst), overvoltage, electrostatic discharge and high-frequency EMI
- · Separate ground line for inputs and outputs

Protective circuit for the input channels

Peripherals | Board



APCIe-1564

PCI Express interface

32 digital inputs, 24 V,

including 16 interruptible inputs

32 digital outputs, 24 V, 500 mA/channel

Optical isolation 1000 V

Input and output filters

Connection through industry-standard

D-Sub connector

Applications

- Industrial I/O control
- PLC coupling
- Signal switching
- Interface to electromechanical relays
- Automatic test equipment
- ON/OFF monitoring of motors, lights...
- Watchdog timer
- ...

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000 ٠
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

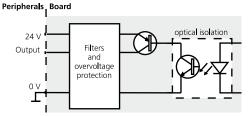
• C#.NET, C

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads

Protective circuit for the output channels





* Preliminary

product information

info@addi-data.com

www.addi-data.com

on request

Specifications*

Digital inputs

32 digital inputs, channel 0-2 can be used as 32-bit counter inputs (up to 500 kHz)		
16 channels (cha	nnel 4 to 19)	
1000 V through o	1000 V through opto-couplers, from PC to peripheral	
24 V		
Channel 0-3:	6.6 mA	at 24 V, typical
Channel 4-31:	2 mA	at 24 V, typical
Channel 0-2:	500 kHz	at 24 V
Channel 3-31:	5 kHz	at 24 V
UH (max.):	30 V / 3.1 i	mA, typical (channel 4-31)
UH (min.):		A, typical (channel 4-31)
UH (max.):		nA, typical (channel 0-3)
UH (min.):	19 V / 3.4 ı	mA, typical (channel 0-3)
UL (max.):	14 V / 0.1 ı	mA, typical
UL (min.):	0 V / 0 mA	, typical
RC filters, Z diod	e, opto-couple	rs
	channel 0-2 can (up to 500 kHz) 16 channels (cha 1000 V through of 24 V Channel 0-3: Channel 0-3: Channel 0-2: Channel 3-31: UH (max.): UH (max.): UH (min.): UH (max.): UH (min.): UH (min.): Input filters, tran	channel 0-2 can be used as 32 (up to 500 kHz) 16 channels (channel 4 to 19) 1000 V through opto-couplers, 24 V Channel 0-3: 6.6 mA Channel 4-31: 2 mA Channel 0-2: 500 kHz Channel 3-31: 5 kHz UH (max.): 30 V / 3.1 m UH (min.): 19 V / 3.4 UH (max.): 19 V / 3.4 UL (max.): 14 V / 0.1

Digital outputs

Number of outputs:	32 digital outputs
Output type:	High-side (load to ground) acc. to IEC 1131-2
Optical isolation:	1000 V (through opto-couplers), from PC to peripheral
Nominal voltage:	24 V
Supply voltage range:	11 to 36 V
Current limit:	1.5 A per 8 channels (through PTC)
Output current per output:	500 mA (typical)
Short-circuit current per output:	1.5 A (typ.) pulse current
	shutdown at 24 V, R_{load} < 0.1 Ω
RDS ON resistance:	0.2 Ω at 25 °C
Switch-on time:	l out=0.5 A, load = resistance: 50 μs
Switch-off time:	l out=0.5 A, load = resistance: 75 μs
Overtemperature (shutdown):	135 °C (output driver)
Temperature hysteresis:	15 °C (output driver)

Timer/watchdog

Watchdog:	12-bit, programmable as timer from $1\mu s$ to 4095 s
Safety	
Shutdown logic (V _{CC} diagnostic):	When the ext. 24 V voltage drops below 7 V, the outputs are switched off.
Watchdog:	For resetting the outputs to "0"
Common diagnostics:	For all 16 channels at overtemperature of one channel

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

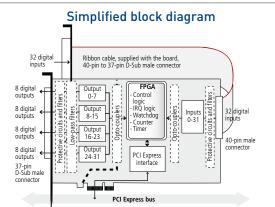
Physical and environmental conditions

Dimensions:	168 x 99 mm	
System bus:	Acc. to PCI Express base specification,	
-	Revision 1.0a (PCI Express 1.0a)	
Space required:	1-/4-lane PCI Express slot	
Operating voltage:	+ 3.3 V from PC	
Current consumption:	Inputs and outputs inactive 340 mA ± 10 %, typical	
	inputs and outputs active 590 mA \pm 10 %, typical	
Front connector:	37-pin D-Sub male connector	
Temperature range:	0 to 60 °C (with forced cooling)	

APCIe-1564

Digital I/O board, optically isolated, 64 digital inputs and outputs, 24 V, for PCI Express. Incl. technical description and software drivers.

Standard round cable, shielded, twisted pairs, 2 m
Standard round cable, shielded, twisted pairs, 5 m
Same as ST010, for high currents
Round cable between PX8500-G and PX901
or PX9000, shielded, 2 m
D: Ribbon cable for cascading two PX8500-G
-

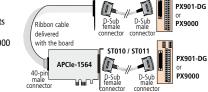


Pin assignment – 37-pin D-Sub male connector

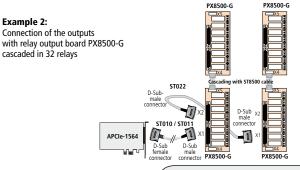
Counter input 1		Counter input 0	Die esteut 1	Outputs	Diq. output 0
Counter input 1 Dig. input 3 Dig. input 7 Dig. input 7 Dig. input 7 Dig. input 19 Dig. input 11 Dig. input 15 GND 0 GND 3 Dig. input 15 Dig. input 17 Dig. input 23 Dig. input 23 Dig. input 23 Dig. input 23 Dig. input 23 Dig. input 23	20 • 1 2 21 • 2 3 4 23 • 4 4 24 • 5 • 6 6 26 • 7 7 27 • 7 8 • 9 0 10 • 12 2 30 • 1 10 10 31 • 12 3 33 • 15 15 33 • 16 16 33 • 19 19 33 • 19 19 33 • 19 19 34 • 19 19 35 • 16 19 37 • 19 19 19 37 • 19 19 37 • 19	Counter input 2 Dig. input 4 Dig. input 6 Dig. input 6 Dig. input 10 Dig. input 10 Dig. input 10 Dig. input 10 Dig. input 14 GND 1 Dig. input 14 Dig. input 16 Dig. input 20 Dig. input 20 Dig. input 20 Dig. input 24 Dig. input 24 Dig. input 24 Dig. input 24 Dig. input 26 Dig. input 23	Dig. output 1 Dig. output 3 Dig. output 5 Dig. output 5 Dig. output 1 Dig. output 1 Dig. output 13 Dig. output 13 Dig. output 13 Dig. output 13 Dig. output 10 Dig. output 23 Dig. output 23 Dig. output 29 Dig. output 29 Dig. output 29 Dig. output 29 Dig. output 29 Dig. output 21	20 - 21 - 22 - 33 - 25 - 6 - 77 - 8 - 9 - 11 - 31 - 32 - 33 - 34 - 37 -	Ing. output 0 big. output 2 big. output 4 big. output 8 big. output 8 big. output 10 big. output 10 big. output 12 24 V ext. GND big. output 14 24 vext. Dig. output 16 big. output 18 big. output 18 big. output 20 big. output 22 big. output 23 big. output 24 big. output 25 big. output 24 big. output 25 big. output 26 big. output 26 big

ADDI-DATA connection

- Example 1: Connection of the inputs
- (Ribbon cable)
- Connection of the outputs through screw terminal panel PX901-DG or PX9000



ST010 / ST011



Ordering information

* Preliminary product information



PCI >>

Digital I/O board, optically isolated, 64 digital inputs and outputs, 5 V, for PCI Express





PCI >> express





Also for **PCI** See APCI-1564, page 152







Features

Inputs

- 32 optically isolated inputs, 5 V, incl. 16 interruptible inputs
- Channels 0–2 can be used as 32-bit counter inputs (up to 500 kHz)
- Reverse voltage protection
- All inputs are filtered

Outputs

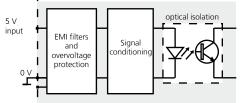
- 32 optically isolated outputs, 5 V
- APCI-e1564-5V: Open Collector outputs APCIe-1564-5V-HS: High-Side outputs
- Output current per channel 50 mA
- Watchdog for resetting the outputs to "0"
- At Power-On, reset of the outputs to "0"
- Current limit: ~0.5 A per 8 channels (through PTC)
- Short-circuit current per output ~0.5 A typ.
- Self-resetting fuse (electronic fuse)
- Overtemperature and overvoltage protectionExternal voltage supply screened and filtered
- Timer / Watchdog / Counter
 2 timers (12-bit), of which one can be used as a watchdog
- 3 counter (32-bit)

Safety features

- Optical isolation 1000 V
- Creeping distance IEC 61010-1
- Protection against fast transients (burst), overvoltage, electrostatic discharge and high-frequency EMI
- Separate ground line for inputs and outputs

Protective circuit for the input channels

Peripherals | Board



* Preliminary product information

APCIe-1564-5V / APCIe-1564-5V-HS

PCI Express interface

32 digital inputs, 5 V,

including 16 interruptible inputs

32 digital outputs, 5 V, 50 mA/channel,

Open Collector (5V) or High-Side (5V-HS)

Optical isolation 1000 V

Input and output filters

Connection through industry-standard

D-Sub connector

Applications

- Industrial I/O control
- PLC coupling
- Signal switching
- Interface to electromechanical relays
- Automatic test equipment
- ON/OFF monitoring of motors, lights...
- Watchdog timer
- ...

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

• C#.NET, C

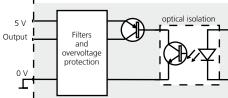
On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads

Protective circuit for the output channels

Peripherals Board

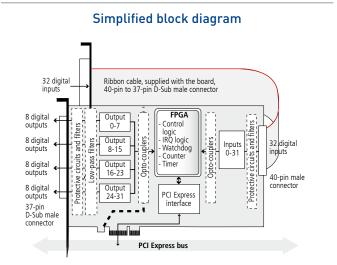




Specifications*

•	
Digital inputs	
Number of inputs:	32 digital inputs,
(common ground	channel 0-2 can be used as 32-bit counter inputs
acc. to IEC 1131-2)	(up to 500 kHz)
	16 channels (channel 4 to 19)
Interruptible inputs:	
Optical isolation:	1000 V through opto-couplers, from PC to peripheral
Nominal voltage:	5V
Input current:	Channel 0-3: 8.5 mA at 5 V, typ.
	Channel 4-31: 5.9 mA at 5 V, typ.
Input frequency (max.):	Channel 0-3: 500 kHz at 5 V
	Channel 4-31: 5 kHz at 5 V
Logic input levels:	UH (max.): 6 V / 7.8 mA typ. (channel 4-31)
	UH (min.): 4 V / 4.1 mA typ. (channel 4-31)
	UH (max.): 6 V / 11.5 mA typ. (channel 0-3)
	UH (min.): 4 V / 5.6 mA typ. (channel 0-3)
	UL (max.): 2 V / 0.8 mA typ. (channel 4-31)
	UL (max.): 2 V / 1.1 mA typ. (channel 0-3)
	UL (min.): 0 V / 0 mA typ.
Filters/protective circuit:	Input filters, transil diode,
	RC filters, Z diode, opto-couplers
Digital outputs	
Number of outputs:	32 digital outputs
Output type:	APCIe-1564-5V: Open Collector
	APCIe-1564-5V-HS: High-Side (load to ground)
	acc. to IEC 1131-2
Optical isolation:	1000 V (through opto-couplers), from PC to peripheral
Nominal voltage:	5 V
Supply voltage range:	5 to 12 V (5V-Version)
	5 to 35 V (5V-HS-Version)
Current limit:	0.5 A (typ.) per 8 channels (through PTC)
Output current per output:	50 mA (typical)
Short-circuit current per output:	0.5 A (typ.)
Timer/watchdog	
Watchdog:	12-bit, programmable as timer from 1 μs to 4095 s
Safety	
Watchdog:	For resetting the outputs to "0"
Common diagnostics:	For all 16 channels at overtemperature of one channel
	Tor an To channels at overtemperature of one channel
EMC – Electromagn	etic compatibility
a certified EMC laboratory in acc 61326). The limit values as set ou	rropean EMC directive. The tests were carried out by ordance with the norm from the EN 61326 series (IEC It by the European EMC directive for an industrial The respective EMC test report is available on request.
Physical and enviror	mental conditions
Dimensions:	168 x 99 mm
Suctom buci	Acc. to DCL Express base specification

Dimensions:	168 x 99 mm
System bus:	Acc. to PCI Express base specification,
-	Revision 1.0a (PCI Express 1.0a)
Space required:	1-/4-lane PCI Express slot
Operating voltage:	+ 3.3 V from PC
Current consumption:	Inputs and outputs inactive 340 mA ± 10 %, typical
	inputs and outputs active 590 mA \pm 10 %, typical
Front connector:	37-pin D-Sub male connector
Temperature range:	0 to 60 °C (with forced cooling)



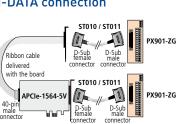
Pin assignment – 37-pin D-Sub male connector

Inputs			Outputs	
Counter input 1 Dig. input 3 Dig. input 7 Dig. input 7 Dig. input 7 Dig. input 7 Dig. input 12 Dig. input 13 Dig. input 13 CMD 0 Dig. input 15 Dig. input 15 Dig. input 17 Dig. input 21 Dig. input 21 Dig. input 23 Dig. input 23 Dig. input 23 Dig. input 23 Dig. input 23 Dig. input 23 Dig. input 27 Dig. input 29 Dig. input 31 Dig. input 29 Dig. input 31 Dig. input 31	Counter input 0 Counter input 2 Dig. input 4 Dig. input 8 Dig. input 8 Dig. input 10 Dig. input 12 Dig. input 12 Dig. input 14 GND 1 GND 2 Dig. input 16 Dig. input 18 Dig. input 20 Dig. input 22 Dig. input 22 Dig. input 22 Dig. input 28 Dig. input 28 Dig. input 28 Dig. input 30 Not connected	Dig. output 1 Dig. output 3 Dig. output 5 Dig. output 7 Dig. output 17 Dig. output 13 Dig. output 13 Ext. voltage* GND Dig. output 17 Dig. output 19 Dig. output 23 Dig. output 23 Dig. output 25 Dig. output 29 Dig. output 29 Dig. output 29 Dig. output 29 Dig. output 29	20 1 1 21 3 3 22 3 4 23 4 5 24 5 6 25 6 7 28 9 9 29 11 12 30 112 13 31 12 13 32 14 15 35 16 17 37 19 19	Dig. output 0 Dig. output 2 Dig. output 4 Dig. output 6 Dig. output 6 Dig. output 8 Dig. output 10 Dig. output 12 Dig. output 14 Ext. voltage* GND Dig. output 14 Dig. output 16 Dig. output 16 Dig. output 20 Dig. output 22 Dig. output 22 Dig. output 22 Dig. output 22 Dig. output 22 Dig. output 23 Dig. output 26 Dig. output 28 Dig. output 28 Dig. output 28 Dig. output 28
		* 5V-version (O 5V-HS-versior	pen Collector): n (High-Side):	5 to 12 V 5 to 35 V

ADDI-DATA connection

Connection of the inputs (ribbon cable) through screw terminal panel Connection of the outputs

through screw terminal



Ordering information

APCIe-1564-5V / APCIe-1564-5V-HS

Digital I/O board, optically isolated, 64 digital inputs and outputs, 5 V, for PCI Express. Incl. ribbon cable, technical description and software drivers.

Versions

APCIe-1564-5V: 64 digital I/O, 5 V, Open Collector outputs APCIe-1564-5V-HS: 64 digital I/O, 5 V, High-Side outputs

Accessories

panel

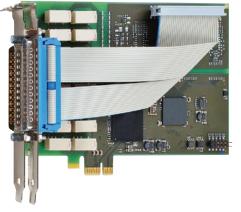
PX901-ZG: Screw terminal panel for DIN rail ST010: Standard round cable, shielded, twisted pairs, 2 m ST011: Standard round cable, shielded, twisted pairs, 5 m

* Preliminary product information



Relay board, optically isolated, 8/16 relays, 8/16 digital inputs, 24 V







Also for PCI see APCI-2200, page 162











Features

Relays

- 8 or 16 electromechanical relays with change-over contacts
- Max. switching voltage for the relays: 200 VDC, 200 VAC
- Max. switching capacity: 60 W, max. 2 A
- Short response time
- Watchdog: switched on/off through software

Digital inputs

- 8 or 16 inputs, optically isolated,
- incl. 7 or 15 interruptible inputs
- Input voltage 24 V

Safety features

EMC tested

- Watchdog activity can be read back
- Optical isolation of the relays
- Creeping distance IEC 61010-1

Applications

- Industrial digital I/O controlling
- Automatic test equipment
- Signal switching
- Interface to electromechanical relays
- ON/OFF monitoring of motors, lights...
- Alarm monitoring
- Machine interfacing
- ...

APCIe-2200

8 or 16 relay output channels

Max. switching voltage 200 VDC, 200 VAC

max. switching current 2 A

8/16 digital inputs 24 V,

incl. 7/15 interruptible inputs

Optical isolation 1000 V

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

- .NET
- Microsoft VC++ Borland C++
- Visual Basic
 Delphi
- LabVIEW
 LabWindows/CVI

ADDIPACK functions:

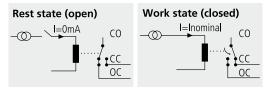
- Digital input Digital output
- Watchdog Timer

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads

Function principle of the relays



CO: Change-over contact **CC:** Closing contact **OC:** Opening contact



PCI >>

Specifications

Relays	
Type of contacts:	8/16 change-over
Max. switching voltage:	200 VDC, 200 VAC
Max. switching current:	2 A
Max. switching capacity:	60 W
Contact resistance:	< 100 mΩ
Contact material:	Ag and Au plated
Response time:	Max. 4 ms, typ. 2.5 ms
Release time:	Max. 4 ms, typ. 0.9 ms
Mechanical life:	10 ⁸ operations
Electrical life:	10 ⁵ operations at rated load

Digital inputs

Number of inputs:	8/16
	incl. 7/15 interruptible inputs
Optical isolation:	Through opto-couplers, 1000 V
Nominal voltage:	24 V
Input current:	5 – 8 mA
Signal delay:	70 μs (at 24 V)
Maximal input frequency:	10 kHz (at 24 V)

Timer

Tir

Time settings:	16-bit, programmable, 1 µs to 65535 s
Safety	
Test voltage:	1000 V
Watchdog:	For resetting the outputs to "0": 12-bit, programmable, 1 µs to 4095 s

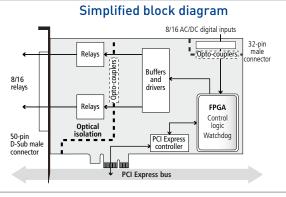
EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and environmental conditions

i nysicat ana chimo	
Dimensions:	149 x 99 mm
System bus:	Acc. to PCI Express base specification,
	Revision 1.0a (PCI Express 1.0a)
Space required:	1-/4-lane, 1 PCI Express slot
Operating voltage:	+3.3 V from the PC
Max. current consumption:	1 A ±10 % (typ. APCIe-2200-16-16)
Front connector:	50-pin D-Sub male connector
Additional connector:	32-pin male connector.
	APCIe-2200-16-8/APCIe-2200-16-16:
	Connection with delivered ribbon cable.
	Connects the board to a bracket
	with a 37-pin D-Sub male connector.
	For connecting the PX901-ZG.
Temperature range:	0 up to 60 °C (with forced cooling)





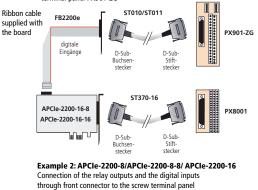
Pin assignment – 50-pin D-Sub connector APCIe-2200-16-8/APCIe-2200-16-16

Pin		Pin		_	\sim			Pin
34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50	OC of relay 0 OC of relay 1 OC of relay 2 OC of relay 2 OC of relay 2 OC of relay 4 OC of relay 6 OC of relay 6 OC of relay 7 OC of relay 10 OC of relay 11 OC of relay 12 OC of relay 13 OC of relay 13 OC of relay 15 -	18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33	CC of relay 0 CC of relay 1 CC of relay 2 CC of relay 3 CC of relay 3 CC of relay 5 CC of relay 5 CC of relay 7 CC of relay 7 CC of relay 8 CC of relay 10 CC of relay 11 CC of relay 11 CC of relay 13 CC of relay 13 CC of relay 13	34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50	18 18 18 18 18 18 18 18 18 18	1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17	CO of relay 10 CO of relay 11 CO of relay 12 CO of relay 13 CO of relay 14	1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17

OC: Opening contact CC: Closing contact CO: Change-over contact

ADDI-DATA connection

Example 1: APCIe-2200-16-8/ APCIe-2200-16-16 - Connection of the relay outputs through screw terminal panel PX8001 - Connection of the digital inputs through ribbon cable to the screw terminal panel PX901-ZG





Ordering information

APCIe-2200

Relay board, optically isolated, 8/16 relays, 8/16 digital inputs, 24 V. Incl. technical description and software drivers.

APCIe-2200-16-16:	16 relays, 16 dig. inputs, with ribbon cable
4.0.01	for the connection of the digital inputs
APCIe-2200-16-8:	16 relays, 8 dig. inputs, with ribbon cable for the connection of the digital inputs
	5 1
APCIe-2200-8-8:	8 relays, 8 digital inputs, 24 V
APCIe-2200-16:	16 relays
APCIe-2200-8:	8 relays

Accessories

- PX8001: 3-row screw terminal panel for DIN rail ST370-16: Shielded round cable, 2 m PX901-ZG: Screw terminal panel for DIN rail



Multifunction counter board, optically isolated, fast counter inputs - programmable functions, for PCI Express





Also for **PCI** see APCI-1710 page 166

Also for *CompactPCI*TM see CPCI-1710 page 234





LabVIEW™

The board APCIe-1711 is a fast multifunction and multichannel counter board for the PCI Express bus. The strengths of this board are its wide range of applications and high precision and reliability in harsh industrial environment.

With this board you can realise many different applications on the same hardware base. The board is supplied with a pool of functions which provides the user with maximum efficiency yet minimum space and parts requirement. The functions are individually configured for each channel through the supplied software. The flexible programming facilities on this board allow many different user applications to be quickly and easily developed and reconfigured as further requirements arise. Thanks to the FPGA board structure, further counting applications can be realised through software adaptation. Contact us!

Features

- 32-bit data access
- RS422 driver with max 5 MHz max.
- (10 MHz for the APCIe-1711-10MHz without ESD protection)
- With RS422/TTL input/output signals (APCIe-1711) or 24 V input signals (APCIe-1711-24 V)
- Four onboard function modules
- Functions
- Incremental counter for the acquisition of incremental encoders (90° phase-shifted signals)
- BiSS-Master (B and C mode)
- SSI Synchronous Serial Interface. The SSI function is an interface for systems which allow an absolute position information via serial data transfer.
- Counter/timer (82C54)
- Pulse acquisition
- Frequency measurement
- Pulse width modulation (PWM)
- Period duration measurement
- Velocity measurement
- Digital inputs and outputs
- Edge time measurement (ETM)
- Parallel interface
- Sin/Cos (1 Vpp, 11 App)
- EnDat 2.2
- Customised functions
- Available channels on one function module
- 4 channels, programmable either as digital inputs or outputs, optically isolated, RS422

* Preliminary product information 3 channels, digital inputs, optically isolated, 24 V

info@addi-data.com

www.addi-data.com

1 digital power output, optically isolated, 24 V

APCIe-1711

Available functions:

Incremental counter, SSI Synchronous Serial Interface, counter/timer, pulse acquisition, frequency, pulse width, period duration, velocity measurement, PWM, BiSS-Master, digital inputs and outputs, Sin/Cos, EnDat 2.2 ...

Function selection through software

Optical isolation

Inputs and outputs: RS422, TTL, 24 V

Customised functions

Additional channels

• 28 TTL I/O, without optical isolation

Versions	RS422/ TTL- I/O	24 V inputs	5 V inputs	24 V outputs	TTL I/O
APCIe-1711	16	12	-	4	28
APCIe-1711-24V	-	28	-	4	28
APCIe-1711-5V-I	16	-	12	4	28
APCIe-1711-10MHz	16	12	-	4	28

Safety features

- Creeping distance IEC 61010-1
- Optical isolation 1000 V
- Noise neutralisation of the PC supply

Applications

- Event counting
- Position acquisition
- Motion control
- Batch counting
- ...

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

Microsoft VC++ • Borland C++ 5.01

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads



Wide range of applications through the free combination of functions

4 function modules quickly and easily programmable with numerous functions

Each of the four modules is programmed with one function. You can program 4 times the same function or freely combine 4 different functions.

Configuration example 1						
Function module 0	Function module 1	Function module 2	Function module 3			
Incremental counter	Incremental counter	Pulse acquisition	Counter/Timer			
Configuration example 2						
Function module 0	Function module 1	Function module 2	Function module 3			

Programmable onboard modules

Each module can be programmed with the function of your choice. You can operate simultaneously up to 4 different functions on one board. If your application must be modified, you can load a new function quickly and easily.

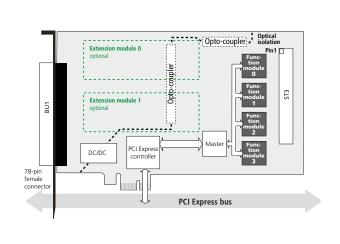
Overview of signal generators resp. functions

Application	Max. number of signal generators or functions for each function module	Max. number of function modules for each APCIe-1711	Max. number of signal generators or functions of each APCIe-1711	Page
Incremental counter	1 (32-bit) or 2 (16-bit)	4	4 or 8	168
SSI	3	4	12	168
Chronos	1	4	4	169
BiSS-Master	6	4	24	130
Counter/Timer	3	4	12	170
TOR	2	4	8	171
Pulse acquisition	4	4	16	172
PWM	2	4	8	172
ЕТМ	2	4	8	173
Digital I/O	8	4	32	173
TTL	24	1	24	-
Parallel Interface	1	4	1	130
Sin/Cos*	2	2	4	132
EnDat 2.2	2	4	8	131

*Extension module (EM) is required

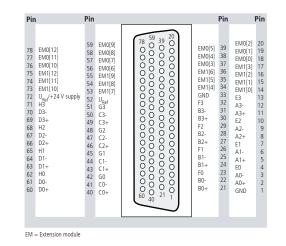


Customer-tailored modifications, designed to suit your needs. Hardware and software, firmware, PLDs, ... Contact us!

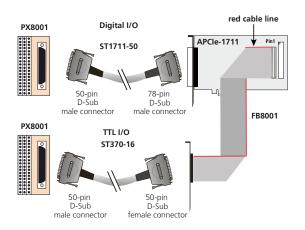


Simplified block diagram

Pin assignment – 78-pin D-Sub female connector



ADDI-DATA connection





Function Parallel Interface



With the **Parallel Interface** function, the digital inputs of the APCle-1711 are acquired parallelly. Up to 28 digital inputs, 24 V, can be acquired with the APCle-1711-24 V. Up to 16 R5422 and 12 24 V digital inputs can be acquired with the APCle-1711.

There are different methods for the acquisition of the inputs:

- Timer controlled (max. resolution 1 μ s = 1 MHz)
- Digital input (by masking the digital inputs,
- rising or falling edge)Software

This function can be loaded up to 4 times for each APCle-1711, i.e. it is possible to acquire 8-bit (7 inputs), 16-bit (14 inputs), 24-bit (21 inputs) or 32-bit (28 inputs) parallelly. If an external trigger signal is used (maskable, rising or falling edge) then there is no need to use one of the inputs for triggering.

The data is transferred directly via DMA in the RAM of the PC. If the **Parallel Interface** function is loaded on all function modules, then up to 28 digital inputs (RS422 / 24 V) are available.

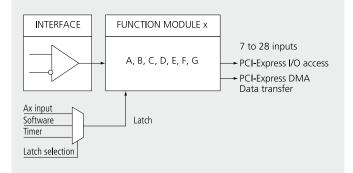
Used signals

Pin name	Signal type	Function
Ax +/-	24 V*/ RS422	Digital input
Bx +/-	24 V*/ RS422	Digital input
Cx +/-	24 V*/ RS422	Digital input
Dx +/-	24 V*/ RS422	Digital input
Ex	24 V	Digital input
Fx	24 V	Digital input
Gx	24 V	Digital input

The 24 V switching level can be adjusted optionally down to 1 V

x: Number of the module (See pin assignment page 129) * 24 V for the APCIe-1711-24 V

Block diagram Parallel Interface



Function BiSS-Master

The **BiSS-Master** function is a **bi**directional **s**ensor interface for the communication with up to 6 sensors. BiSS B and C are supported. Features of the BiSS-Master function:

- 1 function module with a maximum amount of 6 sensors which can be assigned freely to 1 or 2 channels.
- Sensor data transmission
- Register data transmission

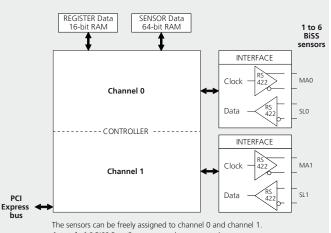
More information about the function range of the BiSS interface on www.biss-interface.com.

Used signals

Signal name	Pin name	Signal type	Function
Output_Ch0_x	Ax +/-	RS422	Dig. output 0 (clock line from master to slave) MA 0
Input_Ch0_x	Bx +/-	RS422	Dig. input 0 (data line from slave to master) SL 0
Output_Ch1_x	Cx +/-	RS422	Dig. output 1 (clock line from master to slave) MA 1
Input_Ch1_x	Dx +/-	RS422	Dig. input 1 (data line from slave to master) SL 1

x: Number of the module (See pin assignment page 129)

Block diagram BiSS-Master



A total of 6 BiSS B or C sensors can be connected.







EnDat 2.2 is a bidirectional synchronous-serial interface for position measurement devices. This interface allows the reading out of absolute position values and parameters, the writing of status and initialisation registers and the transfer of additional information about the position value. Furthermore, the **EnDat 2.2** function modules support the analysis of diagnostic values and access to the OEM memory. Data transfer is effected serially.

On one board you can use up to 8 **EnDat 2.2** sensors (2 sensors per function module). Each function module has its own clock pulse line (B or D) and data line (A or C).

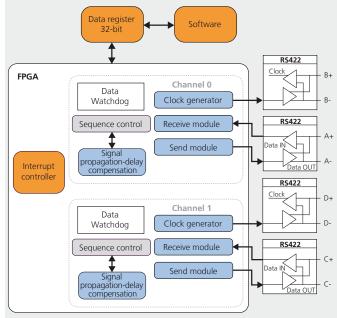
The function EnDat 2.2 is only available for the APCle-1711! (not for the APCle-1711-24V and APCle-1711-5V-I)

Used signals

Channel	Signal name	I/O	Pin name	Function
0	CLK_0+	0	Bx +	Clock pulse line
0	CLK_0-	0	Bx -	Clock pulse line
0	DATA_0+	I/O	Ax +	Data line
0	DATA_0-	0	Ax -	Data line
1	CLK_1+	0	Dx +	Clock pulse line
1	CLK_1-	0	Dx -	Clock pulse line
1	DATA_1+	I/O	Cx +	Data line
1	DATA_1-	0	Cx -	Data line

x: Number of the module (See pin assignment page 129)

Block diagram EnDat 2.2



Up to 8 EnDat 2.2 sensors can be connected (2 per function module)!

EnDat 2.

Application example

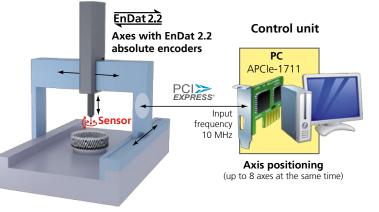
Exact positioning of axes for the regulation of surface measurement devices for rotationally symmetric parts (e.g. gear wheels)

Challenge

For the measurement of the surfaces of rotationally symmetric parts numerous axes must be positioned. Furthermore the signals must be fastly transferred in order to detect the position as exactly as possible. To safe time, absolute encoders are used because they do not need any reference runs when started.

Solution

The measurement device consists of a measurement table with a gate. The rotationally symmetric parts are fixed on the measurement table and their surface is tested with a sensor connected to the gate. To move the sensor around the parts the gate has several axes equipped with EnDat 2.2 absolute encoders. The precision of the axis position is assured by the PCI Express counter board APCle-1711: Thanks to its high input speed of 10 MHz (optional APCle-1711-10MHZ version) and its resistance to interferences, the board is able to move the axes precisely even at high speed. Measurement table with gate





Function Sin/Cos



With the function Sin/Cos, up to 4 Sin/Cos sensors can be used on one board (function module 0 or 1 as well as 2 or 3). The extension module EM-SINCOS-1V_{DD} is meant for the connection of signals with 1 V_{DD} , the EM-SINCOS-11µApp is able to acquire 11 µApp signals. A signal period of the Sin/Cos signal is divided in a predefined number of steps, depending on the chosen resolution. The maximum input frequency of the counter input also depends on the chosen resolution.

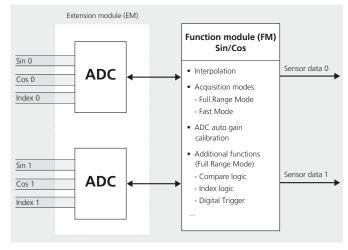
Please note: The function Sin/Cos can only be used with the extension module EM-SINCOS.

Used signals

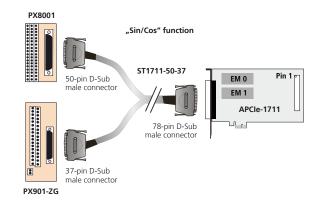
Extension module 0					
Signal	Sensor 1 Pin no.*	Sensor 2 Pin no.*			
Sin+	18	57			
Sin-	38	76			
Cos+	19	58			
Cos-	39	77			
Index+	20	59			
Index-	56	78			
24 V Digital Input	37	37			
Extension module 1					
Sin+	14	53			
Sin-	34	73			
Cos+	15	54			
Cos-	35	74			
Index+	16	55			
Index-	36	75			
24 V Digital Input	17	17			

* 78-pin D-Sub female connector (pin assignment see page 129)

Block diagram Sin/Cos



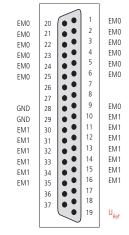
ADDI-DATA connection



Pin assignment – 37-pin D-Sub male connector

Pin	Pin	[Pin
34 +24 V supply 35 HO 36 H1 37 H2 38 H3 39 EO 40 E1 41 E2 42 E3 43 FO 44 F1 45 F2 46 F3 47 GO 48 G1 49 G2 50 G3	Hunction module Hunction module Huncti	34 -18 1 35 - 2 36 - 3 37 - 4 38 - 5 39 - 6 40 - 7 41 - 8 42 - 9 43 - 11 45 - 12 46 - 13 47 - 14 48 - 14 49 - 16 50 - 33 17	GND 1 A0+ 2 A0- 3 B0+ 4 B0- 5 C0+ 6 C0- 7 D0- 9 A1+ 10 L1 111 B1+ 12 D1+ 16 D1- 17

Pin assignment – 50-pin D-Sub male connector



EM = Extension Module



Specifications*

Free programming of the functions

- Acquisition of incremental encoders (1 x 32-bit or 2 x 16-bit)
- SSI (max. 3 encoders per module)
- Counter/Timer (3 counters similar to 82C54) Pulse counter (4 x 32-bit counters per module)
- Chronos (chronometer)
- TOR (pulse counter with time slices, ...)
- Digital I/O (8 I/O, 24 V, TTL, RS422)
- PWM (pulse width modulation, 2 x per module)
- BiSS-Master (B and C mode) ETM (Timer interface for period duration measurement, edge time, ...) TTL (TTL I/O without isolation)
- Parallel Interface
- EnDat 2.2 Sin/Cos
- Customised functions

Signals

Digital I/O signals, TTL or RS422, 24 V

Inputs

mparo					
Differential inputs or outpu	ts (A, B, C, D)				
Differential inputs, RS422:	16 (can be used as inputs or outputs)				
Nominal voltage:	3.3 VDC				
Common mode range:	+12 / -7 V				
Input sensitivity:	200 mV				
Input hysteresis:	50 mV				
Input impedance:	12 kΩ				
Terminal resistor:	120 Ω (not supplie				
Max. input frequency:	APCIe-1711: 5 MHz (at nominal voltage)				
		lz: 10 MHz (at nominal voltage)			
Mass-related inputs, 24 V (E	, F, G):				
Number of inputs:	12				
Nominal voltage:	24 VDC				
Logic input levels:	Unominal:	24 V			
	UH max.:	30 V			
	UH min.:	19 V			
	UL max.:	14 V			
	UL min.:	0 V			
Maximal input frequency:	1 MHz (at nomina	l voltage) depending on the function			

Outputs

outputo	
Nominal voltage:	3.3 VDC
Maximum output frequency:	5 MHz (diff. outputs)
Max. number of outputs:	16 (if they are not used as diff. inputs)
Digital outputs, 24 V (H)	
Output type:	High-side (load to ground)
Number of outputs:	4
Nominal voltage:	24 VDC
Supply voltage range.	4.75 V to 35 VDC (via 24 V ext. pin)
Maximum current:	90 mA per output / 270 mA for all outputs (PTC)
Overtemperature:	165 °C (all outputs switch off)

Technical data APCIe-1711-24 V version

	24 V inputs (Chann This board version connection of 24 V	is intended for the		
	Only 24 V signals can be connected to the inputs.			
Nominal voltage:	24 VDC			
Max. input frequency:	1 MHz (at nominal	voltage) depending on the function		
Logic input levels :	Unominal:	24 V		
(Standard)	UH max.:	30 V		
	UH min.:	18 V		
	UL max.:	16 V		
	UL min.:	0 V		
Functions				
On the board APCIe-1711-24V Av as outputs. Therefore not any fun Available functions:		,		

Available functions: Incremental counter - Sin/Cos

Partially available: - PWM

Please find more detailed information in the respective function manual.

Safety

Optical isolation: 1000 V

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

PC system requirements and environmental conditions

Dimensions:	168 x 98 mm
System bus:	Acc. to PCI Express base specification,
	Revision 1.0a (PCI Express 1.0a)
Space required:	1-/4-lane PCI Express slot
Operating voltage:	+ 3.3 V / + 12 V from the PC
	+24 V ext.
Current consumption APCIe-1711	: 3.3 V / 341 mA
	12 V / 76 mA
	typ.
Front connector:	78-pin D-Sub female connector
Additional connector:	50-pin D-Sub male connector
Temperature range:	0 to 60 °C (with forced cooling)

Ordering information

APCIe-1711

Multifunction counter board, optically isolated, fast counter inputs - programmable functionality, for PCI Express. Incl. technical description and software drivers.

APCle-1711: APCle-1711-2 APCle-1711-5 APCle-1711-1	24V: 5V-I:	Isolated counter board with programmable functi 24 V instead of RS422 (A, B, C, D). 5 V inputs (E, F, G) instead of 24 V Input frequency 10 MHz, Inputs (A, B, C, D)	onality	
Option			For the Sin/Cos func	tion
Opt. 5V:		5 V outputs (H1, H2, H3, H4) instead of 24 V	EM-SINCOS-11µAPP:	Extension module,
				2 x 11 µA _{pp} inputs, 1 dig. output, 24 V
Accessories	5		EM-SINCOS-1VPP:	Extension module,
PX8001: 3-	-row screv	v terminal panel with housing for DIN rail		2 x 1 V _{pp} inputs, 1 dig. output, 24 V
ST1711-50: St	tandard ro	ound cable, shielded, twisted pairs, 2 m,	ST1711-50-37:	Y-cable, round, shielded, twisted pairs,
78	8-pin male	e connector to 50-pin male connector		78-pin D-Sub male connector to 50-pin D-Sub male connector and 37-pin D-Sub male connector
For the TTL I/	O functio	'n		connector and 57-pin D-500 male connector
ST370-16: St	tandard ro	ound cable, shielded, twisted pairs, 2 m		
FB8001: R	ibbon cab	le		

* Preliminary product information



Multifunction board, optically isolated, 16 SE/8 differential inputs, 4/8 analog outputs, 16-bit







Also for **PCI** see APCI-3120, page 180 Also for **CompactPCI**TM see CPCI-3120, page 238













Features

Analog inputs

- 16 single-ended/8 differential inputs
- 16-bit resolution
- Optical isolation 500 V
- Throughput: 100 kHz
- Input ranges: 0-10 V, ±10 V, 0-5 V, ±5 V,
 0-2 V, ±2 V, 0-1 V, ±1 V, 0-20 mA (option) freely programmable through software for each channel
- Gain PGA x1, x2, x5, x10 freely programmable through software for each channel
- PCI Express DMA for analog data acquisition
- Overvoltage protection
- Input filters: 159 kHz
- Analog acquisition
- One single channel, several channels, several channels
 through scan list
- Automatic analog acquisition through cyclic timer control
- Acquisition through scan list: up to 16 entries with gain, channel, unipolar/bipolar
- Acquisition triggered through software, timer, external event
- Trigger functions: Software trigger or external trigger: the analog acquisition (single or sequence) is started through signal switching from 0 V to 24 V at the digital input 0.
- Interrupt: end of single channel, end of multichannel, end of scan list

Analog outputs

- 4 or 8 analog outputs, optically isolated 500 V
- Voltage or current outputs
- 16-bit resolution (15-bit for 0-10 V)
- Output voltage: ±10 V, 0-10 V (through software)
- Output voltage after reset: 0 V
- Each output has its own ground line (without optical isolation)
- Output current ±5 mA max. for voltage outputs
- Current outputs: 0-20 mA, min. load 10 Ω, max. load 560 Ω, at 20 mA
- EMI filters

Digital

- 4 dig. inputs including 1 interruptible input
- 4 dig. outputs, 24 V, optically isolated

APCIe-3121 / APCIe-3123

PCI Express interface

16 single-ended/

8 differential inputs, 16-bit

8/4 analog outputs, 16-bit

Optical isolation of inputs and outputs, 500 V

PCI Express DMA, programmable gain

Trigger functions

8 digital I/O, 24 V, optically isolated, timer, watchdog

Timer

• 2 timers, incl. 1 which can be used as a watchdog

Safety features

- Optical isolation 500 V min.
- Creeping distance IEC 61010-1
- Overvoltage protection ± 40 V, analog inputs
- Protection against high-frequency EMI
- Input filters: 159 kHz
- Noise neutralisation of the PC supply

Applications

- Industrial process control
- Industrial measurement and monitoring
- Multichannel data acquisition
- Control of chemical processes
- Factory automation
- Acquisition of sensor data, current measurement
- Laboratory equipment, instrumentation

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

- .NET
- Microsoft VC++ Borland C++
- Visual Basic Delphi
- LabVIEW LabWindows/CVI

ADDIPACK functions

- Analog input Analog output Digital input
- Digital output Watchdog Timer

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads



Specifications

Analog inputs	
Number of inputs:	16 single-ended/8 differential inputs or 8 single-ended/4 differential inputs
Resolution:	16-bit
Optical isolation:	500 V through opto-couplers from PC to peripheral
Input ranges:	0-10 V, ±10 V, 0-5 V, ± 5 V, 0-2 V, ± 2 V, 0-1 V, ± 1 V,
	0(4)-20 mA (optional),
	software-programmable for each channel
Throughput:	100 kHz
Gain:	Software programmable (1, 2, 5, 10)
Relative precision (INL):	± 2 LSB max. (A/D converter)
Diff. non-linearity (DNL):	± 1 LSB max. (A/D converter)
Bandwidth (- 3 dB):	Limited to 159 kHz with low-pass filter
Trigger:	Through software, timer, external event (24 V input)
Data transfer:	Data to the PC through FIFO memory,
	I/O commands, interrupt at EOC (End Of Conversion)
	and EOS (End of Scan), DMA transfer at EOC
Interrupts:	End of conversion, at timer overrun, End of scan

Analog outputs

Number of outputs:	8 or 4
Resolution:	16-bit
Optical isolation:	500 V through opto-couplers
Output range:	0-10 V, ±10 V switchable through software
	(0-20 mA optional)
Overvoltage protection:	±15 V
Max. output current / load:	±5 mA, 2 kΩ
Short-circuit current:	±35 mA (short time)
Output voltage after reset:	0 V
Current outputs	
Resolution:	15-bit
Output range:	0-20 mA
LSB:	610.35 nA
Load (at 20 mA):	10 Ω min., 560 Ω max.
Output current after reset:	0 mA

Digital I/O

Number of I/O channels:	4 dig. inputs, 4 dig. high-side outputs, 24 V
Optical isolation:	1000 V through opto-couplers
Input current at 24 V:	10 mA typ.
Input range:	0-30 V
Supply voltage:	8-32 V
Max. switching current:	65 mA typ.

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and environmental conditions

Dimensions:	168 x 99 mm
System bus:	Acc. to PCI Express base specification,
	Revision 1.0a (PCI Express 1.0a)
Space required:	1-/4-lane PCI Express slot
Operating voltage:	+ 3.3 V, + 12 V from PC
Front connector:	37-pin D-Sub male connector
Temperature range:	0 to 60 °C (with forced cooling)

APCIe-3121 / APCIe-3123

Multifunction board, optically isolated, 16 SE/8 differential inputs, 4/8 analog outputs, 16-bit, for PCI Express. Incl. techn. description and software drivers.

ST011:

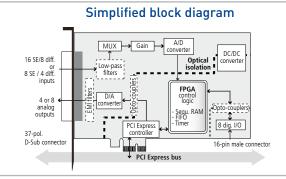
FB3000:

Versions

Voltage	
APCIe-312x-16-8	Version with 16 SE/8 diff. inputs, 8 analog outputs
APCIe-312x-16-4	Version with 16 SE/8 diff. inputs, 4 analog outputs
APCIe-312x-8-8	Version with 8 SE/4 diff. inputs, 8 analog outputs
APCIe-312x-8-4	Version with 8 SE/4 diff. inputs, 4 analog outputs

Current

APCIe-3121-16-8C Version with 16 SE/8 diff. inputs, 8 analog outputs APCIe-3121-16-4C Version with 16 SE/8 diff. inputs, 4 analog outputs APCIe-3121-8-8C Version with 8 SE/4 diff. inputs, 8 analog outputs APCIe-3121-8-4C Version with 8 SE/4 diff. inputs, 4 analog outputs



PCI >>

Pin assignment – 37-pin D-Sub male connector

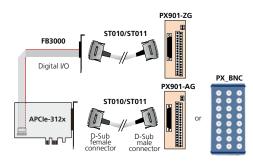
DIFF	SE			١	SE	DIFF
(+) An. input 0	(+) An, input 0	20		1	(+) An. input 8	(+) An. input 4
(+) An. input 1	(+) An, input 1	21		2	(+) An. input 9	(+) An. input 5
(+) An. input 2	(+) An, input 2	22		3	(+) An. input 10	(+) An. input 6
(+) An, input 3	(+) An, input 3	23		4	(+) An. input 11	(+) An. input 7
(-) An. input 3	(+) An. input 7	24		5	(+) An. input 15	(-) An. input 7
(-) An. input 2	(+) An. input 6	25		6	(+) An. input 14	(-) An. input 6
(-) An. input 1	(+) An, input 5	26		7	(+) An. input 13	(-) An. input 5
(-) An. input 0	(+) An. input 4	27	֥	8	(+) An. input 12	(-) An. input 4
. Analog inpi	it GND	28		9	Analog input	
Analog inpi	it GND	29		10	Analog input	GND 1
An. output	0 GND	30	••	11	Analog input	
An. output	1 GND	31	••	12	An. outpu	
An. output	2 GND	32	••	13	An. outpu	
An. output	3 GND	33	••	14	An. outpu	
An. output	4 GND	34	• •	15	An. outpu	
An. output	5 GND	35	• •	16	An. outpu	
An. output	5 GND	36	••	17	An. outpu	
An. output	7 GND	37	• •	18	An. outpu	
		1 '	•	19	An. outpu	t7

1: The analog inputs have a common ground line 2: Each analog output has its own ground line

Pin assignment - 16-pin male connector

Dig. input 2- Dig. input 1- Dig. input 0- 24 V voltage supply 24 V voltage supply	16 15 14 13 12 11 10 9 8 7 6 5	Dig. input 3+ Dig. input 2+ Dig. input 1+ Dig. input 0+ High-side output 3 (24 V) High-side output 2 (24 V)
	6 ■ 5 4 ■ 3	
GND (dig. output) GND (dig. output)	4	High-side output 1 (24 V) High-side output 0 (24 V)
and (ag. output)	2 .	right-side output 0 (24 V)

ADDI-DATA connection



Ordering information

0-1-1-

Uptions			
Please indica	Please indicate the number of channels		
Option SF:	Precision filter for 1 single-ended channel		
Option DF:	Precision filter for 1 diff. channel		
Option PC:	Current input 0(4)-20 mA for 1 channel		
	PC-SE: for single-ended PC-Diff: for differential		
Accessori	es		
PX901-A:	Screw terminal panel for connecting the analog I/O		
PX901-AG:	Same as PX901-A with housing for DIN rail		
PX_BNC:	BNC connection box for connecting the analog I/O		
PX901-ZG:	Screw terminal panel for connecting the dig. I/O		
ST010:	Standard round cable, shielded, twisted pairs, 2 m		

Standard round cable, shielded, twisted pairs, 5 m

Ribbon cable for digital I/O



Analog input board, optically isolated, 16 SE/8 differential inputs, 16-bit





Also for **PCI** see APCI-3001, page 190 and APCI-3010 / APCI-3016, page 184 Also for **CompactPCI**TM see CPCI-3001, page 240







IabVIFW™





Features

Analog inputs

- 16 single-ended/8 differential inputs
- 16-bit resolution
- Optical isolation 500 V
- Throughput: 100 kHz
- Input ranges: 0-10 V, ±10 V, 0-5 V, ±5 V, 0-2 V, ±2 V, 0-1 V, ±1 V, 0-20 mA (option) freely programmable through software for each channel
- Gain PGA x1, x2, x5, x10 freely programmable through software for each channel
- PCI Express DMA for analog data acquisition
- Overvoltage protection
- Input filters: 159 kHz
- Analog acquisition
- One single channel, several channels, several channels through scan list
- Automatic analog acquisition through cyclic timer control
- Acquisition through scan list: up to 16 entries with gain, channel, unipolar/bipolar
- Acquisition triggered through software, timer, external event
- Trigger functions: Software trigger or
- external trigger: the analog acquisition (single or sequence) is started through signal switching from 0 V to 24 V at the digital input 0.
- Interrupt: end of single channel, end of multichannel, end of scan list

Digital

- 4 dig. inputs including 1 interruptible input
- 4 dig. outputs, 24 V, optically isolated

Timer

• 1 timer

APCIe-3021

PCI Express interface

16 single-ended/

8 differential inputs, 16-bit

Optical isolation 500 V

PCI Express DMA, programmable gain

Trigger functions

8 digital I/O, 24 V, optically isolated, timer

Safety features

- Optical isolation 500 V min.
- Creeping distance IEC 61010-1
- Overvoltage protection ± 40 V, analog inputs
- Protection against high-frequency EMI
- Input filters: 159 kHz
- Noise neutralisation of the PC supply

Applications

- Industrial process control
- Industrial measurement and monitoring
- Multichannel data acquisition
- Control of chemical processes
- Factory automation
- Acquisition of sensor data, current measurement
- Laboratory equipment, instrumentation

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

- .NET
- Microsoft VC++ Borland C++
- Visual Basic Delphi
- LabVIEW LabWindows/CVI

ADDIPACK functions

- Analog input
 Digital input
- Digital output
 Watchdog
 Timer

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads



Specifications

Analog inputs	
Number of inputs:	16 single-ended/8 differential inputs or
	8 single-ended/4 differential inputs
Resolution:	16-bit
Optical isolation:	500 V through opto-couplers from PC to peripheral
Input ranges:	0-10 V, ±10 V, 0-5 V, ± 5 V, 0-2 V, ± 2 V, 0-1 V, ± 1 V,
	0(4)-20 mA (optional)
	software-programmable for each channel
Throughput:	100 kHz
Gain:	Software programmable (1, 2, 5, 10)
Relative precision (INL):	± 2 LSB max. (A/D converter)
Diff. non-linearity (DNL):	± 1 LSB max. (A/D converter)
Bandwidth (- 3 dB):	Limited to 159 kHz with low-pass filter
Trigger:	Through software, timer, external event (24 V input)
Data transfer:	Data to the PC through FIFO memory,
	I/O commands, interrupt at EOC (End Of Conversion)
	and EOS (End of Scan), DMA transfer at EOC
Interrupts:	End of conversion, at timer overrun, End of scan

Digital I/O

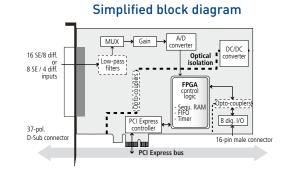
•	
Number of I/O channels:	4 dig. inputs, 4 dig. high-side outputs, 24 V
Optical isolation:	1000 V through opto-couplers
Input current at 24 V:	10 mA typ.
Input range:	0-30 V
Supply voltage:	8-32 V
Max. switching current:	65 mA typ.

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and environmental conditions

Dimensions:	168 x 99 mm
System bus:	Acc. to PCI Express base specification,
	Revision 1.0a (PCI Express 1.0a)
Space required:	1-/4-lane PCI Express slot
Operating voltage:	+ 3.3 V, + 12 V from PC
Front connector:	37-pin D-Sub male connector
Temperature range:	0 to 60 °C (with forced cooling)



Pin assignment – 37-pin D-Sub male connector

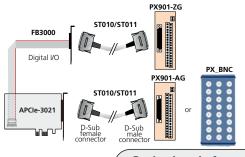
DIFF	SE	\sim	٦	SE	DIFF
(+) An, input 0 (+) An, input 1 (+) An, input 2 (+) An, input 3 (-) An, input 3 (-) An, input 1 (-) An, input 1 (-) An, input 1 (-) An, input 1 (-) An, input 0 1 {Analog in		20 21 22 23 24 25 26 27 28 29 30 31 32 33 33 34 35 36 37	1 1 2 3 4 5 6 7 7 8 9 10 11 12 13 14 15 16 17 18 19	(+) An input 9 (+) An input 9 (+) An, input 10 (+) An, input 15 (+) An, input 15 (+) An, input 13 (+) An, input 13 (+) An, input 13 Analog inp Analog inp	(-) An, input 5 (-) An, input 4 out GND out GND

1: The analog inputs have a common ground line

Pin assignment - 16-pin male connector

Dig. input 3-	16 🔳 🖿 15	Dig. input 3+
Dig. input 2-	14 🔳 🔳 13	Dig. input 2+
Dig. input 1-	12 🔳 🔳 11	Dig. input 1+
Dig. input 0-	10 🔳 🔳 9	Dig. input 0+
24 V voltage supply	8 🔳 🗖 7	High-side output 3 (24 V)
24 V voltage supply	6 🔳 🖬 5	High-side output 2 (24 V)
GND (dig. output)	4 🔳 🖬 3	High-side output 1 (24 V)
GND (dig. output)	2 🔳 🗖 1	High-side output 0 (24 V)





Ordering information

APCIe-3021

Analog input board, optically isolated, 16 SE/8 differential inputs, 16-bit. Incl. technical description and software drivers.

Versions

101010110	
APCIe-3021-16	Version with 16 SE/8 diff. inputs
APCIe-3021-8	Version with 8 SE/4 diff. inputs
APCIe-3021-4	Version with 4 SE/2 diff. inputs
Options	
Please indicate the	number of channels
Option SF:	Precision filter for 1 single-ended channel
Option DF:	Precision filter for 1 diff. channel
Option PC:	Current input 0(4)-20 mA for 1 channel
	PC-SE: for single-ended PC-Diff: for differential

Accessories

PX901-A:Screw terminal panel for connecting the analog I/OPX901-AG:Same as PX901-A with housing for DIN railPX_BNC:BNC connection box for connecting the analog I/OPX901-ZG:Screw terminal panel for connecting the dig. I/OST010:Standard round cable, shielded, twisted pairs, 2 mST011:Standard round cable, shielded, twisted pairs, 5 mFB3000:Ribbon cable for digital I/O



PCI >>

Analog output board, optically isolated, 8/4 differential outputs, 16-bit





Also for **PCI** and APCI-3501, page 192



Features

Analog outputs

- 4 or 8 analog outputs, optically isolated 500 V
- Voltage or current outputs
- 16-bit resolution (15-bit for 0-10 V)
- Output ranges: ±10 V, 0-10 V (through software)
- Output voltage after reset: 0 V
- Each output has its own ground line (without optical isolation)
- Output current ±5 mA max. for voltage outputs
- Current outputs: 0-20 mA, min. load 10 Ω, max. load 560 Ω, at 20 mA

Digital

• EMI filters

- 4 dig. inputs including 1 interruptible input
- 4 dig. outputs, 24 V, optically isolated

Timer

• 2 timers, incl. 1 which can be used as a watchdog

APCIe-3521

PCI Express interface

8/4 analog outputs, 16-bit

Optical isolation 500 V

8 digital I/O, 24 V, optically isolated, timer, watchdog

Safety features

- Optical isolation 500 V min.
- Creeping distance IEC 61010-1
- Protection against high-frequency EMI
- Noise neutralisation of the PC supply

Applications

- Industrial process control
- Industrial measurement and monitoring
- Control of chemical processes
- Factory automation
- Laboratory equipment, instrumentation

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- · Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

- .NET
- Microsoft VC++ Borland C++
- Visual Basic Delphi
- LabVIEW
 LabWindows/CVI

ADDIPACK functions

- Analog input
 Analog output
 Digital input
- Digital output Watchdog Timer

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads



 138
 Phone: +49 7229 1847-0
 info@addi-data.com

 Fax:
 +49 7229 1847-222
 www.addi-data.com







Windows 64/32-bit drivers

PCI >>

Specifications

Analog outputs

Analog outputs	
Number of outputs:	8 or 4
Resolution:	16-bit resolution
Optical isolation:	500 V through opto-couplers
Output range:	0-10 V, ±10 V switchable through software
	(0-20 mA optional)
Overvoltage protection:	±15 V
Max. output current / load:	±5 mA, 2 kΩ
Short-circuit current:	±35 mA (short time)
Output voltage after reset:	0 V
Current outputs	
Resolution:	15-bit
Output range:	0-20 mA
LSB:	610.35 nA
Load (at 20 mA):	10 Ω min., 560 Ω max.
Output current after reset:	0 mA

Digital I/O

Digitati, o	
Number of I/O channels:	4 dig. inputs, 4 dig. high-side outputs, 24 V
Optical isolation:	1000 V through opto-couplers
Input current at 24 V:	10 mA typ.
Input range:	0-30 V
Supply voltage:	8-32 V
Max. switching current:	65 mA typ.

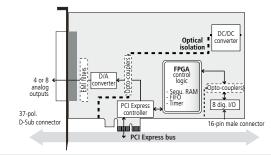
EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

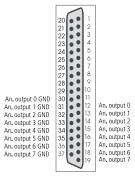
Physical and environmental conditions

Dimensions:	168 x 99 mm
System bus:	Acc. to PCI Express base specification,
	Revision 1.0a (PCI Express 1.0a)
Space required:	1-/4-lane PCI Express slot
Operating voltage:	+ 3.3 V, + 12 V from PC
Front connector:	37-pin D-Sub male connector
Temperature range:	0 to 60 °C (with forced cooling)

Simplified block diagram



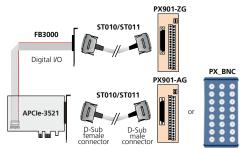
Pin assignment – 37-pin D-Sub male connector



Pin assignment – 16-pin male connector

	Dig. input 3- Dig. input 2- Dig. input 1- Dig. input 0- 24 V voltage supply 24 V voltage supply GND (dig. output) GND (dig. output)	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	Dig. input 3+ Dig. input 2+ Dig. input 1+ Dig. input 0+ High-side output 3 (24 V) High-side output 2 (24 V) High-side output 1 (24 V)
GND (dig. output) 2	GND (dig. output)	4 ■■ 3	High-side output 1 (24 V)
	GND (dig. output)	2 ■■ 1	High-side output 0 (24 V)





Ordering information

Analog output board, optically isolated, 8/4 differential outputs, 16-bit, for PCI Express. Incl. technical description and software drivers.

Versions

APCIe-3521

Voltage APCIe-3521-8

Version with 8 analog voltage outputs APCIe-3521-4 Version with 4 analog voltage outputs Current APCIe-3521-8C Version with 8 analog current outputs APCIe-3521-4C Version with 4 analog current outputs

Accessories

PX901-A:	Screw terminal panel for connecting the analog I/O
PX901-AG:	Same as PX901-A with housing for DIN rail
PX_BNC:	BNC connection box for connecting the analog I/O
PX901-ZG:	Screw terminal panel for connecting the dig. I/O
ST010:	Standard round cable, shielded, twisted pairs, 2 m
ST011:	Standard round cable, shielded, twisted pairs, 5 m
FB3000:	Ribbon cable for digital I/O



Watchdog board, optically isolated, 7 watchdogs/timer









Features

Maximise the reliability of your telecom, ISP, Voice Mail, File Server or industrial systems under Windows operating systems with the APCIe-040 PCI Express watchdog board. The board is equipped with 7 watchdogs for simultaneous software and hardware monitoring. External devices can thus be monitored (e.g. alarm systems, PLCs) and controlled (e. g. modems, dialing devices).

In addition, the PCI Express watchdog board APCIe-040 has a two-level alarm system and can initiate a hardware reset in case of emergency. The principle is based on the computer software having to send signals to the board at regular intervals.

If the board does not receive an expected signal within a certain period of time, the first alarm level is activated. The emergency program is started which determines the cause and tries to remove the error. If this fails, the operating system and, if necessary, external devices are prepared for the hardware reset. The second alarm level is automatically triggered after a defined timeout. The internal PC temperature can be monitored through the onboard temperature sensor.

Watchdog

- 7 watchdogs/timers
- 7 trigger channels/Gate inputs (24 V)
- ٠ Activation through software
- Configuration through software, readable ٠
- ٠ Can be triggered through software or digital input
- Time base for the watchdog/timer: µs, ms, s, min
- Several alarm levels are possible for each watchdog
- Level 1 generates an interrupt or switches the warning relay, level 2 switches the reset relay (only watchdog 0 has 2 relays)
- With the two-level alarm, the operating system can be warned through an interrupt that a hardware reset is going to take place. There is then enough time to close the active tasks or to reset the warning relay.
- The alarm time can be read back at any time, so that the time remaining for further tasks can be established.
- Switching time of the reset relay: 2 s

Defined state after booting

• The watchdogs are switched off through the system reset

APCIe-040

7 watchdogs/timer

8 electromechanical relays

with change-over contacts

7 digital inputs 24 V

2 alarm levels

Temperature monitoring from -35 °C to + 85 °C

Optical isolation 1000 V

Diagnostic

- The status of the 7 watchdogs is readable
- 7 digital inputs (watchdog trigger or timer gate)
- Safety
- Optical isolation 1000 V

Temperature measurement

- 1 temperature onboard sensor
- Alarm function when a programmable limit value is exceeded

Digital inputs

- 7 inputs, optically isolated
- Input voltage 24 V

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

C#.NET, C

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads





on request



* Preliminary product information

on request

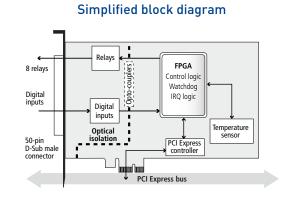


LabVIEW™

Windows 64/32-bit drivers

Specifications*	
Relays	
Type of contacts:	8 change-over
Max. switching voltage:	200 VDC, 200 VAC
Max. switching current:	2 A
	60 W
Max. switching capacity:	
Contact resistance:	< 100 mΩ
Contact material:	Ag and Au plated
Response time:	Max. 4 ms, typ. 2.5 ms
Release time:	Max. 4 ms, typ. 0.9 ms
Mechanical life:	5 x 10 ⁸ operations
Electrical life:	10 ⁵ operations at rated load
Watchdogs/timers	5
Depth:	8-bit
Switching time of the reset relation	
Programmable time of the	
7 watchdogs/timers:	Can be set from 2 µs to 255 min.
Time units:	μs, ms, s, min
	μο, mo, ο, mm
Digital inputs	
Number of inputs:	7
Optical isolation:	Through opto-couplers, 1000 V
Nominal voltage:	24 V
Input current:	5 – 8 mA
Signal delay:	70 μs (at 24 V)
Maximal input frequency:	10 kHz (at 24 V)
Temperature mon	itoring
Accuracy:	±1°C
Measurement range:	-35 °C to 85 °C
measurement range.	(real range of application 0-60 °C)
Resolution:	8-bit
Safety	
	40001/
Test voltage:	1000 V
EMC – Electromag	netic compatibility
a certified EMC laboratory in a 61326). The limit values as set	European EMC directive. The tests were carried out by ccordance with the norm from the EN 61326 series (IEC out by the European EMC directive for an industrial h. The respective EMC test report is available on request.
Physical and envir	onmental conditions
Dimensions:	149 x 99 mm
System bus:	Acc. to PCI Express base specification, Revision 1.0a (PCI Express 1.0a)
Space required:	1-/4-lane, 1 PCI Express slot
Operating voltage:	+3.3 V from the PC
Max. current consumption:	0,67 A ±10 %
Front connector:	50-pin D-Sub male connector
Tomporature range:	0 to 60 %C (with forced cooling)

0 to 60 °C (with forced cooling)

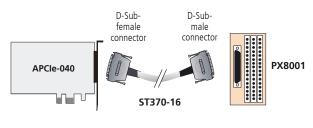


Pin assignment

Pin	Pin	\frown		Pin
44 OC of relay 0 55 OC of relay 1* 60 Co f relay 1* 60 Co f relay 2 77 OC of relay 3 80 Co f relay 4 99 OC of relay 6 10 Co f relay 7 11 OC of relay 6 10 Co f relay 7 12 Digital input 0 (+) 13 Digital input 2 (+) 15 Digital input 2 (+) 16 Digital input 5 (+) 18 Digital input 5 (+) 19 -	18 CC of relay 0 19 CC of relay 1* 20 CC of relay 3 21 CC of relay 3 22 CC of relay 5 24 CC of relay 6 25 CC of relay 7 6 - 27 - 28 - 29 - 30 - 31 - 32 -	34 18 1 35 6 2 36 4 38 6 40 6 41 6 43 9 44 9 43 11 44 9 43 11 45 12 46 13 47 14 48 14 49 16 50 33	Digital input 0 (-) Digital input 0 (-) Digital input 0 (-) Digital input 0 (-) Digital input 0 (-)	1 3 4 5 6 7 8 9 10 11 2 3 4 5 6 7 8 9 10 11 2 3 14 15 16 7 17

OC: opening contact CC: closing contact CO: change-over contact * = reset relay

ADDI-DATA connection





APCIe-040 Watchdog board, optically isolated, 7 watchdogs/timer. Incl. technical description and software drivers. APCIe-040: 7 watchdogs, 8 relays, 7 digital inputs, 24 V Accessories

 PX8001:
 3-row screw terminal panel, 50-pin, for DIN-rail mounting

 ST370-16:
 Shielded round cable, 2 m

Temperature range:



1 to 8-port serial interface, RS232, RS422, RS485, 20 mA CL, modular mounting through modules





Also for **PCI** see APCI-7xxx-3, page 210 Also for **CompactPCI**TM see CPCI-7500, page 242





The APCle-7xxx communication boards are configured by

The APCle-7xxx communication boards are configured by inserting SI modules which the board identifies automatically. The serial interfaces can be configured through modules in the following modes: RS232, RS422, RS485 (with or without optical isolation) and current loop (with optical isolation).

The SI modules with optical isolation allow a protection of up to 1000 V for the use in noisy environments where earth loops can occur.

The I/O lines are protected against short-circuits, fast transients, electrostatic discharge and high-frequency EMI. The interface is supported through a 128-byte FIFO buffer for sending and receiving data and guarantees reliable operation at high transfer rates.

Features

- Asynchronous communication adapter
- Modular mounting through SI modules

 socket for 1-port serial interface (APCIe-7300)
 sockets for 2-port serial interface (APCIe-7420)
 sockets for 4-port serial interface (APCIe-7500,

APCIe-7500/4C) 8 sockets for 8-port serial interface (APCIe-7800)

- Can be configured as RS232, RS422, RS485 with/without optical isolation, 20 mA Current Loop (active, passive), with optical isolation through separate SI modules
- Automatic addressing through BIOS
- Automatic module identification
- UART 16C950, downwards compatible until 16C450
- 128-byte FIFO buffer for sending and receiving data
- Programmable transfer rate
- 5, 6, 7 or 8-bit character
- 1, 1¹/₂ or 2 stop bits
- Parity: even, odd or none
- Automatic transmitter control for RS485 and transmitter control through FIFO level
- Common interrupt

Safety features

- SI modules available with optical isolation 1000 V
- Creeping distance IEC 61010-1 (VDE411-1)
- Protection against fast transients (Burst)
- Short-circuit protection for RS422 and RS485
- Internal diagnostic possibility, break, parity, overrun and framing error

APCIe-7300 – 1-port serial interface APCIe-7420 – 2-port serial interface APCIe-7500 – 4-port serial interface APCIe-7800 – 8-port serial interface

RS232, RS422, RS485, 20 mA Current Loop

Free mode configuration for each port through SI modules

With/without optical isolation 1000 V

128-byte FIFO buffer for each port

16C950-compatible UART

Applications

- Industrial serial communication
- Data acquisition
- Multi-user systems
- PLC interface
- Multidrop applications
- Modem and printer control, etc.

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

- Microsoft VC++
- Visual Basic Delphi

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads



Operating mode	RS	232	RS4	22	RS4	20 mA CL	
	SI232-G	SI232	SI422-G	SI422	SI485-G	SI485	SITTY
Optical isolation 1000 V	1		\$		1		1
Creeping distance 3.2 mm	1		1		1		1
Short-circuit protection			1	1	1	1	
ESD protection	1	1	1		1		
Burst protection	1	1	1	✓	1	1	1
Duplex	Full	Full	Full	Full	Half	Half	Full
Max. Baud rate	1 MBaud	1 MBaud	1 MBaud	1 MBaud	1 MBaud	1 MBaud	19.2 kBaud
Modem control signals	1	1	Optional RTS/CTS (SI-422-PEP)				
Autom. transmitter control					1	1	
Current consumption	16 mA	1 mA	15 mA	5 mA	15 mA	5 mA	82 mA

Serial interface – 1-port, 2-port, 4-port, 8-port

	(i) Provide the
Mode:	RS232, RS422, RS485, 20 mA Current Loop (active, passive) with or without optical isolation through separate SI modules
Transmission mode:	Asynchronous, full or half duplex (SI modules)
Addressing:	Automatic through BIOS
Memory:	128-byte FIFO buffer for transmitter and receiver
Transfer rate:	Programmable up to 1 MBaud
Protocol:	5-, 6-,7- or 8-bit character 1,1½ or 2 stop bits
Parity:	Even, odd, none, mark, space
Interrupt lines:	Automatic configuration through BIOS

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

APCIe-7300 / APCIe-7420 / APCIe-7500 / APCIe-7800

Optical isolation:	1000 V (SI modules)
optical isolation.	
Physical and en	vironmental conditions
Dimensions:	168 x 99 mm
System bus:	Acc. to PCI Express base specification,
	Revision 1.0a (PCI Express 1.0a)
Space required:	1 PCI Express slot
Operating voltage:	+ 3,3 V from the PC
Front connector:	9-pin D-Sub male connector (APCIe-7300)
	2 x 9-pin D-Sub male connector (APCIe-7420)
	37-pin D-Sub male connector (APCIe-7500)
	78-pin D-Sub female connector (APCIe-7800)
Temperature range:	0 to 60 °C (with forced cooling)

Ordering information

APCle-7300 / APCle-7420 / APCle-7500 / APCle-7800APCle-7300:1-port serial interface (1 x 9-pin D-Sub)APCle-7420:2-port serial interface (2 x 9-pin D-Sub)APCle-7500:4-port serial interface (1 x 37-pin D-Sub)APCle-7500/4C:4-port serial interface (4 x 9-pin D-Sub)APCle-7800:8-port serial interface (1 x 78-pin D-Sub)Each incl. technical description and software drivers.

SI modules: Please order the modules separately!

SI232-G:	RS232 mode, isolated
SI232:	RS232 mode
SI422-G:	RS422 mode, isolated
SI422-PEP:	RS422 mode, isolated, with RTS/CTS
SI422:	RS422 mode
SI485-G:	RS485 mode, isolated
SI485:	RS485 mode
SITTY:	20 mA Current Loop mode (active, passive), isolated

Accessories

ST075:	Shielded round cable, 37 to 4 x 9-pin (for APCIe-7500)
ST074:	Shielded round cable, 37 to 4 x 25-pin (for APCle-7500)
ST7809:	Shielded round cable, 78 to 8 x 9-pin (for APCIe-7800)
ST7825:	Shielded round cable, 78 to 8 x 25-pin (for APCIe-7800)



PCI BOARDS: DIGITAL I/O



Correct state identification

With the digital boards by ADDI-DATA, you can reliably find out the state (0 or 1) of the connected devices or sensors. There is thus no undefined range that could lead to switching errors.

Digital boards are used for industrial I/O regulation, signal switching, as interface to automatic test devices, for the on/off monitoring of electrical consumers or as interfaces to machines. They can activate e.g. ventilation, valves, pumps and electromechanical relays.

A large product range

Our product range of digital input and output boards for the PCI bus is as varied as their possibilities of use. In our product range you will find:

- Digital input boards, 5 V, 24 V
- Digital I/O boards, 5 V, 12 V, 24 V, TTL
- Digital output boards, 24 V
- Relay boards, 8 to 16 relays
- All PCI boards can be used in 5 V systems.

Some of the boards have been developed for use in 5 V systems as well as in 3.3 V systems.

More performance through CPU relief

The interruptible inputs of the digital I/O boards make continuous cyclic scan processes (polling) for routine monitoring and analysis tasks unnecessary, which relieves the processor and the software. At a status change of the digital inputs, a corresponding interrupt is generated, enabling the system to react to the event through the interrupt routine and thus meet the defined real-time criteria.

Safe machine start

Until the current supply provides its nominal voltage in the initialisation phase, the logic components on electronic devices run through different undefined intermediate states.

Without special measures the state of the digital outputs cannot be ascertained. Thus the I/O and output boards reset all outputs to "0" at power-on or reset. This allows straight solutions for machine starts or the start of measurement processes.

Real-time complete system MSX-Box

Combination of the MSX-Box PAC system and PCI boards

- Compact and flexible
- Stand-alone system (own CPU)
- Long-term availability

MSX-Box product information see page 20



HIGH DEGREE OF PROTECTION

- Optical isolation from 500 V to 1000 V
- Protection against short-circuits, overtemperature, overvoltage
- Filters for the inputs and outputs
- Industry-standard D-Sub connectors





Product overview

	Digital I/O, 24 V		Digita 24	Digital input, 24 V		Digital output, 24 V		Digital I/O, TTL			
	APCI-1500 APCI-1500-12V	APCI-1516	APCI-1564	APCI-1564_3,3V	APCI-1564-5V APCI-1564-5V	APCI-1032 APCI-1032-5	APCI-1016	APCI-2032 APCI-2032-5	APCI-2016	APCI-2200_3,3V APCI-2200	APCI-1648 APCI-1696
32-bit PCI bus	PCI 5 V	5 V	5 V	3.3 V	5 V	5 V	5 V	5 V	5 V	3.3 V / 5 V	3.3 V / 5 V
FPGA				/	1					1	
Filters and protective circuits	1	1	v	/	1	1	1	1	1	1	1
Input channels	16	8	3	2	32	32	16				48 TTL (APCI-1648) 96 TTL (APCI-1696)
Optical isolation 1000 V	1	1		/	1	1	1				
Interruptible input channels	14		1	6	16	16					
Nominal voltage (V) DC (V)	24 V (19-30) 12 V (APCI- 1500-12V)	24 V (19-30)	24 V (19-26)	5 V	24 V (19-30) 5 V (APCI- 1032-5)	24 V (19-30)				ΠL
Input current at 24 VDC	6 mA	6 mA	5 r	nA		5 mA	6 mA				
Output channels (24 V high-side drivers)	16	8	3	2				32	16		48 TTL (APCI-1648) 96 TTL (APCI-1696)
Output channels (5 V high-side drivers)					32 (APCI- 1564-5V-HS)						
Output channels Open Collector					32 (APCI- 1564-5V)						
Relays										8/16 relays	
Optical isolation 1000 V	1	1		/	1	1	1	~	1	1	
Nominal voltage (V)	24 V DC (10-36)	24 V DC (10-36)		(10-36) -1564-5V)	5 V			24 V DC (10-36) 5 V (APCI- 2032-5)	24 V DC (10-36)	60 V DC 48 V AC	TTL
Output current (A) for one channel	0.5(1)	0.5(1)	0.!	5(1)	50 mA			0.5(2)	0.5(2)	1	12 LS TTL Loads
Watchdog (depth)	✓ (16-bit)	🗸 (8-bit)	8) 🗸	3-bit)	✔ (8-bit)			✔ (8-bit)	🗸 (8-bit)	🗸 (8-bit)	
Timer / Counter (depth)	3/- (16-bit)			/3 -bit)	1/3 (32-bit)						
Page	146	148	15	50	152	154	156	158	160	162	164
Software	Driver down	Driver download: www.addi-data.com/downloads									

(1) Limited to 3 A for all outputs, self-resetting fuse against short-circuits(2) Limited to 2x3 A for all outputs, self-resetting fuse against short-circuits



Digital I/O board, optically isolated, 32 digital inputs and outputs, 24 V / 12 V





PCI 32-bit

Also for *CompactPCI*TM See CPCI-1500, page 176

Also for PC/104-PLUS See page 172

Also for PCI >>> see EXPRESS[®] page 78







LabVIEW™



LabWindows/CVI™



Features

- 3 programmable timers
- Connector compatible to the ISA board PA 1500.
 Connector and software compatible to the digital I/O boards APCIe-1532 for the PCI Express bus and CPCI-1500 for the CompactPCI bus.
- Monitoring program for testing and setting the board functions

Inputs

- 16 optically isolated digital inputs, 24 V or 12 V (APCI-1500-12V), including 14 interruptible inputs
- Reverse voltage protection All inputs are filtered
- -
- 16 optically isolated digital outputs, 10 V to 36 V
- Output current per channel 500 mA
- Timer programmable watchdog for resetting the outputs to "0"
- Diagnostic report through status register at shortcircuits, overtemperature, voltage drop or watchdog
- Interrupt triggered through watchdog, timer, error
- At Power-On, reset of the outputs to "0"
- Short-circuit current for 16 outputs ~ 3 A typ.
- Short-circuit current per output ~1.5 A typ.
- Self-resetting fuse (electronic fuse)
- Overtemperature and overvoltage protection
- 24 V power outputs with protection diodes and filters
- Output capacitors against electromagnetic emissions
- External 24 V voltage supply screened and filtered
- Shutdown logic, when the external supply voltage drops below 5 V

Safety features

- Optical isolation 1000 V
- Creeping distance IEC 61010-1
- Protection against fast transients (burst), overvoltage, electrostatic discharge and high-frequency EMI
- Separate ground lines for inputs and outputs

APCI-1500 / APCI-1500-12V

16 digital inputs, 24 V or 12 V,

including 14 interruptible inputs

16 digital outputs, 10-36 V, 500 mA/channel

Optical isolation 1000 V

Input and output filters

Watchdog, timer

At Power-On the outputs are reset to "0"

Applications

- Industrial I/O control
- PLC coupling
- Reading of encoder values for process control
- Signal switching
- Interface to electromechanical relays
- ON/OFF monitoring of motors, lights...
- Watchdog / Timer
- Machine interfacing,

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request
- RTX drivers (real-time)

Drivers and samples for the following compilers and software packages:

- Microsoft VC++ Microsoft C
- Borland C++ Borland C
- Visual Basic Delphi
- .NET on request
- LabVIEW LabWindows/CVI DASYLab DIAdem

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads





Digital inputs	
Number of inputs:	16 (common ground acc. to IEC 1131-2)
Including interruptible inputs:	14, IRQ line selected through BIOS
Optical isolation:	Through opto-couplers, 1000 V from PC to peripheral
Compare logic:	AND and OR mode; OR priority
24 V version (APCI-1500)	
Nominal voltage:	24 V
Input current at 24 V:	6 mA typ.
Logic input levels:	U nominal: 24 V
UH max.:	30 V/current 9 mA typ.
UH min.:	19 V/current 2 mA typ.
UL max.:	14 V/current 0.7 mA typ.
UL min.:	0 V/current 0 mA typ.
Signal delay:	70 μs (at nominal voltage)
Maximum input frequency:	5 kHz (at nominal voltage)
12 V version (APCI-1500-12)	
Nominal voltage:	12 V
Input current at 12 V:	4.2 mA typ.
Logic input levels:	U nominal: 12 V
UH max.:	16 V/current 6.3 mA typ.
UH min.:	9 V/current 2.7 mA typ.
UL max.:	6 V/current 1.2 mA typ.
UL min.:	0 V/current 0 mA typ.
Signal delay:	70 μs (at nominal voltage)
Maximum input frequency:	5 kHz (at nominal voltage)
Digital outputs	
Number of outputs:	16, optically isolated up to 1000 V
	through opto-couplers
Output type:	High-side (load to ground) acc. to IEC 1131-2
Nominal voltage:	24 V / 12 V (APCI-1500-12V)
Supply voltage:	10 V to 36 V, min. 5 V (via front connector)
Max. current for 16 outputs:	3 A typ.
Output current/output:	500 mA max.

 Timer:
 3

 Watchdog:
 Timer-programmable, 10 µs to 37 s

 EMC - Electromagnetic compatibility

 The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC

1.5 A

Short-circuit current/output shutdown at 24 V, $R_{load} < 0.1 \Omega$:

 $0.4 \Omega max$

170 °C (output driver) 20 °C (output driver)

The outputs are switched off.

Status bit or interrupt to the PC

l out=0.5 A, load = resistance: 100 μs

When the ext. 24 V voltage drops below 5 V:

l out=0.5 A, load = resistance: 60 µs

RDS ON resistance:

Overtemperature (shutdown):

Temperature hysteresis:

Switch-on time:

Switch-off time:

Safety

Diagnostics:

Shutdown logic:

61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and environmental conditions		
Dimensions:	131 x 99 mm	
System bus:	PCI 32-bit 3.3/5 V acc. to specification 2.1 (PCISIG)	
Space required:	1 PCI slot	
Operating voltage:	+5 V, ± 5 % from the PC	
Current consumption:	400 mA typ. ± 10 %	
Front connector:	37-pin D-Sub male connector	
Temperature range:	0 to 60 °C (with forced cooling)	

APCI-1500 / APCI-1500-12V

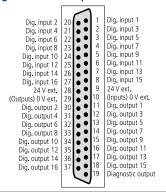
Digital I/O board, optically isolated, 32 digital inputs and outputs, 24 V / 12 V. Incl. technical description, software drivers and monitoring program.

Versions APCI-1500: Digital I/O board, opt. isolated, 32 dig. I/O, 24 V inputs, outputs 10 to 36 V APCI-1500-12V: Digital I/O board, opt. isolated, 32 dig. I/O, 12 V inputs, outputs 10 to 36 V Accessories PX901-D: Screw terminal panel, LED status display	PX9000: PX8500-G: ST010: ST011: ST010-S: ST021: ST022:	3-row screw terminal panel for DIN rail, LED status display Relay output board for DIN rail, cascadable Standard round cable, shielded, twisted pairs, 2 m Standard round cable, shielded, twisted pairs, 5 m Same as ST010, for high currents (separate 24 V supply) Round cable between APCI-1500 and PX8500-G, shielded, twisted pairs, 2 m Cable between PX8500-G and PX901-DG, shielded, 2 m
PX901-DG: Screw terminal panel, LED status display, for DIN rail	ST8500:	Ribbon cable for cascading two PX 8500

Simplified block diagram couplers 16 digital inputs 16 digital inputs FPGA Opto-Control Prologic Watchdog IRQ inputs 16 digital outputs tective 16 . circuit and filters digital outputs ē Optical isolation Opto PCI interface 37-pin D-Sub male connector

Pin assignment - 37-pin D-Sub male connector

PCI hus



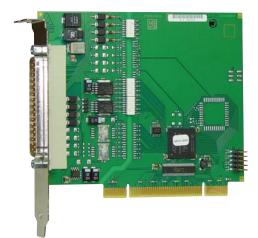
ADDI-DATA connection

Example 1 Connection of the inputs and outputs through screw terminal panels ST010 / ST011 PX901-DG or PX9000 APCI-1500 D-Sub female D-Sub male Example 2 - Connection of the inputs through screw terminal panel PX901-DG - Connection of the outputs PX8500-G through relay output board PX8500-G cascaded in 16 relays ST022 9 able ST8500 ST02 APCI-1500 D-Sub male corr PX8500-G

Ordering information

ADDI-DATA SPIRIT OF EXCELLENCE

Digital I/O board, optically isolated, 16 digital inputs and outputs, 24 V





PCI 32-bit

Also for PCI see EXPRESS[®] page 80







InhVIFW



LabWindows/CVI™



Features

Inputs

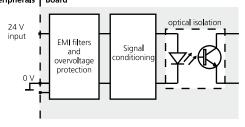
- 8 optically isolated inputs, 24 V
- Reverse voltage protection
- 8 optically isolated outputs, 10 V to 36 V
- Output current per channel 500 mA
- Total current: 3 A typ. (fused through PTC resistor)
- Watchdog for resetting the outputs to "0"
- At Power-On, reset of the outputs to "0"
- Short-circuit current for 8 outputs ~ 3 A typ.
- Short-circuit current per output ~1.5 A typ.

- drops below 5 V
- Diagnostic function for detecting short-circuits and overtemperature

Safety features

- Optical isolation 1000 V
- Creeping distance IEC 61010-1
- Separate ground lines for inputs and outputs
- Protection against fast transients (burst), overvoltage, electrostatic discharge and high-frequency EMI

Protective circuit for the input channels Peripherals | Board



APCI-1516

8 digital inputs, 24 V

8 digital outputs, 24 V, 500 mA/channel

Optical isolation 1000 V

Input and output filters

Watchdog

Applications

- Industrial I/O control
- PLC coupling
- Signal switching
- Interface to electromechanical relays
- Automatic test equipment
- ON/OFF monitoring of motors, lights...
- Watchdog •
- Machine interfacing

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

- .NET
- Microsoft VC++ Borland C++
- Visual Basic Delphi
- LabVIEW
 LabWindows/CVI
 DIAdem

ADDIPACK functions:

Digital input
 Digital output
 Watchdog

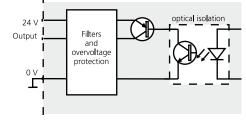
On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads

Protective circuit for the output channels

Peripherals Board

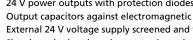




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- ٠

 - Self-resetting fuse (electronic fuse)
 - Overtemperature and overvoltage protection
 - 24 V power outputs with protection diodes and filters
 - Output capacitors against electromagnetic emissions
 - External 24 V voltage supply screened and filtered
 - Shutdown logic, when the external supply voltage



All inputs are filtered Outputs

Digital inputs

Digitat inputo			
Number of inputs:	8 (common ground acc. to IEC 1131-2)		
Nominal voltage:	24 V		
Input current at 24 V:	6 mA typ.		
Logic input levels:			
U nominal:	24 V		
UH max.:	30 V/current 9 mA typ.		
UH min.:	19 V/current 2 mA typ.		
UL max.:	14 V/current 0.6 mA typ.		
UL min.:	0 V/current 0 mA typ.		
Optical isolation:	Through opto-couplers, 1000 V		
	from PC to peripheral		
Signal delay:	70 μs (at 24 V)		
Maximal input frequency:	5 kHz (at 24 V)		

Digital outputs

Number of outputs:	8, optically isolated up to 1000 V through opto-couplers
Output type:	High side (load to ground) acc. to IEC 1131-2
Nominal voltage:	24 V
Supply voltage:	10 V to 36 V, min. 5 V (via front connector)
Max. current for 8 outputs:	3 A typ.
Output current/output:	500 mA max.
Short-circuit current/output	
shutdown at 24 V, $R_{load} < 0.1 \Omega$:	1.5 A
RDS ON resistance:	0.4 Ω max.
Switch-on time:	l out=0.5 A, load = resistance: 100 μs
Switch-off time:	l out=0.5 A, load = resistance: 60 μs
Overtemperature (shutdown):	170 °C (output driver)
Temperature hysteresis:	20 °C (output driver)

Safety

	Shutdown logic:	When the ext. 24 V voltage drops below 5 V: The outputs are switched off.			
Diagnostics (pin 19)		Diagnostics at output overload and overtemperature			
	Watchdog:	Timer-programmable			
		20 ms to 5 s in steps of 20 ms			

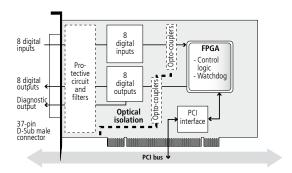
EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

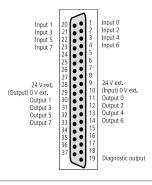
Physical and environmental conditions

Dimensions:	131x 99 mm
System bus:	PCI 32-bit 3.3/5 V acc. to specification 2.1 (PCISIG)
Space required:	1 PCI slot
Operating voltage:	$+5 \text{ V}, \pm 5 \%$ from the PC
Current consumption:	210 mA ±10 % typ.
Front connector:	37-pin D-Sub male connector
Temperature range:	0 to 60 °C (with forced cooling)

Simplified block diagram

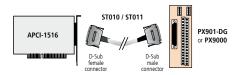


Pin assignment – 37-pin D-Sub male connector



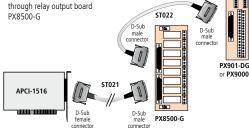
ADDI-DATA connection

Example 1 Connection of the inputs and outputs through screw terminal panels



Example 2

- Connection of the inputs
- through screw terminal panel PX901-DG
- Connection of the outputs



Ordering information

PX8500-G

APCI-1516

Digital I/O board, optically isolated, 16 digital inputs and outputs, 24 V. Incl. technical description, software drivers

Accessori	es		
PX901-D:	Screw terminal panel,	ST010:	Standard round cable, shielded, twisted pairs, 2 m
	LED status display	ST011:	Standard round cable, shielded, twisted pairs, 5 m
PX901-DG:	Screw terminal panel,	ST010-S:	Same as ST010, for high currents
	LED status display, for DIN rail	ST021:	Round cable between APCI-1516 and PX8500-G,
PX9000:	3-row screw terminal panel		shielded, twisted pairs, 2 m
	for DIN rail, with LED status display	ST022:	Round cable between PX8500-G and PX 901 or
PX8500-G:	Relay output board for DIN rail, cascadable		PX9000, shielded, 2 m
		ST8500:	Ribbon cable for cascading two PX 8500



Digital I/O board, optically isolated, 64 digital inputs and outputs, 24 V



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PCI 32-bit

Also for PCISS see EXPRESS page 122







LabVIEW™





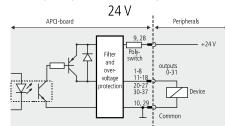
Features

- 32-bit, 33 MHz, PCI interface
- PCI 5 V (APCI-1564)
- PCI 3.3 V (APCI-1564_3,3V)
- Inputs
- 32 optically isolated digital inputs, 24 V, including 16 interruptible and 3 counter inputs
- Inputs organised in 4 groups of 8 channels, each group has its own ground line
- Reverse voltage protection
- All inputs are filtered
- Outputs
- 32 optically isolated digital outputs, 10 V to 36 V
- Output current per channel 500 mA
- Watchdog for resetting the outputs to "0"
- At Power-On, the outputs are reset to "0"
- Total current for 16 outputs ~ 3 A
- Total current for 32 outputs ~ 6 A
- Electronic fuse
- Short-circuit current per output ~1.5 A
- Overtemperature and overvoltage protection
- 24 V power outputs with protection diodes and filters
- Output capacitors against electromagnetic emissions
- Ext. 24 V voltage supply screened and filtered
- Shutdown logic, when the external supply voltage drops below 5 V
- Safety features
- Optical isolation 1000 V
- Creeping distance IEC 61010-1
- Protection against fast transients (burst), overvoltage, electrostatic discharge and high-frequency EMI

Connection principle of the 24 V outputs

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www.addi-data.com



APCI-1564/APCI-1564_3,3V

PCI 5 V (APCI-1564) PCI 3.3 V (APCI-1564_3,3V)

32 digital inputs, 24 V,

including 16 interruptible, filtered

32 digital outputs, 24 V, 500 mA/channel, filtered

Optical isolation 1000 V

Watchdog, timer,

3 x 32-bit counters up to 500 kHz

The outputs are reset to "0" at Power-On

- Interrupt started through counter, timer
- Separate ground lines for inputs and outputs

Applications

- Industrial I/O control PLC coupling Signal switching
- Interface to electromechanical relays
- Automatic test equipment
- ON/OFF monitoring of motors, lights...
- Watchdog Machine interfacing
- ...

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request
- RTX drivers (real-time)

Drivers and samples for the following compilers and software packages:

- .NET
- Microsoft VC++ Borland C++
- Visual Basic Delphi
- LabVIEW
 LabWindows/CVI
- ADDIPACK functions:

Digital input • Digital output Watchdog • Timer • Counter

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads



Specifications			
Digital inputs			
Number of inputs:	Input: 0-7, 8-15 - 0-2: fas	, 16-23, 24	nputs, 500 kHz
Optical isolation:	Through opto-c		1
_ •	Channel 0-3		Channel 4-31
Input current at 24 V:	10.5 mA typ.		5 mA typ.
Logic input levels:			
U nominal	24 V		24 V
UH max.	26 V / 12.3 mA	typ.	26 V / 5 mA typ.
UH min.	19 V / 5.5 mA t	yp.	19 V / 3.2 mA typ.
UL max.	14 V / 0.7 mA t	yp.	14 V / 1.3 mA typ.
UL min.	0 V / 0 mA typ.		0 V / 0 mA typ.
Signal delay:	1 µs		70 µs
Maximal input frequency:	500 kHz		5 kHz
Digital outputs			
Number of outputs:	32, optically iso	lated up to	1000 V
Output type:			acc. to IEC 1131-2
Nominal voltage:	24 V		
Supply voltage:	10 V to 36 V)		
Max. current for 16 / 32 outputs:	3 A typ./6 A typ.		
Output current/output:	500 mA max.		
Short-circuit current/output			
shutdown at 24 V, R_{load} < 0.1 Ω :	1.5 A		
RDS ON resistance:	0.4Ω max.		
Switch-on time:	l out=0.5 A,	load = re	esistance: 94 µs typ.
Switch-off time:	l out=0.5 A,		esistance: 8 µs typ.
Overtemperature (shutdown):	170 °C (output		
Temperature hysteresis:	20 °C (output d	river)	
Safety			
Shutdown logic:	When the ext. 2	4 V voltage	e drops below 5 V:
	The outputs are	5	
Diagnostics:	Pin 19: status b		
Timer:	12-bit		
Watchdog:	8-bit, timer-prog in steps of 20 m		from 20 ms to 5 s
EMC – Electromagne	tic compa	tibility	
The product complies with the Euro a certified EMC laboratory in accor 61326). The limit values as set out environment are complied with. Th	dance with the ne by the European le respective EMC	orm from th EMC direct test report	ne EN 61326 series (IEC ive for an industrial

Physical and environmental conditions

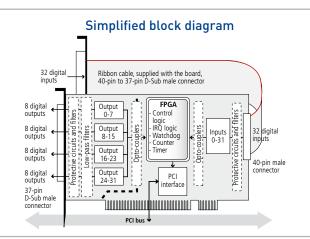
Dimensions:	171 x 99 mm
System bus:	PCI 32-bit 5 V acc. to specification 2.1 (PCISIG) or 3.3 V
Space required:	1 PCI slot + 1 additional slot opening
Operating voltage:	+5 V, ± 5 % from the PC
Current consumption:	410 mA ± 10 % typ.
Front connector:	37-pin D-Sub male connector for 32 digital outputs
Additional connector:	37-pin D-Sub male connector on separate
	bracket for 32 digital inputs
Temperature range:	0 to 60 °C (with forced cooling)

APCI-1564/APCI-1564_3,3V

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APCI-1564: Digital I/O board, optically isolated, 64 digital inputs and outputs, 24 V. Incl. ribbon cable, technical description, software drivers APCI-1564_3,3V: Same as APCI-1564, for PCI 3.3 V

PX901-DG: PX9000:	Screw terminal panel Screw terminal panel for DIN rail 3-row screw terminal panel	ST011: ST010-5: ST022: ST8500:	Standard round cable, shielded, twisted pairs, 5 m Same as ST010, for high currents (24 V supply separate) Between 2 relay output boards PX8500-G Ribbon cable for cascading two PX8500-G
PX8500-G:	Relay output board for DIN rail, cascadable		
ST010:	Standard round cable shielded twisted pairs 2 m		



Pin assignment – 37-pin D-Sub male connector

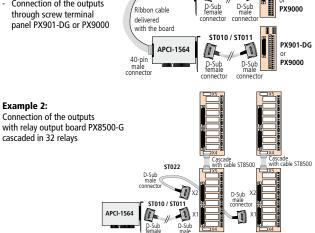
Counter input 1 Dig. input 3 Dig. input 7 Dig. input 7 Dig. input 1 Dig. input 11 Dig. input 11 Dig. input 15 GND 0 GND 3 Dig. input 17 Dig. input 23 Dig. input 23 Dig. input 23 Dig. input 29 Dig. input 21	20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37		ts 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 19	Counter input 0 Counter input 2 Dig. input 4 Dig. input 8 Dig. input 8 Dig. input 10 Dig. input 10 Dig. input 10 Dig. input 12 Dig. input 14 GND 1 GND 2 Dig. input 14 Dig. input 24 Dig. input 22 Dig. input 22 Dig. input 24 Dig. input 24 Dig. input 24 Dig. input 25 Dig. input 26 Dig. input 26 Din	Dig. output 1 Dig. output 3 Dig. output 3 Dig. output 7 Dig. output 7 Dig. output 1 Dig. output 1 Dig. output 13 Dig. output 15 Nom. voltage ext. GND Dig. output 17 Dig. output 17 Dig. output 23 Dig. output 22 Dig. output 29 Dig. output 23	20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37		1 2 3 4 5 6 7 8 9 10 11 12	Dig. output 0 Dig. output 2 Dig. output 4 Dig. output 4 Dig. output 8 Dig. output 10 Dig. output 10 Dig. output 11 Dig. output 12 Dig. output 12 Dig. output 16 Dig. output 18 Dig. output 20 Dig. output 22 Dig. output 22 Dig. output 24 Dig. output 24 Dig. output 24 Dig. output 24 Dig. output 24 Dig. output 25 Dig. output 26 Dig. output 26 Dig. output 26 Dig. output 26 Dig. output 28 Dig. output 28 Dig. output 28
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ADDI-DATA connection

- Example 1: Connection of the inputs
- (Ribbon cable)

Example 2:

Connection of the outputs through screw terminal



Ordering information

ST010 / ST011

PX901-DG

Phone: +49 7229 1847-0 info@addi-data.com Fax: +49 7229 1847-222 www.addi-data.com



Digital I/O board, optically isolated, 64 digital inputs and outputs, 5 V





Also for PCI >>> see EXPRESS[®] page 122 see







LabVIEW^T



DIAdem*

Features

- 32-bit, 33 MHz, PCI interface
- PCI 5 V

Inputs

- 32 optically isolated digital inputs, 5 V, including 16 interruptible and 3 counter inputs
- Inputs organised in 4 groups of 8 channels, each group has its own ground line
- Reverse voltage protection
- All inputs are filtered

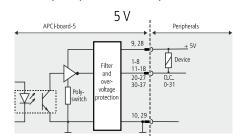
Outputs

- 32 optically isolated digital outputs, 5 V APCI-1564-5V: **Open Collector outputs** APCI-1564-5V-HS: High-side outputs
- Output current per channel 50 mA
- Watchdog for resetting the outputs to "0" ٠
- At Power-On, the outputs are reset to "0"
- Total current for 8 outputs ~ 0.5 A (via PTC) •
- Electronic fuse •
- Short-circuit current per output ~1.5 A
- Overtemperature and overvoltage protection
- Output capacitors against electromagnetic emissions
- Ext. 24 V voltage supply screened and filtered
- Shutdown logic, when the external supply voltage drops below 5 V
- Safety features
- Optical isolation 1000 V
- Creeping distance IEC 61010-1
- Protection against fast transients (burst), overvoltage, ٠ electrostatic discharge and high-frequency EMI

Connection principle of the 5 V outputs

info@addi-data.com

www.addi-data.com



APCI-1564-5V/APCI-1564-5V-HS

32 digital inputs, 5 V, including 16 interruptible, filtered

32 digital outputs, 5 V, 500 mA/channel, filtered open collector (5V) or high side (5V-HS)

Optical isolation 1000 V

Watchdog, timer, 3 x 32-bit counters up to 500 kHz

The outputs are reset to "0" at Power-On

- Interrupt started through counter, timer
- Separate ground lines for inputs and outputs

Applications

- Industrial I/O control PLC coupling Signal switching
- Interface to electromechanical relays
- Automatic test equipment
- ON/OFF monitoring of motors, lights...
- Watchdog Machine interfacing

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request
- RTX drivers (real-time)

Drivers and samples for the following compilers and software packages:

- .NFT
- Microsoft VC++ Borland C++
- Visual Basic Delphi
- LabVIEW LabWindows/CVI
- ADDIPACK functions:

Digital input • Digital output Watchdog • Timer • Counter

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads



Digital inputs		
Number of inputs:	32; 4 groups of cha	annels with common ground:
	Input: 0-7, 8-15, 16	5-23, 24-31
	- 0-2: fast co	ounter inputs, 500 kHz
	- 4-19: inter	ruptible inputs
Optical isolation:	Through opto-coup	olers, 1000 V
	Channel 0-3	Channel 4-31
Input current at 5 V:	8,5 mA typ.	6 mA typ.
Logic input levels:		
U nominal	5 V	5 V
UH max.	6 V / 11.3 mA typ.	6 V / 8.4 mA typ.
UH min.	4 V / 5.5 mA typ.	4 V / 4 mA typ.
UL max.	2 V / 1 mA typ.	2 V / 0.8 mA typ.
UL min.	0 V / 0 mA typ.	0 V / 0 mA typ.
Signal delay:	1 µs	70 µs
Maximal input frequency:	500 kHz	5 kHz
Digital outputs		
Number of outputs:	32, optically isolate	ed up to 1000 V
Output type:	High side (load to g	ground) acc. to IEC 1131-2
	Open collector (onl	y APCI-1564-5V)
Nominal voltage:	5 V	
Supply voltage:	5 V to 35 V (APCI-15	564-5V-HS)
	5 V to 12 V (APCI-15	564-5V)
Max. current for 16 / 32 outputs:	0.8 A typ./1.6 A typ	
Output current/output:	50 mA max.	
Short-circuit current/output		
shutdown at 24 V, R_{load} < 0.1 Ω :	1.5 A	
RDS ON resistance:	0.4 Ω max.	
Switch-on time:	l out=50 mA,	load = resistance: 250 µs typ
Switch-off time:	l out=50 mA,	load = resistance: 3 µs typ.
Overtemperature (shutdown):	170 °C (output driv	ver)
Temperature hysteresis:	20 °C (output drive	er)
Safety		
Diagnostics:	Pin 19: status bit o	r interrupt to the PC
Timer:	12-bit	
Watchdog:	8-bit, timer-program	nmable from 20 ms to 5 s
5	in steps of 20 ms	
EMC – Electromagne	tic compatib	oility
The product complies with the Eur		•
a certified EMC laboratory in accor	•	,
61326). The limit values as set out		C directive for an industria at report is available on re

Physical and environmental conditions

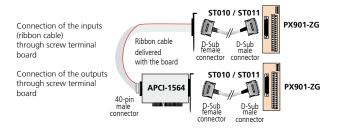
Dimensions:	171 x 99 mm	
System bus: PCI 32-bit 5 V acc. to specification 2.1 (PCISIG		
Space required:	1 PCI slot + 1 additional slot opening	
Operating voltage:	+5 V, ± 5 % from the PC	
Current consumption:	410 mA ± 10 % typ.	
Front connector:	37-pin D-Sub male connector for 32 digital outputs	
Additional connector:	37-pin D-Sub male connector on separate	
	bracket for 32 digital inputs	
Temperature range:	0 to 60 °C (with forced cooling)	

Simplified block diagram Ribbon cable, supplied with the board, 40-pin to 37-pin D-Sub male connector 32 digital inputs 8 digital outputs FPGA Output 0-7 ntrol and filters logic IRQ logic Watchdog Counter Timer 8 digital outputs Output 8-15 32 digital inputs Inputs 0-31 Protective circuits 8 digital outputs Output 16-23 Dpto-40-pin male 8 digital outputs Output 24-31 connector PCI interface 37-pin D-Sub male connecto PCI bus

Pin assignment – 37-pin D-Sub male connector

	Inputs			Outputs	
Counter input 1 Dig. input 3 Dig. input 5 Dig. input 7 Dig. input 9 Dig. input 11 Dig. input 13 Dig. input 13 Dig. input 13 Dig. input 17 Dig. input 17 Dig. input 23 Dig. input 23 Dig. input 27 Dig. input 29 Dig. input 29 Dig. input 31	20 1 21 2 22 3 23 4 24 5 25 6 27 8 29 9 30 11 31 12 32 3 34 15 35 6 36 17 37 18	Counter input 0 Counter input 2 Dig. input 4 Dig. input 8 Dig. input 8 Dig. input 10 Dig. input 12 Dig. input 14 GND 1 GND 2 Dig. input 14 GND 2 Dig. input 16 Dig. input 20 Dig. input 20 Dig. input 20 Dig. input 26 Dig. input 28 Dig. input 28 Dig. input 30 Not connected	Dig. output 1 Dig. output 3 Dig. output 5 Dig. output 9 Dig. output 10 Dig. output 11 Dig. output 13 Dig. output 13 Dig. output 13 Dig. output 17 Dig. output 19 Dig. output 23 Dig. output 23 Dig. output 29 Dig. output 23 Dig. output 23	20 21 22 23 24 24 5 25 6 6 7 27 8 9 9 1 1 2 24 5 5 6 6 7 7 27 8 9 9 9 10 12 12 23 3 4 5 5 6 7 7 8 9 9 9 10 11 12 12 12 12 12 12 12 12 12	Dig. output 0 Dig. output 2 Dig. output 2 Dig. output 4 Dig. output 6 Dig. output 10 Dig. output 10 Dig. output 12 Dig. output 14 Ext. voltage* GND Dig. output 14 Dig. output 14 Dig. output 14 Dig. output 16 Dig. output 20 Dig. output 22 Dig. output 22 Dig. output 22 Dig. output 24 Dig. output 26 Dig. out
				pen Collector): n (High-Side):	5 to 35 V

ADDI-DATA connection



Ordering information

APCI-1564-5V/APCI-1564-5V-HS

Digital I/O board, optically isolated, 64 digital inputs and outputs, 5 V. Incl. ribbon cable, technical description, software drivers**APCI-1564-5V:**open collector outputs**APCI-1564-5V-HS:**high-side outputs

Accessories

PX 901-ZG:Screw terminal panel (only for APCI-1564-5V)**ST010:**Standard round cable, shielded, twisted pairs, 2 m**ST011:**Standard round cable, shielded, twisted pairs, 5 m



-

Digital input board, optically isolated, 32 digital inputs, 24 V / 5 V





PCI 32-bit

Windows

64/32-bit drivers

Features

 32 optically isolated digital inputs, 24 V (APCI-1032) or as 5 V version (APCI-1032-5) including 16 interruptible inputs

Safety features

- Optical isolation 1000 V
- Creeping distance IEC 61010-1
- Reverse voltage protection
- All inputs are filtered
- Protection against fast transients (burst), overvoltage, electrostatic discharge and high-frequency EMI
- Additional noise suppression on the interrupt lines

Applications

- Industrial I/O control
- Signal switching
- Interface to electromechanical relays
- Automatic test equipment
- ON/OFF monitoring of motors, lights...
- Machine interfacing

APCI-1032 / APCI-1032-5

32 digital inputs, 24 V or 5 V,

including 16 interruptible inputs

Optical isolation 1000 V

Input filters

Reverse voltage protection

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request
- RTX drivers (real-time)

Drivers and samples for the following compilers and software packages:

- .NET
- Microsoft VC++ Borland C++
- Visual Basic Delphi
- LabVIEW LabWindows/CVI DIAdem

ADDIPACK functions:

Digital input

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads

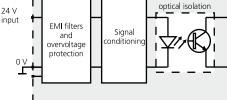


LabVIEW



Protective circuit for the input channels







Spec	ificat	ions
------	--------	------

Digital inputs		
Number of inputs:	32	
Optical isolation:	through opto-couplers, 1000 V	
	from PC to peripherals	
Interruptible inputs:	16 (input 0 to 15)	
Interrupt compare logic:	AND and OR mode	
Nominal voltage:	24 V (APCI-1032);	5 V (APCI-1032-5)
Input current at U nominal:	5 mA (24 V),	6 mA typ. (5 V)
Logic input levels:	U nominal: 24 V	U nominal: 5 V
UH max.:	30 V/current 7.3 mA typ.	6 V/8.4 mA typ
UH min.:	19 V/current 3.2 mA typ.	4 V/4 mA typ.
UL max.:	14 V/current 1.3 mA typ.	2 V/0.7 mA typ.
UL min. at nominal voltage:	0 V/current	0 mA typ.
Signal delay:	70 µs	
Maximal input frequency:	5 kHz at nominal voltage	

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and environmental conditions				
Dimensions:	131 x 99 mm			
System bus:	PCI 32-bit 5 V acc. to specification 2.1 (PCISIG)			
Space required:	1 PCI slot			
Operating voltage:	$+5 V$, $\pm 5 \%$ from the PC			
Max. current consumption:	(+5 V from the PC) 140 mA ± 10 %			
Front connector:	37-pin D-Sub male connector			
Temperature range:	0 to 60 °C (with forced cooling)			

APCI-1032

ST010:

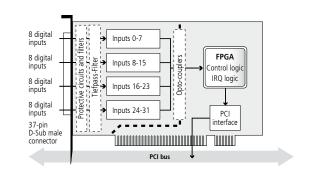
ST011:

Screw terminal panels PX9000 and PX901-DG with cable ST010

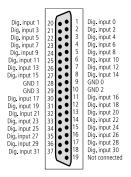


Simplified block diagram

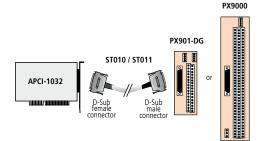
0



Pin assignment – 37-pin D-Sub male connector



ADDI-DATA connection for the APCI-1032



(without illustration: The APCI-1032-5 can only be connected to the PX901-ZG)

Ordering information

APCI-1032 / APCI-1032-5 APCI-1032: Digital input board, optically isolated, 32 digital inputs, 24 V. Incl. technical description and software drivers APCI-1032-5: Digital input board, optically isolated, 32 digital inputs, 5 V. Incl. technical description and software drivers Accessories for the APCI-1032 Accessories for the APCI-1032-5 PX901-D: Screw terminal panel, PX 901-ZG: Screw terminal panel, for DIN rail LED status display PX901-DG: Screw terminal panel, ST010: Standard round cable, shielded, twisted pairs, 2 m LED status display, for DIN rail ST011: Standard round cable, shielded, twisted pairs, 5 m PX9000: 3-row screw terminal panel for DIN rail, LED status display



Standard round cable, shielded, twisted pairs, 2 m

Standard round cable, shielded, twisted pairs, 5 m

Digital input board, optically isolated, 16 digital inputs, 24 V















DIA*dem**

Features

• 16 optically isolated digital inputs, 24 V

Safety features

- Optical isolation 1000 V
- Creeping distance IEC 61010-1
- Reverse voltage protection
- All inputs are filtered
- Protection against fast transients (burst), overvoltage, electrostatic discharge and high-frequency EMI

Applications

- Industrial I/O control
- Signal switching
- Interface to electromechanical relays
- Automatic test equipment
- ON/OFF monitoring of motors, lights...
- Machine interfacing
- ...

APCI-1016

16 digital inputs, 24 V

Optical isolation 1000 V

Input filters

Reverse voltage protection

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

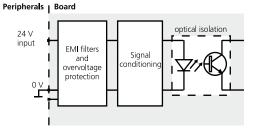
- .NET
- Microsoft VC++ Borland C++
- Visual Basic Delphi
- LabVIEW
 LabWindows/CVI
 DIAdem

ADDIPACK functions: Digital input

On request: Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads

Protective circuit for the input channels





info@addi-data.com www.addi-data.com

Digital inputs

Digital inputs	
Number of inputs:	16
Optical isolation:	Through opto-couplers, 1000 V
	from PC to peripheral
Nominal voltage:	24 V
Input current at U nominal:	6 mA typ.
Logic input levels:	U nominal: 24 V
UH max.:	30 V/current 9 mA typ.
UH min.:	19 V/current 2 mA typ.
UL max.:	14 V/current 0.7 mA typ.
UL min. at nominal voltage:	0 V/current 0 mA typ.
Signal delay:	70 µs
Maximal input frequency:	5 kHz at nominal voltage

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

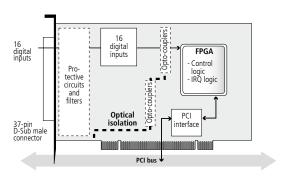
Physical and environmental conditions	
Dimensions:	131 x 99 mm (PCI short)
System bus:	PCI 32-bit 3.3/5 V acc. to specification 2.1 (PCISIG)
Space required:	1 PCI slot
Operating voltage:	$+5 \text{ V}, \pm 5 \%$ from the PC
Max. current consumption:	(+5 V from the PC) 190 mA \pm 10 mA typ.
Front connector:	37-pin D-Sub male connector
Temperature range:	0 to 60 °C (with forced cooling)

APCI-1016

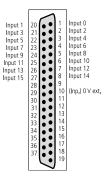
Screw terminal panels PX9000 and PX901-DG with cable ST010



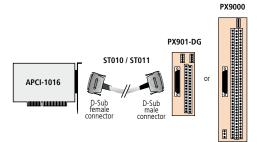
Simplified block diagram



Pin assignment – 37-pin D-Sub male connector



ADDI-DATA connection



Ordering information APCI-1016 Digital input board, optically isolated, 16 digital inputs, 24 V. Incl. technical description and software drivers Accessories PX901-D: Screw terminal panel, LED status display PX901-DG: Screw terminal panel, LED status display, for DIN rail PX9000: 3-row screw terminal panel for DIN rail, LED status display ST010: Standard round cable, shielded, twisted pairs, 2 m Standard round cable, shielded, twisted pairs, 5 m ST011:



Digital output board, optically isolated, 32 digital outputs, 24 V / 5 V



• 32 digital outputs, 24 V (APCI-2032)

Voltage range: 10 V to 36 V

Output current per channel: 500 mA

or as 5 V version (APCI-2032-5), optically isolated

Diagnostic report, through status register at short-

• Programmable watchdog for resetting the outputs

to "0", function release through software Interrupt triggered through error • At Power-On the outputs are reset to "0"

circuits, overtemperature, voltage drop or watchdog











LapAIEM¹



DIA*dem**

APCI-2032 / APCI-2032-5

32 digital outputs, 24 V or 5 V, 500 mA/channel

Optical isolation 1000 V

Output filters, short-circuit protection

Watchdog

The outputs are reset to "0"

at Power-On

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

- .NET
- Microsoft VC++ Borland C++
- Visual Basic Delphi
- LabVIEW LabWindows/CVI DIAdem

ADDIPACK functions:

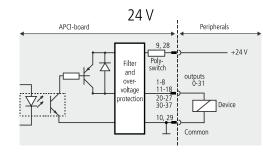
Digital output • Watchdog

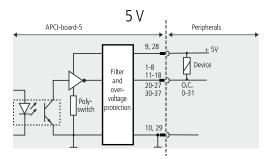
On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads

Connection principle of the outputs at 24 V (APCI-2032) and 5 V (APCI-2032-5)







(2 x 3 A) • 24 V power outputs with protection diodes and filters •

Safety features

Optical isolation 1000 V

Creeping distance IEC 61010-1

Features

•

- Self-resetting fuse (electronic fuse)
- Short-circuit current per output 1.5 A typ. ٠
- Output capacitors against electromagnetic emissions

Protection against fast transients (burst), overvoltage,

electrostatic discharge and high-frequency EMI Maximum output current for 32 outputs 6 A typ.

- Fast demagnetisation in case of inductive loads
- External 24 V voltage supply screened and filtered

Applications

- Signal switching
- Interface to electromechanical relays
- Automatic test equipment
- ON/OFF monitoring of motors, lights...
- Watchdog timer Machine interfacing

Digital outputs		
Outputs:	32	
Output type:	High-side	(load to ground) acc. to IEC 1131-2
Optical isolation:	through c	ppto-couplers, 1000 V
	from PC t	o peripheral
Nominal voltage:	24 V (APC	CI-2032); or 5 V (APCI-2032-5)
Supply voltage:	for 24 V v	version: 10 V to 36 V
	for 5 V ve	rsion: 5 V to 12 V via front connector
Max. current for 32 outputs:	6 A typ. (2	2x3 A)
Output current:	500 mA r	nax./channel
Short-circuit current/output		
shutdown at 24 V, $R_{load} < 0.1 \Omega$:	1.5 A	
RDS ON resistance:	0.4 Ω ma	х.
Switch-on time:	out=0.5 A,	load = resistance: 94 µs typ. (APCI-2032)
		load = resistance: 250 µs typ. (APCI-2032-5V)
		load = resistance: 8 µs typ. (APCI-2032)
I	out=50 mA,	load = resistance: 3 µs typ. (APCI-2032-5V)
Overtemperature (shutdown):		utput driver)
Temperature hysteresis:	20 °C (ou	tput driver)

Safety

Shut-down logic (V_{CC} diagnostic): If the ext. 24 V voltage drops below 5 V,

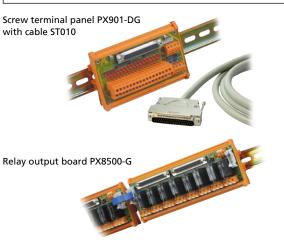
then the outputs are switched off.		
CC-Diagnostics:	Pin 19: status bit or interrupt to the PC	
Watchdog:	8-bit, programmable, 20 ms up to 5 s	
	in steps of 20 ms	

EMC – Electromagnetic compatibility

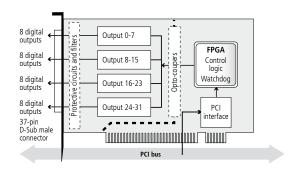
The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and environmental conditions

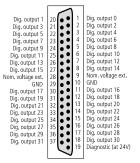
Dimensions:	131 x 99 mm
System bus:	PCI 32-bit 5 V acc. to specification 2.1 (PCISIG)
Space required:	1 PCI slot
Operating voltage:	+5 V, \pm 5 % from the PC
Current consumption:	230 mA ± 10 % typ.
Front connector:	37-pin D-Sub male connector
Temperature range:	0 to 60 °C (with forced cooling)



Simplified block diagram

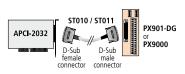


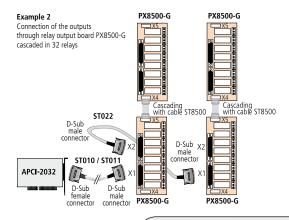
Pin assignment - 37-pin D-Sub male connector



ADDI-DATA connection

Example 1 Connection of the outputs through screw terminal panels





Ribbon cable for cascading two PX8500-G

Ordering information

APCI-2032 /APCI-2032-5

7.1 01 2002	.,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,		
APCI-2032:	Digital output board, optically isolated, 32 digital outputs, 24	V. Incl. techi	nical description and software drivers
APCI-2032-5	Digital output board, optically isolated, 32 digital outputs, 5	V. Incl. techni	ical description and software drivers
Accessorie	25		
PX901-D:	Screw terminal panel, LED status display	ST010:	Standard round cable, shielded, twisted pairs, 2 m
PX901-DG:	Same as PX901-D, for DIN rail	ST011:	Standard round cable, shielded, twisted pairs, 5 m
PX 901-ZG:	Screw terminal panel (only APCI-2032-5)	ST010-S:	Same as ST010, for high currents (24 V supply separate)
PX9000:	3-row screw terminal panel	ST022:	Round cable between two PX8500-G, shielded, 2 m

ST8500:

for DIN rail. LED status display

PX8500-G: Relay output board for DIN rail, cascadable



Digital output board, optically isolated, 16 digital outputs, 24 V





PCI 32-bit









DIA*dem**

Features

- 16 optically isolated digital outputs, 10 V to 36 V
- Output current per channel 500 mA
- Watchdog for resetting the outputs to "0"
- One ground line for all outputsAt Power-On, the outputs are reset to "0"
- Safety features
- Optical isolation 1000 V
- Creeping distance IEC 61010-1
- Protection against fast transients (burst), overvoltage, electrostatic discharge and high-frequency EMI
- Short-circuit current for 16 outputs ~ 3 A typ.
- Short-circuit current per output ~ 1.5 A typ.
- Self-resetting fuse (electronic fuse)
- Overtemperature and overvoltage protection
- 24 V power outputs with protection diodes and filters
- Output capacitors against electromagnetic emissions
- External 24 V voltage supply screened and filtered
- Shutdown logic, when the external supply voltage drops below 5 V
- Diagnostic function for detecting short-circuits and overtemperature

APCI-2016

16 digital outputs, 24 V, 500 mA/channel

Optical isolation 1000 V

Output filter, short-circuit protection

Watchdog

The outputs are reset to "0"

at Power-On

Applications

- Control of industrial PC-based processes
- Signal switching
- Interface to electromechanical relays
- Automatic test equipment
- ON/OFF monitoring of motors, lights...
- Watchdog timer
- Machine interfacing, ...

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

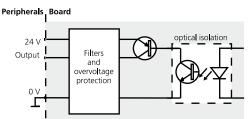
- .NET
- Microsoft VC++
 Borland C++
- Visual Basic Delphi
- LabVIEW
 LabWindows/CVI
 DIAdem

ADDIPACK functions: Digital output • Watchdog

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads





info@addi-data.com www.addi-data.com

Protective circuit for the output channels

Digital outputs

Digitat outputs			
Number of outputs:	16		
Optical isolation:	Through opto-couplers, 1000 V		
	from PC to peripheral		
Output type:	High-side (load to ground) acc. to IEC 1131-2		
Nominal voltage:	24 V		
Supply voltage:	10 V to 36 V, min. 5 V (via front connector)		
Max. current for 16 outputs:	3 A typ.		
Output current/output:	500 mA max.		
Short-circuit current/output			
shutdown at 24 V, $R_{load} < 0.1\Omega$:	1.5 A		
RDS ON resistance:	0.4 Ω max.		
Switch-on time:	l out=0.5 A, load = resistance: 100 μs		
Switch-off time:	l out=0.5 A, load = resistance: 60 μs		
Overtemperature (shutdown):	170 °C (output driver)		
Temperature hysteresis:	20 °C (output driver)		

Safety

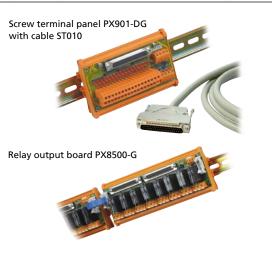
Shutdown logic:	When the ext. 24 V voltage drops below 5 V:
	The outputs are switched off.
Watchdog:	8-bit, programmable, 20 ms up to 5 s
	in steps of 20 ms

EMC – Electromagnetic compatibility

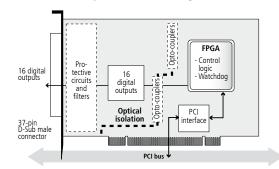
The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and environmental conditions

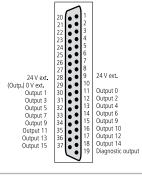
Dimensions:	131 x 99 mm
System bus:	PCI 32-bit 3.3/5 V acc. to specification 2.1 (PCISIG)
Space required:	1 PCI slot
Operating voltage:	+5 V, \pm 5 % from the PC
Current consumption:	233 mA ± 10 % typ.
Front connector:	37-pin D-Sub male connector
Temperature range:	0 to 60 °C (with forced cooling)



Simplified block diagram

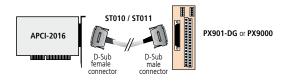


Pin assignment – 37-pin D-Sub male connector



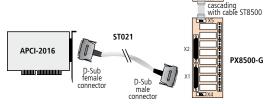
ADDI-DATA connection

Example 1 Connection of the outputs through screw terminal panels









Ordering information

PX8500-G

APCI-2016

Digital output board, optically isolated, 16 digital outputs, 24 V. Incl. technical description and software drivers

Accessori	es		
PX901-D:	Screw terminal panel, LED status display	ST010:	Standard round cable, shielded, twisted pairs, 2 m
PX901-DG:	Screw terminal panel,	ST011:	Standard round cable, shielded, twisted pairs, 5 m
	LED status display, for DIN rail	ST010-S:	Same as ST010, for high currents (separate 24 V supply)
PX9000:	3-row screw terminal panel	ST021:	Round cable between APCI-2016 and PX8500-G,
	for DIN rail, LED status display		shielded, twisted pairs, 2 m
PX8500-G:	Relay output board for DIN rail, cascadable	ST8500:	Ribbon cable for cascading two PX8500-G



Relay board, optically isolated, 8/16 relays, 8 digital inputs, 24 V



Features

Relavs

• PCI 5 V (APCI-2200)

change-over contacts

• 8 inputs, optically isolated • Input voltage: 12-24 V (DC)

• Watchdog activity can be read back • Optical isolation of the relays • Creeping distance IEC 61010-1

• Industrial digital I/O controlling Automatic test equipment

 Interface to electromechanical relays ON/OFF monitoring of motors, lights...

Short response time

Digital inputs

Safety features

Applications

Signal switching

 Alarm monitoring Machine interfacing

•

•

•

٠ ...

• EMC tested

PCI 3.3 V (APCI-2200-8-8 3,3V)

• 8 or 16 electromechanical relays with

• Max. switching capacity: 30 W, max. 1 A

• Watchdog: switched on/off through software

Max. switching voltage for the relays: 60 VDC, 48 VAC



PCI 32-bit

Also for PCI >> express see APCIe-2200 page 126











APCI-2200 / APCI-2200-8-8 3,3V

PCI 5 V (APCI-2200)

PCI 3.3 V (APCI-2200-8-8 3,3V)

8 or 16 relay output channels

Max. switching voltage 60 VDC, 48 VAC

max. switching current 1 A

8 digital inputs 24 V

Optical isolation 1000 V

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

- .NET
- Microsoft VC++ Borland C++
- Visual Basic Delphi •
- LabVIEW LabWindows/CVI DIAdem

ADDIPACK functions:

Digital output • Watchdog

On request:

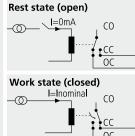
Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads

Function principle of the relays



CO: Change-over contact **CC:** Closing contact



OC: Opening contact



Relays	
Type of contacts:	8/16 change-over
Max. switching voltage:	60 VDC, 48 VAC
Max. switching current:	1 A
Max. switching capacity:	30 W
Contact resistance:	< 100 mΩ
Contact material:	Ag and Au plated
Response time:	Max. 5 ms, typ. 2.5 ms
Release time:	Max. 5 ms, typ. 0.9 ms
Mechanical life:	5 x 10 ⁶ operations
Electrical life:	10 ⁵ operations at rated load

Digital inputs

Number of inputs:	8
Optical isolation:	Through opto-couplers, 1000 V
Nominal voltage:	12 - 24 V (DC)
Nominal input current	
at 12 - 24 V (DC):	5 - 8 mA
Signal delay:	70 μs (at 24 V)
Maximal input frequency:	5 kHz (at 24 V)

20 ms to 5 s in steps of 20 ms

Watchdog

Watchdog time:

Safoty

Salety	
Test voltage:	1000 V
Watchdog:	8-bit, programmable, 20 ms to 5 in steps of 20 ms

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and environmental conditions

r nysicat and chimeintat conditions				
Dimensions:	131 x 99 mm			
System bus:	PCI 32-bit 5 V acc. to specification 2.1 (PCISIG) or 3.3 V			
Space required:	1 PCI slot			
Operating voltage:	+5 V, \pm 5 % from the PC			
Current consumption:	550 mA ± 10 % typ. (APCI-2200-16-8)			
Front connector:	50-pin D-Sub male connector			
Additional connector:	16-pin male connector.			
	APCI-2200-16-8: Connection with delivered			
	ribbon cable FB2200-3.			
	Connects the board to a bracket			
	with a 37-pin D-Sub male connector.			
	For connecting the PX 901-ZG.			
Temperature range:	0 up to 60 °C (with forced cooling)			



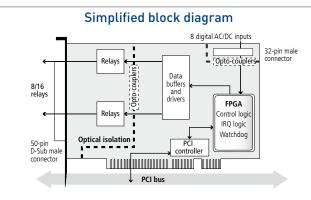
APCI-2200 / APCI-2200-8-8 3,3V

Relay board, optically isolated, 8/16 relays output channels, 8 digital inputs, 24 V. Incl. technical description and software drivers.

Accessories PX8001:

ST370-16:

APCI-2200-16-8:	16 relays, 8 dig. inputs, with ribbon cable for the connection of the dig. inputs, PCI 5 V
APCI-2200-8-8:	8 relays, 8 digital inputs, 24 V, PCI 5 V
APCI-2200-8-8_3,3V	8 relays, 8 digital inputs, 24 V, PCI 3.3 V
APCI-2200-16:	16 relays, PCI 5 V
APCI-2200-8:	8 relays, PCI 5 V



Pin assignment – 50-pin D-Sub connector APCI-2200-16-8

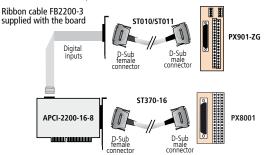
Pin		Pin		_	\sim		1	Pin
34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50	OC of relay 0 OC of relay 1 OC of relay 2 OC of relay 3 OC of relay 4 OC of relay 4 OC of relay 6 OC of relay 6 OC of relay 6 OC of relay 8 OC of relay 10 OC of relay 10 OC of relay 12 OC of relay 12 OC of relay 13 OC of relay 14 OC of relay 14 OC of relay 15	18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33	CC of relay 1 CC of relay 1 CC of relay 2 CC of relay 2 CC of relay 3 CC of relay 5 CC of relay 5 CC of relay 5 CC of relay 7 CC of relay 7 CC of relay 10 CC of relay 10 CC of relay 11 CC of relay 12 CC of relay 13 CC of relay 13 CC of relay 14 CC of relay 14 CC of relay 15	34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50	118 18 18 18 18 18 18 18 18 18	1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17	CO of relay 0 CO of relay 1 CO of relay 2 CO of relay 2 CO of relay 3 CO of relay 6 CO of relay 6 CO of relay 7 CO of relay 7 CO of relay 10 CO of relay 11 CO of relay 11 CO of relay 13 CO of relay 14 CO of relay 14 CO of relay 14 CO of relay 15	1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17

OC: Opening contact CC: Closing contact CO: Change-over contact

ADDI-DATA connection

Example 1: APCI-2200-16-8

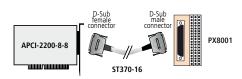
- Connection of the relay outputs through screw terminal panel PX8001
- Connection of the digital inputs through ribbon cable to the screw terminal panel PX901-ZG



Example 2: APCI-2200-8-8, APCI-2200-8, APCI-2200-16 Connection of the relay outputs and the digital inputs through the front connector to the screw terminal panel

Shielded round cable, 2 m

PX 901-ZG: Screw terminal panel for DIN rail

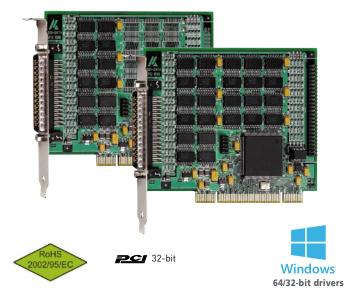


3-row screw terminal panel, 50-pin, for DIN-rail mounting

Ordering information



TTL I/O board, 48 or 96 digital TTL inputs and outputs



APCI-1696 – 96 digital TTL I/O APCI-1648 – 48 digital TTL I/O

Driver capacity up to 15 TTL loads

Can be configured as inputs

or outputs in groups of 8 channels

Filters on each I/O line







Specifications

48 TTL I/O channels - 96 TTL I/O channels

Inputs and outputs:	48 digital TTL I/O (APCI-1648)
	96 digital TTL I/O (APCI-1696)
I/O address range:	128 byte
Addressing:	32-bit
Programming:	Through write/read commands
Driver type:	74 HC 574
Max. input and output voltage:	TTL Level
Output current:	DC ± 35 mA

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and environmental conditions

Dimensions:	131 x 99 mm	
System bus:	universal PCI 32-bit 3.3/5V	
	acc. to spec. 2.2 (PCISIG)	
Space required:	1 PCI slot	
	+ 1 slot opening for bracket (APCI-1696)	
Operating voltage:	$+5 V \pm 5 \%$ from the PC	
Current consumption:	124 mA ± 10 %	
	(APCI-1696, all channels as output without load)	
Connectors:	APCI-1648: 50-pin D-Sub male connector	
	APCI-1696: 50-pin D-Sub male connector	
	50-pin D-Sub male connector and	
	I/O 49 - 96 to 50-pin male connector	
Temperature range:	0 to 60 °C (with forced cooling)	

Features

- PCI 3.3 V or 5 V
- 48 digital TTL inputs/outputs (APCI-1648)
- 96 digital TTL inputs/outputs (APCI-1696)
- Each group of 8 lines (1 port) can be configured as input or output.
- All I/Os are driven through pull-up resistors to 5 V
- Easy programming through I/O read/write commands

Connection

- APCI-1648: 50-pin D-Sub male connector
- APCI-1696: 50-pin D-Sub male connector and I/O 49 - 96 to 50-pin male connector

Software drivers

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

- .NET
- Microsoft VC++ Borland C++
- Visual Basic Delphi
- LabVIEW
 LabWindows/CVI

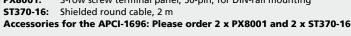
ADDIPACK functions:

Digital input • Digital output

On request:

Further operating systems, compilers and samples. Driver download: www.addi-data.com/downloads

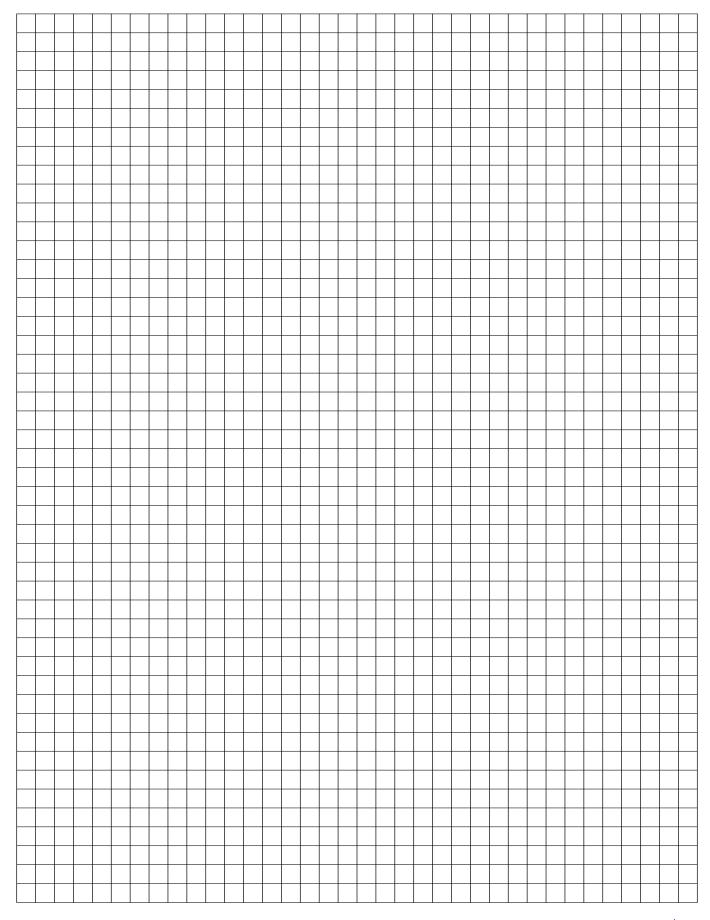
 APCI-1648: TTL I/O board, 48 digital TTL inputs and outputs. Incl. technical description and software drivers.
 APCI-1696: TTL I/O board, 96 digital TTL inputs and outputs. Incl. ribbon cable FB1696, technical description and software drivers.
 Accessories
 PX8001: 3-row screw terminal panel, 50-pin, for DIN-rail mounting





Ordering information











Also for *CompactPCI* See CPCI-1710, page 234

Also for PCL EXPRESS see APCle-1711 page 128







I abVIFW™

Adem*

DASYLab10

for SSI, incremental counter, digital I/O



The board APCI-1710 is a fast multifunction and multichannel counter board for the PCI bus. The strengths of this board are its wide range of applications and high precision and reliability for rough industrial applications. With this board you can realise many different applications on the same hardware base. The board is supplied with a pool of functions which provides the user with maximum efficiency yet minimum space and parts requirement. The functions are individually configured for each channel through the supplied software. The flexible programming facilities on this board allow many different user applications to be quickly and easily developed and reconfigured as further requirements arise. Thanks to the FPGA board structure, further counting applications can be realised through software adaptation. Contact us!

- 32-bit data access
- Signals in TTL or RS422 mode (APCI-1710), 24 V signals (APCI-1710-24V)

Functions

- · Incremental counter for the acquisition of incremental encoders (90° phase-shifted signals)
- SSI synchronous serial interface. The SSI function is an interface for systems which allow an absolute position information via serial data transfer.
- Counter/timer (82C54)
- Pulse acquisition
- Frequency measurement
- Pulse width modulation (PWM)
- Period duration measurement
- Velocity measurement
- **BiSS-Master**
- Digital inputs and outputs •
- Edge time measurement (ETM)
- Customised functions

Available channels for all four function modules

- 20 channels for digital inputs, optically isolated
- 8 channels, programmable either as digital inputs or outputs, optically isolated
- 4 digital power outputs, optically isolated

APCI-1710

Available functions:

incremental counter, SSI synchronous serial interface, counter/timer, pulse acquisition, frequency/pulse width/period duration/velocity measurement, PWM, BiSS-Master, digital inputs and outputs, ...

Function selection through software

Optical isolation

TTL, RS422, 24 V

Available lines for each function module

- 8 lines are available for each function module
- Input lines:
 - 2 x TTL and RS422 (APCI-1710) or 2 x 24 V (APCI-1710-24V)
 - 3 x 24 V, optional 5 V for channels E, F, G
- Output lines;
- 1 x 24 V, optional 5 V (power output) · 2 channels, programmable either as digital inputs or outputs, optically isolated: 2 x TTL, RS422

Safety features

- Creeping distance IEC 61010-1
- Optical isolation 1000 V
- Noise neutralisation of the PC supply

Applications

- Event counting Position acquisition
- Motion control Batch counting

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request
- RTX drivers (real-time)

Drivers and samples for the following compilers and software packages:

- .NET
- Microsoft VC++ Microsoft C
- Borland C++ Borland C
- Visual Basic Delphi
- LabVIEW

On request:

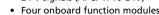
Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads menu The software functions can be adapted to your applications on request. The board can also be implemented for other software applications.



- Features

 - Up to 5 MHz input frequency



Reprogrammable functions

Wide range of applications through free combination of function modules

4 function modules quickly and easily programmable with numerous functions

Each of the four modules is programmed with one function. You can program 4 times the same function or freely combine 4 different functions.

Configuration example 1						
Function module 0	Function module 1	Function module 2	Function module 3			
1 x 32-bit Incremental counter	1 x 32-bit Incremental counter	4 x Pulse counter	3 x Counter/Timer			
Configuration ex	ample 2					
Function module 0	Function module 1	Function module 2	Function module 3			
3 x SSI	3 x SSI	1 x 32-bit Incremental counter	8 x Digital I/O, 24 V			

Programmable onboard modules

Each onboard module can be programmed with the function of your choice. You can simultaneously operate up to 4 different functions on one board. If your application must be modified, you can load a new function quickly and easily per mouse click in the SET1710 configuration program which is delivered with the board.

Overview of signal generators resp. functions

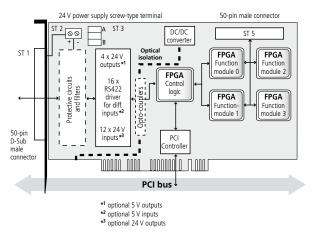
Application	Max. number of signal generators or functions for each function module	Max. number of function modules for each APCI-1710	Max. number of signal generators or functions of each APCI-1710	Page
Incremental counter	1 (32-bit) or 2 (16-bit)	4	4 or 8	168
SSI*	3	4	12	168
Chronos	1	4	4	169
BiSS-Master	1	4	4	169
Counter/Timer*	3	4	12	170
TOR	2	4	8	171
Pulse acquisition	4	4	16	172
PWM*	2	4	8	172
ЕТМ	2	4	8	173
Digital I/O	8	4	32	173
TTL	24	1	24	-

* Function not available for APCI-1710-24V



Customer-tailored modifications, designed to suit your needs. Hardware and software, firmware, PLDs, ... Contact us!

Simplified block diagram

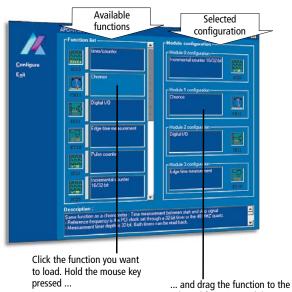


Pin assignment

Pin		Pin			-			Pin	
34 +UREF 35 H0* 36 H1* 37 H2* 38 H3* 39 E0* 40 E1* 41 E2* 42 E3* 43 F0* 44 F1* 45 F2* 46 F3* 47 G0* 48 G1* 49 G2* 50 G3*	FUNCTION MODULE 3 FUNCTION MODULE 2	18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33	A2+ A2- B2+ B2- C2+ C2- D2- D2- A3+ A3- B3+ C3+ C3- D3+ D3-	34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50	18	1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17	EXTGND A0+ A0- B0+ C0+ C0- D0+ D0- A1+ A1- B1+ B1- C1+ C1- D1+ D1-	1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17	FUNCTION MODULE 0 FUNCTION MODULE 1

*Each number corresponds to the number of the function module

Program SET1710



selected function module



Function Incremental encoder ____

Up to 2 incremental encoders can be connected to a module programmed with the function **Incremental encoder**.

- 90° phase-shifted input signals (displacement measurement systems)
- Motion control
- Pulse width and frequency measurement
- Incremental encoder acquisition
 Tolerance measurement
- Velocity measurement
- Rotation measurement
- Electronic "mouse"

Function range of the counter component

- Simple, double, quadruple analysis of 2 phase-shifted clock pulses (A, B)
- Direction recognition for upwards or downwards counting
- Hysteresis circuit for the absorption of the first pulse after a change in rotation: switchable
- 2 x 32-bit data latches, indiv. programmable for internal / external strobe, latch strobe synchronised with an internal clock pulse
- Operating mode is defined by an internal mode register, loadable and readable through the data bus
- Strobe inputs which can be triggered either through 2 external pins (24 V input) or by writing in a register
- Interrupt indication triggered through the external strobe inputs
- Compare logic, interrupt and reference point logic

Used signals

Signal	Pin		
name	name	Signal type	Function
A_x	Ax +/-	Diff./TTL/24 V*	A signal of the incremental encoder (32-bit) resp. A signal of the incremental encoder 0 (16-bit)
B_x	Bx +/-	Diff./TTL/24 V*	B signal of the incremental encoder (32-bit) resp. B signal of the incremental encoder 0 (16-bit)
INDEX_x	Cx +/-	Diff./TTL/24 V*	Index signal of the incremental encoder (32-bit)
C_x	Cx +/-	Diff./TTL/24 V*	A signal of the incremental encoder 1 (2x16-bit)
UAS_x	Dx +/-	Diff./TTL/24 V*	Error signal input (32-bit)
D_x	Dx +/-	Diff./TTL/24 V*	B signal of the incremental encoder 1 (2x16-bit)
REF_x	Ex	24 V / 5 V optional	Digital input (can also control the reference point logic)
ExtStrb_a_x	Fx	24 V / 5 V optional Active High	Digital input (can be used for latch logic, respectively generate an interrupt)
ExtStrb_b_x	Gx	24 V / 5 V optional Active High	Digital input (can be used for latch logic, respectively generate an interrupt)
DIG_OUT_x	Hx	24 V / optional 5 V	Digital output

x: Number of the function module (See pin assignment page 167) * 24 V for the APCI-1710-24 V

Function Synchronous serial interface (SSI)

The function module is programmed as a synchronous serial interface. The **SSI** function is an interface for systems which allow an absolute position information through serial data transfer.

Typical application examples:

- Acquisition of displacement measurement systems
- Axis control (X, Y and Z)
- Tolerance measurement ...



Block diagram SSI

	Output register SSI clock
Mode register	← Input register 1 ← SSI data 1
register	Cutput register 2
	← Input register 3 ← SSI data 3
	→ Digital I/O

Properties

- 4 function modules for each board, up to 3 SSI encoders per function module (depends on encoder)
- Optical isolation of the inputs and outputs through opto-couplers to prevent ground loops
- Serial data transfer
- Common clock pulse for the 3 interfaces per function module
- Clock frequency and number of data bits are software-programmable
- GRAY to BINARY conversion possible
- For each function module there are 3 digital inputs and 1 digital output for an additional function (no effect on SSI function)

The interface includes:

- Three independent 32-bit SHIFT registers, which can be read through the data bus
- Clock and pulse generator
- Function and control logic

Used signals

Signal	Pin		
name	name	Signal type	Function
Clock_x	Ax +/-	RS422	Clock output signal for the SSI encoders
DATA1_x	Bx +/-	RS422/TTL	Data input 1 for the first encoder
DATA2_x	Cx +/-	RS422/TTL	Data input 2 for the second encoder
DATA3_x	Dx +/-	RS422/TTL	Data input 3 for the third encoder
Input1_x	Ex	24 V / 5 V optional	Digital input 1
Input2_x	Fx	24 V / 5 V optional	Digital input 2
Input3_x	Gx	24 V / 5 V optional	Digital input 3
Output_x	Hx	24 V / 5 V optional	Digital output

x: Number of the function module (See pin assignment page 167) The SSI function cannot be programmed on the APCI-1710-24 V.



Function Chronos

The function **Chronos** is a timer interface which allows to measure the time between two "events" like a chronometer.

- 3 functions are available:
- a 32-bit timer to create a time reference,
- a 32-bit measuring timer to determinate and measure the time between start and stop pulse.
- 3 digital inputs and 3 digital outputs

Properties

- Optical isolation of the inputs and outputs through opto-couplers to prevent ground loops
- Interrupt status at the end of the measuring time
- Timer readable
- Input and output channels can be inverted through software, Software GATE possible

Function description

The pulse signals from Timer 0 are counted between the start pulse signal and the stop pulse signal. The number of pulses is then stored in the measuring timer and can be read through I/O read commands. The timer 0 is used as a time reference generator. The divider factor is written in timer 0

Block diagram Chronos

and determines the output frequency.

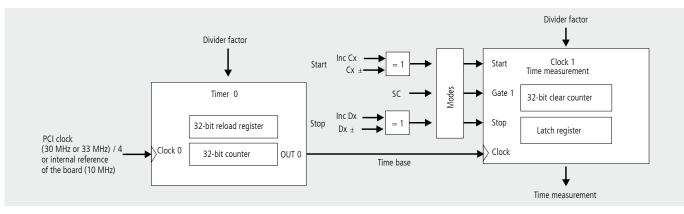
The input frequency is set according to the PCI clock pulse or to the 10 MHz onboard clock generator. Timer 0 is synchronised with the start event or with the 40 MHz quartz of the board.

Timer 0 can be read at any time. The **Chronos** function can be used in 8 different modes.

Used signals

Pin name	Signal type	Function
Ax +/-	Diff./TTL, 24 V*	Digital output 1; set to "0" after reset
Bx +/-	Diff./TTL, 24 V*	Digital output 2; set to "0" after reset
Cx +/-	Diff/TTL/24 V*	Start pulse for measuring
Dx +/-	Diff/TTL/24 V*	Stop pulse for measuring
Ex	24 V / 5 V optional	Digital input 0, inverting
Fx	24 V / 5 V optional	Digital input 1, inverting
Gx	24 V / 5 V optional	Digital input 2, inverting
Hx	24 V / 5 V optional	Digital output 0; set to "0" after reset

x: Number of the function module (See pin assignment page 167) *24 V for the APCI-1710-24 V



Function BiSS-Master _

The function **BiSS-Master** is a bidirectional sensor interface for the communication with positioning encoders. The following types of communications are supported:

Functions of the BiSS-Master:

- Sensor data transmission
- Register data transmission
- Multicycle data transmission

More information about the function range of the BiSS interface on <u>www.biss-interface.com</u>.

Limits and differences with respect to the BiSS specification:

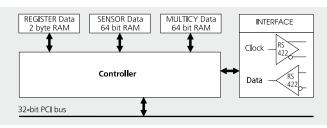
- One channel available, to which a BiSS slave can be connected
- The functionality "automatic sensor data request" is not available
- Data buffer for two sensor registers available
- The board APCI-1710-10K20 is required

Used signals

Signal name	Pin name	Signal type	Function
Input_Ch1_x	Cx +/-	Diff.	Digital input 1 (data line
			from slave to master)
Output Ch1_x	Ax +/-	Diff.	Digital output 1 (clock line
			from master to slave)

x: Number of the function module (See pin assignment page 167)

Block diagram BiSS-Master





Function Counter/Timer _

Function equivalent to Intel 82C54

The module **Counter/Timer** can be used as a programmable interval counter/timer (similar to Intel 82C54) with 3 x 32-bit per module.

It generates time delays through software control. Instead of setting up timing loops in software, the user programs the module for the desired delays. After this delay, the module will interrupt the PC.

- Optical isolation of the inputs and outputs through opto-couplers to prevent ground loops
- 3 x 32-bit counters/timers, binary counting only
- 6 programmable modes
- Status readback and latch command
- Inputs and outputs can be inverted through software
- Hardware and software gate possible, readable
- Simple interface: no multiple assignment of the addresses
- Interrupt enabled with an individual release bit per counter/timer and interrupt status register
- Available clock: PCI clock divided by four (APCI-1710 only) or 10 MHz of the onboard quartz oscillator, selectable through software

Typical applications:

- Event counter
- Programmable rate generator
- Binary rate multiplier
- Square-wave generator
- Complex motor controller / signal generator

Programmable modes

For each counter/timer (3 x 32-bit) there are 6 modes available: mode 0 to mode 5, which can be programmed independently.

Mode 0: Interrupt on terminal count

Mode 0 is particularly suitable for event counting. The output is initially set to "Low" and remains "Low" until the counter reaches 0.

The output then goes "High" and holds this state until a new count or a new counter value is written into the counter.

Mode 1: Hardware retriggerable one-shot

This mode is identical to mode 0 except for the GATE input. The GATE input is not used to activate or deactivate the timer, but to trigger it.

Mode 2: Pulse generator

In this mode the counter is dividing the choosed input clock through the start value "ul_ReloadValue". Mode 2 is used for generating a real-time clock interrupt.

After initialization the output is set to "High". When the initial count has decremented to 1, the output goes "Low". For only one clock pulse the output is setted on "Low" then it goes back to "High". The counter reloads the start value ("ul_ReloadValue") and the counter sequence is repeated. The number of sequences is unlimited. An interrupt can be generated at the end of the cycle. Calculation of time: (ul_ReloadValue +2) x input clock

Mode 3: Square wave mode

Mode 3 is used for baud rate generation. It is similar to mode 2 except for the duty cycle of the output. The output is initially set to "High". When half the initial count has expired, the output is set to "Low" for the remainder of the count. Mode 3 is periodic; the same sequence is repeated indefinitely. Calculation of time: (ul_ReloadValue +2) x input clock

Mode 4: Software-triggered strobe

The output is initially set to "High". When the initial count expires, the output goes "Low" for one clock pulse and then goes "High" again. The counting sequence is triggered by writing the initial count. If a new count is written during counting, it will be loaded on the next clock pulse and counting will continue from the new count.

Mode 5: Hardware-triggered strobe (retriggerable)

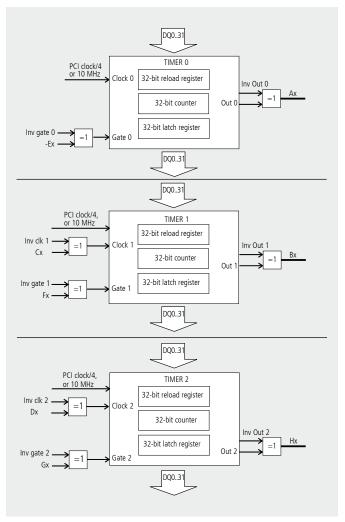
This mode is identical to mode 4 except for the GATE input. The GATE input is not used to activate or deactivate the timer, but to trigger it.

Used signals

Signal name	Pin name	Signal type	Function
OUT1_x	Ax +/-	Diff./TTL	Output of counter/timer 0
OUT2_x	Bx +/-	Diff./TTL	Output of counter/timer 1
OUT3_x	Hx	24 V / 5 V opt.	Output of counter/timer 2
GATE1_x	Ex	24 V / 5 V opt.	GATE Input of counter/timer 0
GATE2_x	Fx	24 V / 5 V opt.	GATE Input of counter/timer 1
GATE3_x	Gx	24 V / 5 V opt.	GATE Input of counter/timer 2
CLK1_x	-	-	Internal clock
CLK2_x	Cx +/-	Diff./TTL/ 24 V opt.	Clock counter input
			of counter/timer 1
CLK3_x	Dx +/-	Diff./ TTL/ 24 V opt.	Clock counter input
			of counter/timer 2

x: Number of the function module (See pin assignment page 167) The Counter/Timer function cannot be programmed on the APCI-1710-24 V.

Block diagram Counter/Timer





Function TOR

The **TOR** function is a counter interface which allows counting input signals in a defined time interval.

2 TOR counters are available on each function module. Each TOR counter includes 2 x 32-bit timers.

The TOR function is a scaled-down version of the Counter/Timer

function. The pulse signal of Timer 1 gives the start and stop pulse signal to Timer 0. Timer 0 counts the input signals. After the stop signal from Timer 0 the number of pulses is stored and can be read through I/O read commands. The timer 1 is used as a time reference generator.

The divider factor is written in timer 1 and determines the output frequency. The input frequency is set according to the PCI clock pulse or the 40 MHz on-board quartz clock. Timer 0 is synchronised with the start event.

Pulse measurement

As soon as a start signal occurs from Timer 1, the Timer 0 is reset.

It counts the pulse signals of the channel Ax(Bx).

During the process the status bit "Counter in Progress" is set. As soon as a stop signal occurs from Timer 1, the Timer 0 is stopped and the status bit "Counter in Progress" is reset.

An interrupt can also be generated. The value can then be read. The latest measured value is read in the counter measurement register.

Properties

- Optical isolation of the inputs and outputs through opto-couplers to prevent ground loops
- Interrupt status at the end of the measuring period
- Inputs and output can be inverted through software
- Software GATE

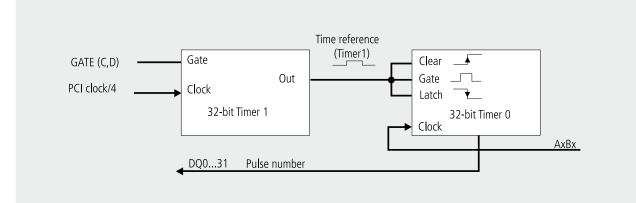
The function **TOR** occupies 4 inputs (A to D) of the corresponding function module of the APCI-1710 or CPCI-1710.

Used signals

Pin name	Signal type	Function	
Ax +/-	Diff./TTL/24 V*	Digital input 1 (TOR1)	
Bx +/-	Diff./TTL/24 V*	Digital input 2 (TOR2)	
Cx +/-	Diff./TTL/24 V*	External Gate (TOR1)	
Dx +/-	Diff./TTL/24 V*	External Gate (TOR2)	

x: Number of the function module (see pin assignment page 167) $^{\star}24\,V$ for the APCI-1710-24 V

Block diagram TOR





Function Pulse counter_

The **Pulse counter** is an interface for the acquisition of external digital pulses. Each rising or falling edge on the counter input starts decrementing from the initially set counter value. An interrupt is generated at logical "0", i.e. the digital output is set or reset.

- 4 x 32-bit downward counters
- Optical isolation of the inputs and outputs through opto-couplers to prevent ground loops
- Each counter can be loaded with a predefined counter value
- Interrupt at overflow
- Output can be set or reset at overflow
- Polarity of the inputs selectable through software

The interface includes:

- 4 x 32-bit counters
- 4 independent 32-bit registers, readable through the data bus
- a function and control logic.

Block diagram Pulse counter Counter 0 Ax+/-32-bit down counter OUT_ENA0 Output Gate0 Gate Н Latch0 Latch Read register 0 PCI bus ≥ 1 Counter 3 Dx+/-32-bit down counter Gate3 Gate Latch3 Latch Read register 3 PCI bus IRO

Used signals

Pin name	Signal type	Function
Ax +/-	Diff./TTL/24 V*	Input of the 1st counter
Bx +/-	Diff./TTL/24 V*	Input of the 2nd counter
Cx +/-	Diff./TTL/24 V*	Input of the 3rd counter
Dx +/-	Diff./TTL/24 V*	Input of the 4th counter
Н	24 V/5 V optional	Common digital output of the counter

x: Number of the function module (see pin assignment page 167)

* 24 V for the APCI-1710-24V

Function PWM (Pulse width modulation)

The function **PWM** is an interface for pulse width modulation. It generates a frequency and defines the time duration (pulse width) of the "Low" and "High" level. The function generates rectangle signals. The output pulses from the timer generate the pulse width modulation.

PWM generator

The "Low/High" time-divider factor is written in the timer and determines the output frequency. The input frequency is set according to the PCI clock or the 40 MHz quartz of the board. The function includes:

- The function includes:
- a 32-bit frequency generator for setting the "Low" and "High" levels
- 2 digital inputs as start or stop trigger
- 2 digital frequency outputs

Properties

- Optical isolation of the inputs and outputs through opto-couplers to prevent ground loops
- Interrupt status at the end of a period
- Selection of the start level
- Selection of the stop level
- Hardware gate
- Software gate

Typical applications

- Frequency generation
- Pulse width modulation
- Drive technology

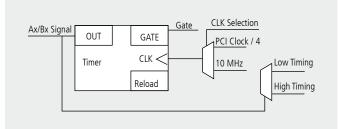
Used signals

Signal name	Pin name	Signal type	Function
PWM_OUT_Ch0_x	Ax +/-	Diff./TTL output	digital output PWM 0
PWM_OUT_Ch1_x	Bx +/-	Diff./TTL output	digital output PWM 1
GATE_Ch0_x	Cx +/-	Diff./TTL input	Gate input PWM 0
GATE_Ch1_x	Dx+/-	Diff./TTL input	Gate input PWM 1
DIG_IN_E_x	Ex	24 V input	digital input
DIG_IN_F_x	Fx	24 V input	digital input
DIG_IN_G_x	Gx	24 V input	digital input
DIG_OUT_H_x	Hx	24 V output	digital output PWM 0 or freely controllable

x: Number of the function module (See pin assignment page 167)

The PWM function only can be used restricted to the 24 V version. Only PWM0 is available for the DIG_OUT_H_x 24 V output.

Block diagram PWM





Function ETM (Edge Time Measurement)

The **ETM** function is a timer interface which allows measuring the duration of a period, and simultaneously, the "High" or "Low" level time of this period. A function module with the ETM function has

- 1 timer to create a time base
- 2 counters to measure the period duration
- 2 counters to measure the "High" or "Low" level time
- 2 gate inputs

The ETM function uses 4 inputs (A to D) with each function module of the APCI-1710 or CPCI-1710. Up to 8 ETM (2 per module) can be operated on one board.

Properties

- Optical isolation of the inputs and outputs through opto-couplers to prevent ground loops
- Interrupt status at the end of a period
- Timer can be read back
- Inputs and outputs can be inverted through software
- Software gate

Block diagram ETM

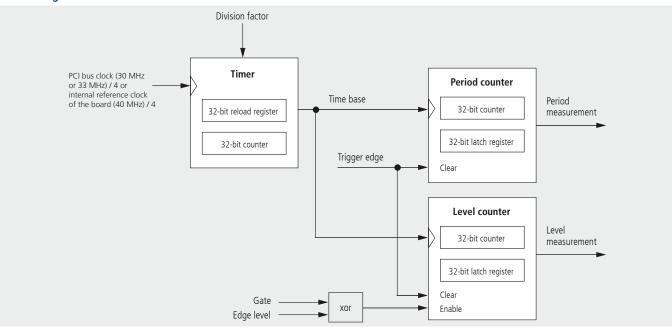
Typical applications

- Period duration measurement
- Level duration measurement

Used signals

Signal name	Pin name	Signal type	Function
Gate0_x	Ax +/-	Diff./TTL/opt. 24 V*	Gate input of ETM counter 0
Input0_x	Bx +/-	Diff./TTL/opt. 24 V*	Input of ETM counter 0
Gate1_x	Cx +/-	Diff./TTL/opt. 24 V*	Gate input of ETM counter 1
Input1_x	Dx +/-	Diff./TTL/opt. 24 V*	Input of ETM counter 1

x: Number of the function module (see pin assignment page 167) *24 V for the APCI-1710-24V



Function Digital input and output

The **Digital input and output** function allows an easy access to the digital I/O available on the function modules. The I/O level of the input and output channels are read and set or reset through read/write commands. The digital I/O have no logical connection to each other. The connection can only be made through software. The complete isolation through opto-couplers avoids earth circuits.

Available channels

- 3 x 24 V mass-related input channels, optional 5 V
- 2 x differential input channels (RS422/485),
- can also be used as TTL input channels
- 1 digital output, 24 V, load to ground (10 to 36 V / 500mA)
- 2 differential inputs or outputs (RS485),
- can also be used as TTL input or output channels. Software configuration.

When the digital I/O function is programmed on all function modules, up to 28 digital input and 12 digital output channels are available

Used signals

Pin name	Signal type	Function
Ax +/-	Diff/TTL/24 V*	Dig. input and output (with 24 V* only input)
Bx +/-	Diff/TTL/24 V*	Dig. input and output (with 24 V* only input)
Cx +/-	Diff./TTL/24 V*	Digital input
Dx +/-	Diff./TTL/24 V*	Digital input
Ex	24 V / 5 V optional	Digital input
Fx	24 V / 5 V optional	Digital input
Gx	24 V / 5 V optional	Digital input
Hx	24 V / 500 mA (10 – 36 V)	Digital output

x: Number of the function module (see pin assignment page 167) *with the APCI-1710-24V



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-	
Counter componer	nts
	Counting depth: 32-bit, Counting frequency: up to 5 MHz
Free programming	g of the functions
	32-bit or 16-bit acquisition of incremental encoders
	Acquisition of absolute encoders/SSI
	Counter/timer
	Chronos/TOR for frequency measurement
	Pulse acquisition
	Chronos for pulse width modulation
	Chronos for period duration measurement
	TOR for velocity measurement
	BiSS-Master
	Digital I/O, 24 V, TTL, RS422 PWM
	ETM
	Customised functions
Signals	
	Digital I/O signals, TTL or RS422
Inputs	
Number of inputs:	20
Differential inputs or outpu	
5 V inputs:	8/16 (8 can be used as inputs or outputs)
Nominal voltage:	5 VDC
Common mode range:	+12 / -7 V
Max. differential voltage	±12 V
Input sensitivity:	200 mV
Input hysteresis:	50 mV
Input impedance:	12 kΩ
Terminal resistor:	150 Ω serial with 10 nF (typ.)
Signal delay:	120 nS (at nominal voltage) 5 MHz (at nominal voltage)
Max. input frequency: Mass-related inputs, 24 V (
Number of inputs:	12
Nominal voltage:	24 VDC
Input current:	11 mA (typical) at nominal voltage
Logic input levels:	Unominal: 24 V
- •	UH max.: 30 V
	UH min.: 19 V
	UL max.: 15 V
	UL min.: 0V
Signal delay:	120 ns (at nominal voltage)
Maximal input frequency:	2.5 MHz (at nominal voltage)
Outputs	
Nominal voltage:	5 VDC
Maximum output frequency:	5 MHz (diff. outputs)
Max. number of outputs:	8 (if they are not used as diff. inputs)
Digital outputs, 24 V:	High side (load to ground)
Output type: Number of outputs:	High-side (load to ground) 4
Nominal voltage:	4 24 VDC
Range of the supply voltage:	10 V to 36 VDC (via 24 V ext. pin)
Maximum current	2 A typ. (limited to the voltage supply)
for 4 outputs:	z / (jp. (innited to the voltage supply)
Maximum output current:	500 mA short-circuit current/
	1.5 A max. (output switches off)
output at 24 V, $R_{load} < 0.1 \Omega$: ON-resistance of the output	· ·
(RDS ON resistance)	0.4 O may

Overtemperature: 170 °C (all outputs switch off)							
Overtemperature protection	(24 V outputs)						
Activated:	From approx. 150-170 °C (chip temperature)						
Deactivated (automatically):	From approx. 125-1	From approx. 125-140 °C (chip temperature)					
Outputs (at overtemperature): Outputs switch off							
Protection against undervoltage (effective at V ext.<5 V):							
Outputs (at undervoltage):	All outputs switch	off					
Switching characteristics of	the 24 V outputs						
(V ext. = 24 V, T=25 °C, ohmic loa	ad: 500 mA) :						
Switch ON time:	200 µs						
Switch OFF time:	15 µs						
Digital outputs, 5 V (option):							
Output type:	TTL						
Number of outputs:	4						
Nominal voltage:	5 VDC						
Switching characteristics of	the 5 V outputs (T	=25 °C, TTL load):					
Switch ON time:	0.06 µs						
Switch OFF time:	0.02 µs						
Technical data for the APCI-1	710-24 V board v	ersion					
24 V inputs (channels A to G). Thi	s board version is ex	clusively used for connecting 24 V					
encoders. Only 24 V signals can b	e connected to the i	nput channels.					
Nominal voltage:	24 VDC / 10 mA						
Max. input frequency:	1 MHz (at nominal	voltage)					
Logic input levels :	Unominal:	24 V					
(Standard)	UH max.:	30 V					
	UH min.:	19 V					
	UL max.:	15 V					
	UL min.:	0 V					
Safety							
Jaiety							

Optical isolation:

EMC – Electromagnetic compatibility

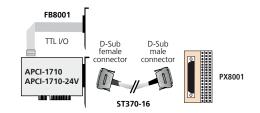
1000 V

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and environmental conditions

· · · ·	
Dimensions:	179 x 99 mm
System bus:	PCI 32-bit 5 V acc. to specification 2.1 (PCISIG)
Space required:	1 slot
Operating voltage:	$+5 \text{ V}, \pm 5 \%$ from the PC
	+24 V ext. /10 mA
Current consumption:	APCI-1710-x: 1.15 A typ. ± 10 %
Front connector:	50-pin D-Sub male connector
Additional connector:	Male connector for the TTL I/O function
Temperature range:	0 to 60 °C (with forced cooling)

ADDI-DATA connection



Ordering information

APCI-1710

(RDS ON resistance):

 Multifunction counter board, optically isolated, encoder, incremental counter, timer/counter, SSI, PWM,...

 Incl. technical description and software drivers.

 APCI-1710:
 Multifunction counter board, optically isolated.

 APCI-1710:24V:
 24 V for differential input signals (A and B for counter, I (Index) and UAS (error) signals).

 APCI-1710-5V-I:
 5 V inputs (E, F, G) instead of 24 V

 APCI-1710-5V-I:
 5 V inputs (E, F, G) instead of 24 V, outputs (H1, H2, H3, H4) 5 V instead of 24 V

 APCI-1710-10K20:
 Same as APCI-1710, with additional function for connecting a Biss interface

 Option
 Accessories

Opt. 5V: 5 V outputs (H1, H2, H3, H4) instead of 24 V

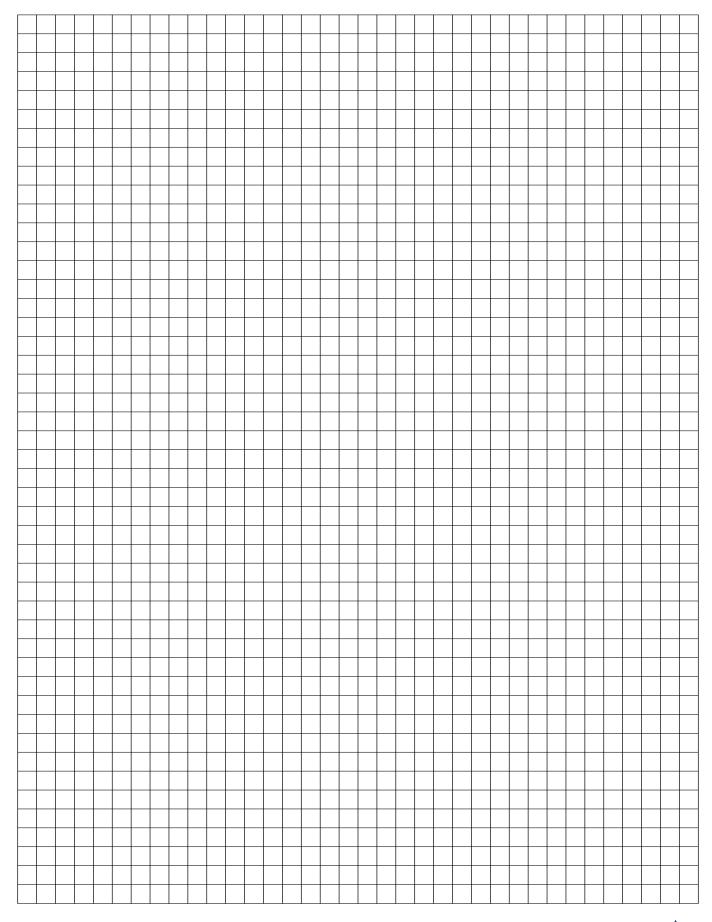
ST370-16:Shielded round cable, 2 mPX8001:3-row terminal panel for DIN railFB8001:Ribbon cable for connecting the TTL I/O function

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 $0.4 \,\Omega$ max.







PCI BOARDS: ANALOG I/O



High-precision measurement in the field

There are numerous measurement systems that provide precise data under laboratory conditions. This is different in the production or in outdoor areas, where the conditions are considerably more demanding. There the measurement results play a central role. Rework and defective goods can only be effectively reduced if the tolerance testing during the production process is precise enough to sort products reliably as being within or without the tolerance range. The importance of accuracy appears even clearer in cases where the measurement is used for regulation.

High-precision measurement

Measurement technology is the basis of every automation process. Therefore the accuracy of the sensor acquisition is highly important. The environment of a production line with a lot of interference requires peak performance from the measurement technology. For more than 25 years, ADDI-DATA has been developing analog boards for data acquisition intended for an industrial environment: they are robust, precise and fast.

Single-ended or differential inputs

When measuring input voltage you can choose between 2 modes with important differences:

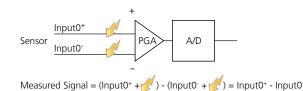
Single-ended mode – One-wire inputs connected to the system ground.



Measured Signal = Input 0+ + 🎷

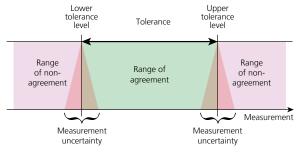
When acquiring analog signals in the single-ended mode, interfering signals are acquired with the signal. Therefore this mode is only advisable in case of high voltage levels and short lines.

Differential mode - Two-wire inputs



Interference signals affecting both lines are not included in the measurement because of the difference on the input. This is the optimal mode for measurement lines with a lot of interferences and long lines.





Precise even in case of temperature drift

Temperature drift can be caused by the surrounding temperature as well as by the board itself. To ensure the accuracy of the measurement, on the one hand we are careful to use only high-quality components with little drift. On the other hand we pay a lot of attention to the board layout. For example, components that generate heat are placed where they will not heat up the other components unnecessarily.

Precision through interference resistance

Not only the quality of the A/D converter is important but the interference resistance of the whole chain of acquisition has to be analysed, from the sensors to the acquisition board. Therefore in addition to our PC boards we offer robust cables and screw terminal panels that are intended for the use in a harsh industrial environment.

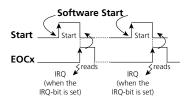
Acquisition modes of the analog inputs

There are four modes available for analog inputs.

The following is a short overview of the settings available for data acquisition:

A. Simple mode

The software initiates and starts the A/D conversion and reads out the digital values of one or more channels after the end of conversion.



B. Sequence modes

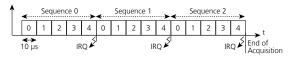
With the DMA function (Direct Memory Access) for a direct data exchange with the PC memory

There are 2 available sequence modes:

1. Simple sequence mode

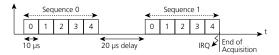
In this example the interrupt is generated at the end of each sequence after 5 acquisitions.

The complete acquisition process ends after 3 sequences.



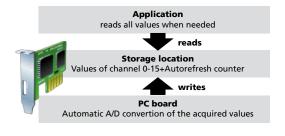
2. Sequence mode with delay

In this example the delay time between the end of one sequence and the start of the next sequence is 20 $\mu s.$



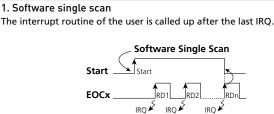
C. Auto refresh mode

The analog acquisition is initialised and the values of the channels are written in a buffer on an analog board. The PC reads the data asynchronously to the acquisition.



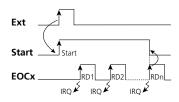
D. Scan mode

There are 6 different scan modes:

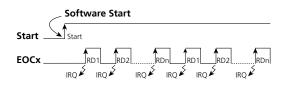


2. Hardware triggered single scan

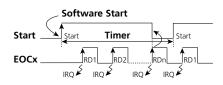
This scan can be triggered with increasing or decreasing edge. The hardware trigger allows to start the acquisition independently from the software or to start the acquisition of more than one board at the same time.



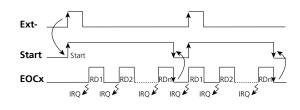
3. Continuous scan (software)



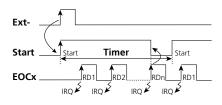
4. Continuous scan with timer delay (software)



5. Continuous scan (hardware)



6. Continuous scan with timer delay (hardware)





Multifunction, analog input and analog output boards for 3.3 V or 5 V PCI

	Mult	ifunction bo	bards		Anal	og input bo	ards		Analog output boards
	APCI-3120	APCI-3110	APCI-3116	APCI-3010	APCI-3016	APCI-3002	APCI-3003	APCI-3001	APCI-3501
32-bit PCI bus	3.3/5 V	3.3/5 V	3.3/5 V	3.3/5 V	3.3/5 V	3.3/5 V	3.3/5 V	3.3/5 V	3.3/5 V
FPGA	1	1	1	1	1	1	1	1	1
Simultaneous acquisition							1		
Analog inputs	•		^	•					
Single Ended	16/8	16/8	16/8	16/8/4	16/8/4			16/8/4	
Differential	8/4	8/4	8/4	8/4/2	8/4/2	16		8/4	
Diff. separated from each other							4		
Resolution (-bit)	16	12	16	12	16	16	16	12	
Optical isolation	500 V	1	1	1	1	1	1	1	
Throughput (kHz)	100	200	200	200	200	200	400/ channel	100	
Voltage range							chunner		
0-10 V; ± 10 V / 0-5 V; ± 5 V	1	1	1	1	1	1	1	1	
$\frac{0-2 \text{ V}; \pm 2 \text{ V} / 0-1 \text{ V}; \pm 1 \text{ V}}{\text{Other ranges (optional)}}$	0-20 mA	0-20 mA	0-20 mA	0-20 mA	0-20 mA	0-20 mA	0-20 mA	0-20 mA	
Gain 1, 2, 5, 10		1	1	1	1	1	1	1	
FIFO (value)	256	512	512	512	512	512	512	256	
Functions of the analog inputs									
DMA (scatter gather, single, continuous, Sequence)		1	1	1	1	1	1		
DMA (single, continuous, Sequence)	1							1	
Auto Refresh		1	1	1	1	1	1		
Interrupt	1	1	1	1	1	1	1	1	
Programmed I/O	1	1	1	1	1	1	1	1	
Trigger: Software TTL input	✓ -	✓ -	<i>1</i>	✓ -	✓ -	✓ -	✓ -	✓ -,	
24 V input Sequence RAM			<i>\</i> <i>\</i>	<i>\</i> <i>\</i>	<i>\</i>	✓ ✓	✓ ✓	✓ ✓	
Analog outputs	4 or 8	4	4	· ·	•	~	~	• •	4 or 8
Resolution (-bit)	14 or 8	12	12						4 or 8 14
Optical isolation	-								
0-10 V ± 10 V		<i>I</i>	<i>\</i>						✓ ✓
Current outputs	· ·		v						•
Setup time	30 µs	15 µs	15 µs						30 µs
-	50 µs	15 µs	12 μ2						50 μs
Digital I/O 24 V inputs, optically isolated 24 V outputs, optically isolated	4 4 (OpenC)	4 4 (50mA)	4 4 (50mA)	4 4 (50mA)	4 4 (50mA)	4 4 (50mA)	4 4 (50mA)	4 4 (OpenC)	2 2 (OpenC)
TTL I/O		24	24	24	24				
Timer/Counter/Watchdog (depth) and/or	1 / / 1 24-bit	3 / 3 / 2 16-bit	3 / 3 / 2 16-bit	3 / 3 / 1 16-bit	3 / 3 / 1 16-bit	1 / – / – 16-bit	1 / – / – 16-bit	1 / – / – 16-bit	1 / - / - 12-bit
Page	180	182	182	184	184	186	188	190	192
Software	Current driver	rrrent driver list on the web: www.addi-data.com							



PC

Temperature, pressure, noise, vibration and length measurement

	New!				
	Temperature measurement	Pressure measurement	Noise and vibration measurement	Length me	asurement
	APCI-3200	APCI-3300	APCI-3600	APCI-3702	APCI-3701
64-bit or 32-bit PCI-Bus	3.3 V / 5 V	3.3 V / 5 V	3,3 V / 5 V	3.3 V / 5 V	3.3 V / 5 V
FPGA			1		
Noise and vibration			1		
Thermocouples J,K,T,E,R,S,B,N Pt100, Pt1000	1				
Strain gauges		1			
Inductive transducers				Half Bridge, LVDT	Half Bridge, LVDT
Signal conditioning			8 current sources for connecting ICP™ sensors		
Analog inputs	4 groups 4 channels	4 groups 4 channels			
Single Ended (SE)/ differential (diff.)	16 thermo/8 RTDs 8 thermo/4 RTDs 4 thermo/2 RTDs	8/4 inputs for strain gauges	8/8	5 channels <i>simultaneous</i> acquisition for induct. displacement transducers	16/8/1 channels for inductive displacement transducers
Resolution (-bit)	18	18	24	16	16
Optical isolation	1	1			
Throughput	20-160 Hz	20-160 Hz	2-200 kHz (through software)	depends on transducer type	depends on transducer type 2-20 kHz (50 kHz opt.)
Voltage ranges	+ 1.25 V	+ 1.25 V	± 10 V		
Gain	1, 2, 4, 8, 16, 32, 64, 128	1, 2, 4, 8, 16, 32, 64, 128		depends on transducer type	depends on transducer type
FIFO (Values)			128 DWORD		
Functions of the analog inp	uts				
DMA (scatter gather; single; continuous; Sequence)			scatter gather free run, ring buffer	1	1
Auto Refresh				1	1
Interrupt	1	1	1	1	1
Programmed I/O	1	1	✓	✓	1
Trigger: Software 24 V input	1		1	<i>\</i>	1 1
Sequence RAM	V	V	· · · ·	V	✓ ✓
Analog outputs			2		
Resolution (-bit)			16		
Optical isolation					
0-10 V ± 10 V			± 10 V		
Chronometer inputs Gate inputs			4 2		
Timer/Watchdog (depth) in combination, and/or	-1-	-1-	-1-	1 / – 16-bit	1 / – 16-bit
Digital I/O Inputs 24 V, optically isolated Outputs 24 V, optically isolated	4 3 (Open Collector)	4 3 (Open Collector)	8 8 (50 mA)	8 8 (125 mA)	8 8 (125 mA)
Page	194	198	200	204	206
Software	Current driver list on the web:	www.addi-data.com	I		1



Multifunction board, optically isolated, 16/8 SE or 8/4 diff. inputs, 4/8 analog outputs,16-bit





PCI 32-bit

Also for *CompactPCI*TM See CPCI-3121, page 238

Also for

PCI EXPRESS[®] see APCIe-3121, page 134







LabVIEW™



DASYLab10



Features

Analog inputs

- 16 single-ended/8 differential inputs or
 8 single-ended/4 differential inputs
- 16-bit resolution
- Optical isolation 500 V
- Throughput: 100 kHz
- Input voltage: 0-10 V, ±10 V, 0-5 V, ±5 V,
 0-2 V, ±2 V, 0-1 V, ±1 V, 0-20 mA (option) freely programmable through software for each channel
- Gain PGA x1, x2, x5, x10 freely programmable through software for each channel
- PCI DMA for analog data acquisition
- Overvoltage protection
- Input filters: 159 kHz
- Analog acquisition
- One single channel, several channels, several channels through scan list
- Automatic analog acquisition through cyclic timer control
- Acquisition through scan list: up to 16 entries with gain, channel, unipolar/bipolar
- Acquisition triggered through software, timer, external event
- Trigger functions: Software trigger or
- external trigger: the analog acquisition (single or sequence) is started through signal switching from 0 V to 24 V at the digital input 0.
- Interrupt: end of single channel, end of multichannel, end of scan list
- Analog outputs
- 4 or 8 analog outputs, optically isolated 500 V
- Setup time 10 µs typ.
- 14-bit resolution (13-bit for 0-10 V)
- Output voltage: ±10 V, 0-10 V (through software)
- Output voltage after reset: 0 V
- Each output has its own ground line (without optical isolation)
- Driver capacity: 5 mA/500 pF
- Short-circuit protection, EMI filters

Digital

• 4 dig. inputs, 4 dig. outputs, 24 V, optically isolated

Timer

• As cyclic time counter or as watchdog

APCI-3120

16 Single-ended/8 differential inputs, 16-bit

8/4 analog outputs, 14-bit

Optical isolation of inputs and outputs, 500 V

PCI DMA, programmable gain

Trigger functions

8 digital I/O, 24 V, optically isolated, timer

On-site calibration with the CAL3120 option

Safety features

- Optical isolation 500 V min.
- Creeping distance IEC 61010-1
- Overvoltage protection ± 40 V
- Protection against high-frequency EMI
- Input filters: 160 kHz
- Noise neutralisation of the PC supply

Applications

- Industrial process control
- Industrial measurement and monitoring
- Multichannel data acquisition
- Control of chemical processes
- Factory automation
- Acquisition of sensor data, current measurement
- Laboratory equipment, instrumentation

Software

Calibration tool (**Option CAL3120**): Do the fine adjustment fast and reliably and save the generated calibration report file. All you need is a highly precise calibration source and a precise digital multimeter (not included in the delivery content).

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request
- RTX drivers (real-time)

Drivers and samples for the following compilers and software packages:

- .NET on request
- Microsoft VC++ Microsoft C
- Borland C++ Borland C
- Visual Basic Delphi
- LabVIEW
 LabWindows/CVI
 DASYLab
 DIAdem

On request:

Further operating systems, compilers and samples.





Specifications

Analog inputs	
Number of inputs:	16 single-ended/8 differential inputs or 8 single-ended/4 differential inputs
Resolution:	16-bit resolution
Optical isolation:	500 V through opto-couplers from PC to peripheral
Input ranges:	software-programmable for each channel 0-10 V, ±10 V, 0-5 V, ± 5 V, 0-2 V, ± 2 V, 0-1 V, ± 1 V, 0-20 mA optional
Throughput:	100 kHz
Gain:	Software programmable (1, 2, 5, 10)
Common mode rejection:	DC at 10 Hz, 90 dB minimum
Relative precision (INL):	± 1 LSB (ADC)
Diff. non-linearity (DNL):	± 0.5 LSB (ADC)
Input impedance (PGA):	$10^{12} \Omega$ / 10 nF single-ended,
	$10^{12} \Omega$ / 20 nF differential against GND
Bandwidth (- 3 dB):	Limited to 159 kHz with low-pass filter
Trigger:	Through software, timer, external event (24 V input)
Data transfer:	Data to the PC through FIFO memory,
	I/O commands, interrupt at EOC (End Of Conversion)
	and EOS (End of Scan), DMA transfer at EOC
Interrupts:	End of conversion, at timer overrun, End of scan

Analog outputs

Number of outputs:	4 or 8
Resolution:	14-bit resolution
Optical isolation:	500 V through opto-couplers
Output range:	0-10 V, ±10 V switchable through software
Setup time at 2 k Ω , 1000 pF:	10 μs at 10 V step
Overvoltage protection:	±12 V
Max. output current / load:	±5 mA / 500 pF, 2 kΩ
Short-circuit current:	±25 mA
Output voltage after reset:	0 V

Digital I/O

Number of I/O channels:	4 dig. inputs, 4 dig. outputs, 24 V
Optical isolation:	1000 V through opto-couplers
Input current at 24 V:	3 mA typ.
Input range:	0-30 V
Output range:	5-30 V
Max. switching current:	10 mA typ.

EMC – Electromagnetic compatibility

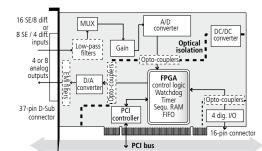
The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

PC system requirements and

environmental conditions

Dimensions:	169 x 99 mm
System bus:	PCI 32-bit 3.3/5 V acc. to specification 2.1 (PCISIG)
Space required:	1 PCI slot for analog I/O,
	1 slot opening for digital I/O with FB3000
Operating voltage:	+5 V, ±5 % from the PC
Current consumption:	From 710 to 790 mA typ. depending on the board version
Front connector:	37-pin D-Sub male connector
Additional connector :	16-pin male connector for connecting the dig. I/O
Temperature range:	0 to 60 °C (with forced cooling)

Simplified block diagram



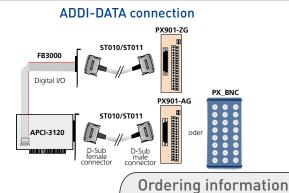
Pin assignment – 37-pin D-Sub male connector

DIFF	SE		 ١	SE	DIFF
(+) An. input 0 (+) An. input 1 (+) An. input 2 (+) An. input 2 (+) An. input 3 (-) An. input 1 (-) An. input 1 (-) An. input 1 (-) An. input 1 (-) An. output An. output An. output An. output An. output An. output	ut GND 0 GND 1 GND 2 GND 3 GND 4 GND 5 GND 6 GND	20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37	1 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19	(+) An. input 8 (+) An. input 9 (+) An. input 10 (+) An. input 11 (+) An. input 15 (+) An. input 13 (+) An. output An. output An. output An. output An. output An. output An. output	GND }1 GND }1 t 0 t 1 t 2 t 3 t 4 t 5 t 6

1: The analog inputs have a common ground line 2: Each analog output has its own ground line

Pin assignment – 16-pin male connector

1 🔳 🗖 2	Dig. output 0 (-)
3 🔳 🖿 4	Dig. output 1 (-)
5 🔳 🖬 6	Dig. output 2 (-)
7 🔳 🔳 8	Dig. output 3 (-)
	Trigger/dig. input 0 (-)
11 🔳 🔳 12	Dig. input 1 (-)
13 🔳 🔳 14	Dig. input 2 (-)
15 🔳 🔳 16	Dig. input 3 (-)
	3 • • 4 5 • • 6 7 • • 8 9 • 10 11 • 12 13 • 14



APCI-3120

Multifunction board, optically isolated, 16 SE/8 diff. inputs, 4/8 analog outputs, 16-bit.

Incl. technical description, monitoring program and software drivers.

Versions

101310113	
APCI-3120-16-8	Version with 16 SE/8 diff. inputs, 8 analog outputs
APCI-3120-16-4	Version with 16 SE/8 diff. inputs, 4 analog outputs
APCI-3120-8-8	Version with 8 SE/4 diff. inputs, 8 analog outputs
APCI-3120-8-4	Version with 8 SE/4 diff. inputs, 4 analog outputs
Options	
Please indicate the	ne number of channels
Option SF:	Precision filter for 1 single-ended channel
Option DF:	Precision filter for 1 diff. channel
Option PC:	Current input 0(4)-20 mA for 1 channel
	PC-SE: for single-ended PC-Diff: for differential

Option CAL3120: Only for 32-bit operation system. On-site calibration of the APCI-3120. Do the fine adjustment fast and reliably and then save the calibration report file.

Accessories

PX901-A:	Screw terminal panel for connecting the analog I/O
PX901-AG:	Same as PX901-A with housing for DIN rail
PX_BNC:	BNC connection box for connecting the analog I/O
PX901-ZG:	Screw terminal panel for connecting the dig. I/O
ST010:	Standard round cable, shielded, twisted pairs, 2 m
ST011:	Standard round cable, shielded, twisted pairs, 5 m
FB3000:	Ribbon cable for digital I/O



Multifunction board, optically isolated, 16/8 SE or 8/4 diff. inputs, 4 analog outputs, 12-/16-bit





PCI 32-bit







LabVIEW^{TI}



LabWindows/CVI™



Customer-tailored modifications designed to suit your needs. Hardware and software, firmware, PLDs, ... Contact us!

Features

• PCI 3.3 V or 5 V

Analog inputs

- 16/8 SE or 8/4 diff. inputs, optically isolated
- Resolution: 12-bit (APCI-3110) or 16-bit (APCI-3116) Throughput: 200 kHz
- Input voltage: 0-10 V, ±10 V, 0-5 V, ±5 V, 0-2 V, ±2 V, 0-1 V, ±1 V, 0-20 mA (option), freely programmable through software for each channel
- Current inputs: 0-20 mA (Option)
- can be combined freely with voltage inputs Gain PGA x1, x2, x5, x10 freely programmable through software for each channel

Analog acquisition

- Different input modes:
- 1) Simple mode
- 2) Scan modes
- 3) Sequence modes
- 4) Auto Refresh mode
- Onboard FIFO (for 512 analog values)
- PCI-DMA for analog data acquisition
- Analog outputs
- 4 analog outputs, optically isolated
- 12-bit resolution
- Setup time 15 µs typ
- Output voltage after reset: 0 V
- Each output has its own ground line (without optical isolation)
- Output voltage range: -10 V up to + 10 V
- Output current: ± 5 mA
- Short-circuit current: ± 20 mA

24 V digital I/O

- 24 V digital I/O enable a high interference distance and a long distance between signal transmitter and data acquisition
- 4 digital inputs, 24 V, optically isolated
- 4 digital outputs, 24 V, optically isolated
- **TTL I/0**
- 24 digital TTL inputs/outputs
- Port0: outputs / Port1: inputs / Port2: I/O
- All I/O are at 5 V through pull-up resistors
- Easy programming through I/O read and write commands

Timer/counter

• 3 / 3, 16-bit

- Watchdog
- 2, 16-bit

APCI-3110 / APCI-3116

PCI 3.3 V or 5 V

Optical isolation 1000 V 16/8 SE or 8/4 diff. inputs 12-bit or 16-bit resolution, 200 kHz PCI DMA, programmable gain 4 analog outputs, 12-bit Timer/counter/watchdog

8 optically isolated dig. I/O, 24 V, 24 TTL I/O

Safety features

- Optical isolation 1000 V min.
- Creeping distance IEC 61010-1
- Circuit part of the analog acquisition is separated from the circuit part of the digital function
- Overvoltage protection ± 40 V
- Protection against high-frequency EMI
- Input filters
- Noise neutralisation of the PC supply
- Connection of the I/O signals through robust industrystandard D-Sub connector

Applications

- Industrial process control
- Industrial measurement and monitoring
- Multichannel data acquisition
- Control of chemical processes
- Factory automation
- Acquisition of sensor data
- Laboratory equipment
- Current measurement
- Instrumentation

Software

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

- .NET
- Microsoft VC++ Borland C++ Visual Basic •
- Delphi LabVIEW LabWindows/CVI

ADDIPACK functions:

Analog input • Analog output • Digital input Digital output • Watchdog • Timer • Counter

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com, download menu



info@addi-data.com www.addi-data.com

Specifications

Analog inputs	
Number of inputs:	16/8 SE or 8/4 differential inputs
Resolution:	12-bit (APCI-3110) or 16-bit (APCI-3116)
Optical isolation:	1000 V through opto-couplers from PC to peripheral
Input ranges:	Software-programmable for each channel
	0-10 V, ±10 V, 0-5 V, ± 5 V, 0-2 V, ± 2 V, 0-1 V, ± 1 V
	0-20 mA optional
Gain:	Software programmable (x1, x2, x5, x10)
Throughput:	200 kHz
Trigger:	through software, timer, external event (24 V input)
Data transfer:	Data to the PC through FIFO memory,
	Interrupt at EOC (End Of Conversion),
	DMA transfer at EOC
Interrupts:	End of conversion, at timer overrun. End of scan

Analog outputs

Number of outputs:	4
Optical isolation:	1000 V through opto-couplers
Resolution:	12-bit
Voltage outputs	
Output range:	-10 V to +10 V (-1 LSB)
LSB:	4.8828 mV
Accuracy:	11-bit
Time to Ready:	typ. 4.5 μs
Setup time:	typ 15 μs (at 10 V step)
Max. output current:	± 5 mA
Short-circuit current:	± 20 mA
Output voltage after reset:	0 V

Digital I/O

Number of I/O channels:	4 digital inputs, 24 V 4 digital outputs, 24 V
Logical "0" level:	0-14 V
Logical "1" level:	19-30 V
Optical isolation:	1000 V through opto-couplers from PC to peripheral

TTL I/O

Number of TTL I/O cha	nels: 24
I/O Address range:	128 Byte, addressing : 32-bit
Programming:	Through write/read commands

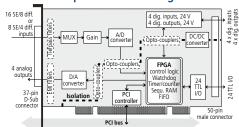
EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and environmental conditions

Dimensions:	175 x 99 mm
System bus:	PCI 32-bit 3.3/5V acc. to spec. 2.2 (PCISiG)
Space required:	1 PCI slot for analog I/O,
	1 slot opening for digital I/O with FB8001
Operating voltage:	$+5$ V, ± 5 % from the PC
Front connector:	37-pin D-Sub male connector
Additional connector :	50-pin male connector for connecting the dig. I/O
Temperature range:	0 to 60 °C (with forced cooling)

Simplified block diagram



Pin assignment – 37-pin D-Sub male connector

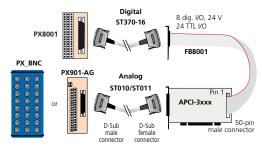
DIFF	SE	\square	SE	DIFF
Channel 0 (+) Channel 1 (+) Channel 2 (+) Channel 3 (+)	Channel 0 Channel 1 Channel 2 Channel 3	20 • 1 21 • 2 22 • 3 22 • 4	Channel 8 Channel 9 Channel 10 Channel 11	Channel 4 (+) Channel 5 (+) Channel 6 (+) Channel 7 (+)
Channel 3 (-) Channel 2 (-) Channel 1 (-) Channel 0 (-)	Channel 7 Channel 6 Channel 5 Channel 4 Signal GND	23 • • 5 24 • • 5 25 • • 6 26 • • 7 27 • 8 28 • 9	Channel 15 Channel 14 Channel 13 Channel 12 Signal GND	Channel 7 (-) Channel 6 (-) Channel 5 (-) Channel 4 (-)
An out An out	Signal GND out 0 GND out 1 GND out 2 GND out 3 GND	29 • 10 30 • 11 31 • 12 32 • 13 33 • 14	An. o An. o	utput 0 utput 1 utput 2
	Signal GND Signal GND Signal GND Signal GND	34 • 15 35 • 16 36 • 17 37 • 18	An. o Signal GND Signal GND Signal GND Signal GND	utput 3

Pin assignment - 50-pin male connector

Assignment	Pin		Assignment
Output 3	1	2	Input 3+
Input 3-	3	4	Output 2
Input 2+	5	6	Input 2-
Output 1	7	8	Input 1 +
Input 1-	9	10	Output 0
Input 0+	11	12	Input 0-
GND 0	13	14	+24 V
Not connected	15 bis 24		Not connected
GND	25	26	GND
TTL 15	27	28	TTL 23
TTL 7	29	30	TTL 14

Assignment	P	in	Assignment
TTL 22	31	32	TTL 6
TTL 13	33	34	TTL 21
TTL 5	35	36	TTL 12
TTL 20	37	38	TTL 4
TTL 11	39	40	TTL 19
TTL 3	41	42	TTL 10
TTL 18	43	44	TTL 2
TTL 9	45	46	TTL 17
TTL 1	47	48	TTL 8
TTL 16	49	50	TTL 0

ADDI-DATA connection



Ordering information

APCI-3110 / APCI-3116

Multifunction board, optically isolated, 16/8 SE or 8/4 diff. inputs, 4 analog outputs, 12-/16-bit. Incl. technical description and software drivers.

Versions

APCI-3110-16:	16 SE/8 diff. inputs, 4 analog outputs, 12-bit
APCI-3110-8:	8 SE/4 diff. inputs, 4 analog outputs, 12-bit
APCI-3116-16:	16 SE/8 diff. inputs, 4 analog outputs, 16-bit
APCI-3116-8:	8 SE/4 diff. inputs, 4 analog outputs, 16-bit
APCI-3116-16:	8 SE/4 diff. inputs, 4 analog outputs, 12-bit 16 SE/8 diff. inputs, 4 analog outputs, 16-bi

Options

Please indicate the number of channels Option SF: Precision filter for 1 single-ended channel Option DF: Precision filter for 1 diff. channel Option PC: Current input 0(4)-20 mA for 1 channel PC-SE: for Single-ended PC-Diff: for differential

Accessories

7100000011	
PX901-A:	Screw terminal panel with transorb diodes
	for connecting the analog I/O
PX901-AG:	Same as PX901-A with housing for DIN rail
PX_BNC:	BNC connection box for connecting the analog I/O
ST010:	Standard round cable, shielded, twisted pairs, 2 m
ST011:	Standard round cable, shielded, twisted pairs, 5 m
PX8001:	3-row screw terminal panel, 50-pin, for DIN-rail mounting
FB8001:	Ribbon cable for digital I/O
ST370-16:	Standard round cable, shielded, twisted pairs, 2 m



Analog input board, optically isolated, 16/8/4 SE or 8/4/2 diff. inputs, 12-/16-bit













LabVIEW[™]



LabWindows/CVI™



Customer-tailored modifications designed to suit your needs. Hardware and software, firmware, PLDs, ... Contact us!

Features

- PCI 3.3 V or 5 V
- Analog inputs
- 16/8/4 SE or 8/4/2 diff. inputs, optically isolated
- Resolution: 12-bit (APCI-3010) or 16-bit (APCI-3016)
- Throughput: 200 kHz
- Voltage inputs: 0-10 V, ±10 V, 0-5 V, ±5 V, 0-2 V, ±2 V, 0-1 V, ±1 V, freely programmable through software for each channel
- Current inputs: 0-20 mA (option)
- can be combined freely with voltage inputs
- Gain PGA x1, x2, x5, x10 freely programmable through software for each channel

Analog acquisition

- Different input modes:
 - 1) Simple mode
 - 2) Scan modes
 - 3) Sequence modes

 - Software trigger or
- external trigger: the analog acquisition (single or sequence) is started through the signal on digital input 0 from 0 V to 24 V
- Onboard FIFO (for 512 Analog values)
- PCI-DMA for analog data acquisition

24 V digital I/O

- 24 V digital I/O enable a high interference distance and a long distance between signal transmitter and data acquisition
- 4 digital inputs, 24 V, optically isolated
- 4 digital outputs, 24 V, optically isolated

TTL I/O

- 24 digital TTL inputs/outputs
- Port1: inputs / Port2: outputs / Port3: I/O
- All I/O are at 5 V through pull-up resistors
- Easy programming through I/O read and write commands

Timer/Counter

- 3 / 3, 16-bit
- Watchdog
- 1. 16-bit

APCI-3010 / APCI-3016

PCI 3.3 V or 5 V

Optical isolation 1000 V

16/8/4 SE or 8/4/2 diff. inputs

12- or 16-bit resolution, 200 kHz

PCI DMA, programmable gain

Trigger functions

Timer/counter/watchdog

8 optically isolated dig. I/O, 24 V, 24 TTL I/O

Safety features

- · For more protection in noisy industrial environment
- Optical isolation 1000 V min.
- Creeping distance IEC 61010-1
- Circuit part of the analog acquisition
- is separated from the circuit part of the digital function Overvoltage protection ± 40 V (analog inputs)
- Protection against high-frequency EMI ٠
- Input filters
- Noise neutralisation of the PC supply
- Connection of the I/O-signals through robust industrystandard 37-pin D-Sub connector

Applications

- Industrial process control
- Industrial measurement and monitoring
- Multichannel data acquisition
- Control of chemical processes
- Factory automation
- Acquisition of sensor data
- Laboratory equipment
- Current measurement
- Instrumentation

Software

A CD-ROM with the following software and programming examples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP

Real-time use with Linux and Windows on request •

Drivers and samples for the following compilers and software packages:

- .NFT
- Microsoft VC++ Borland C++

Visual Basic • Delphi • LabVIEW • LabWindows/CVI ADDIPACK functions:

Analog input • Digital input • Digital output

Watchdog • Timer • Counter

On request:

Further operating systems, compilers and samples.



- - - 4) Auto Refresh mode
 - Trigger functions:

Specifications

Analog inputs	
Number of inputs:	16/8/4 SE or 8/4/2 differential inputs
Resolution:	12-bit (APCI-3010) or 16-bit (APCI-3016)
Optical isolation:	1000 V through opto-couplers from PC to peripheral
Input ranges:	Software-programmable for each channel
	0-10 V, ±10 V, 0-5 V, ± 5 V, 0-2 V, ± 2 V, 0-1 V, ± 1 V
	0-20 mA optional
Gain:	Software programmable (x1, x2, x5, x10)
Throughput:	200 kHz
Trigger:	Through software, timer, external event
	(24 V input)
Data transfer:	Data to the PC through FIFO memory,
	Interrupt at EOC (End Of Conversion),
	DMA transfer at EOC
Interrupts:	End of conversion, at timer overrun,
	End of scan
Digital I/O	

•	
Number of I/O channels:	4 digital inputs, 24 V
	4 digital outputs, 24 V
Logical "0" Level:	0-14 V
Logical "1" Level:	19-30 V
Optical isolation:	1000 V through opto-couplers from PC to peripheral
Outputs	High Side, 50 mA

TTL I/O

Number of TTL I/O channels:	24
Programming:	Through write/read commands

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and environmental conditions

Dimensions:	175 x 99 mm	
System bus:	PCI 32-bit 3.3/5V acc. to spec. 2.2 (PCISiG)	
Space required:	1 PCI slot for analog inputs,	
	1 slot opening for digital I/O with FB8001	
Operating voltage:	+5 V, ±5 % from the PC	
Front connector:	37-pin D-Sub male connector	
Additional connector:	50-pin male connector for connecting the dig. I/O	
Temperature range:	0 to 60 °C (with forced cooling)	
Screw terminal panel PX90	1-AG Connection box PX_BNC	

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Screw terminal panel PX901-AG with cable ST010



APCI-3010 / APCI-3016

Analog input board, optically isolated, 16/8/4 SE or 8/4/2 diff. inputs, 12-/16-bit. Incl. technical description and software drivers.

with cable ST010

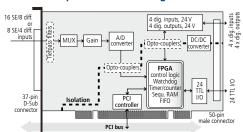
v	ρ	r٩	10	ns	
	~	•••	•••		

10113	
APCI-3010-16:	16 SE/8 diff. inputs, 12-bit
APCI-3010-8:	8 SE/4 diff. inputs, 12-bit
APCI-3010-4:	4 SE/2 diff. inputs, 12-bit
APCI-3016-16:	16 SE/8 diff. inputs, 16-bit
APCI-3016-8:	8 SE/4 diff. inputs, 16-bit
APCI-3016-4:	4 SE/2 diff. inputs, 16-bit

Options

Please indicate t	he number of channels
Option SF:	Precision filter for 1 single-ended channel
Option DF:	Precision filter for 1 diff. channel
Option PC:	Current input 0(4)-20 mA for 1 channel
	PC-SE: for Single-ended PC-Diff: for differential

Simplified block diagram



Pin assignment – 37-pin D-Sub male connector

DIFF	SE		SE	DIFF
An. input 0 (+) An. input 2 (+) An. input 2 (+) An. input 3 (+) An. input 2 (-) An. input 2 (-) An. input 1 (-) An. input 0 (-)	An, input 0 An, input 1 An, input 2 An, input 3 An, input 3 An, input 6 An, input 6 An, input 4 An, signal GND An, signal GND	20 • 1 21 • 2 22 • 3 23 • 4 24 • 5 25 • 6 26 • 7 27 • 8 28 • 9 29 • 10 30 • 11 31 • 12 32 • 13 33 • 14	An, input 8 An, input 9 An, input 10 An, input 11 An, input 11 An, input 13 An, input 13 An, input 12 An, signal GND An, signal GND	An, input 4 (+) An. input 5 (+) An. input 6 (+) An. input 7 (+) An. input 7 (-) An. input 5 (-) An. input 5 (-) An, input 4 (-)
	An. signal GND An. signal GND An. signal GND An. signal GND	34 35 36 37 16 16 17 17 18 19	An. signal GND An. signal GND An. signal GND An. signal GND	

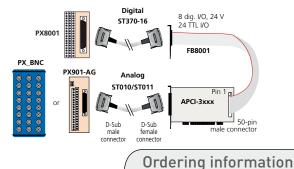
Pin assignment – 50-pin male connector

Assignment Assignment Pin Output 3 Input 3+ 2 Input 3-4 Output 2 3 6 Input 2+ 5 Input 2-Output 1 Input 1 + 10 Input 1-9 Output 0 Input 0+ 11 12 Input 0-13 14 GND 0 +24 V Not conne 15 to 24 Not connect GND 25 26 GND TTL 15 TTL 7
 27
 28
 TTL 23

 29
 30
 TTL 14

Assignment	P	in	Assignment
TTL 22	31	32	TTL 6
TTL 13	33	34	TTL 21
TTL 5	35	36	TTL 12
TTL 20	37	38	TTL 4
TTL 11	39	40	TTL 19
TTL 3	41	42	TTL 10
TTL 18	43	44	TTL 2
TTL 9	45	46	TTL 17
TTL 1	47	48	TTL 8
TTL 16	49	50	TTL 0

ADDI-DATA connection



Accessories

ACCUSS	51105
PX901-A:	Screw terminal panel with transorb diodes,
	for connecting the analog inputs
PX901-AG:	Same as PX901-A with housing for DIN rail
PX_BNC:	BNC connection box for connecting the analog inputs
ST010:	Standard round cable, shielded, twisted pairs, 2 m
ST011:	Standard round cable, shielded, twisted pairs, 5 m
PX8001:	Screw terminal panel for connecting
	the digital I/O, for DIN rail
FB8001:	Ribbon cable for digital I/O
ST370-16:	Standard round cable, shielded, twisted pairs, 2 m



Analog input board, optically isolated, 16 differential inputs, 16-bit





PCI 32-bit







LabVIEW™







Customer-tailored modifications designed to suit your needs. Hardware and software, firmware, PLDs, ... Contact us!

Features

• PCI 3.3 V or 5 V

Analog inputs

- 16 differential inputs
- 16-bit resolution
- Throughput: 200 kHz
- Voltage inputs: 0-10 V, ±10 V, 0-5 V, ±5 V, 0-2 V, ±2 V, 0-1 V, ±1 V, freely programmable through software for each channel
- Current inputs: 0-20 mA (option)
- can be combined freely with voltage inputs • Gain PGA x1, x2, x5, x10 freely programmable through
- software for each channel

Analog acquisition

- Different input modes for the analog acquisition:
 1) Simple mode
- 2) Scan modes
- 3) Sequence modes
- 4) Auto Refresh mode
- Trigger functions:
 - software trigger or
- external trigger: the analog acquisition (single or sequence) is started through the signal on digital input
 from 0 V to 24 V
- Onboard FIFO
- PCI-DMA

24 V digital

- 24 V digital I/O enable a high interference distance and a long distance between signal transmitter and data acquisition
- 4 digital inputs, 24 V, optically isolated
- 4 digital outputs, 24 V, optically isolated

Timer

• 1, 12-bit

Safety features

- For more protection in noisy industrial environment
- Optical isolation 1000 V
- Creeping distance IEC 61010-1
- Overvoltage protection ±40 V
- Protection against high-frequency EMI
- Input filters
- Noise neutralisation of the PC supply

APCI-3002

PCI 3.3 V or 5 V

Optical isolation 1000 V

16 differential inputs,

200 kHz throughput

16-bit resolution

PCI DMA, programmable gain

Trigger functions, timer

8 optically isolated digital I/O, 24 V

Applications

- Industrial process control
- Industrial measurement and monitoring
- Multichannel data acquisition
- Control of chemical processes
- Factory automation
- Acquisition of sensors
- Laboratory equipment
- Current measurement
- Instrumentation

Software drivers

A CD-ROM with the following software and programming examples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

- .NET
- Visual Basic Delphi
- LabVIEW
 LabWindows/CVI

ADDIPACK functions:

Analog input • Digital input • Digital output • Timer

On request:

Further operating systems, compilers and samples.





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Specifications

Ana	DO C	in	nute
Alla	lug		puts

Analog inputs	
Number of inputs:	16 differential inputs
Resolution:	16-bit
Optical isolation:	1000 V through opto-couplers from PC to peripheral
Input ranges:	Software-programmable for each channel
	0-10 V, ±10 V, 0-5 V, ± 5 V, 0-2 V, ± 2 V, 0-1 V, ± 1 V
	0-20 mA optional
Gain:	Software programmable (x1, x2, x5, x10)
Throughput:	200 kHz
Trigger:	Through software, timer, external event
	(24 V input)
Data transfer:	Data to the PC through FIFO memory,
	Interrupt at EOC (End Of Conversion),
	DMA transfer at EOC
Interrupts:	End of conversion, at timer overrun,
	End of scan

Digital I/O

Number of I/O channels:	4 digital inputs, 24 V, 4 digital outputs, 24 V,
	50 mA typ., Open Collector
Logical "0" Level:	0-14 V
Logical "1" Level:	19-30 V
Optical isolation:	1000 V through opto-couplers from PC to peripheral

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and environmental conditions

Dimensions:	175 x 99 mm
System bus:	PCI 32-bit 3.3/5V acc. to specification 2.2
	(PCISiG)
Space required:	1 PCI slot for analog inputs,
	1 slot opening for digital I/O
Operating voltage:	+ 5 V, ± 5 % from the PC
Current consumption:	814 mA ± 10 mA
Front connector:	37-pin D-Sub male connector
Additional connector:	16-pin male connector for ribbon cable
	for connecting the digital inputs and outputs
Temperature range:	0 to 60 °C (with forced cooling)

Screw terminal panel PX901-AG with cable ST010

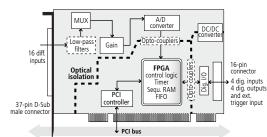
APCI-3002

Analog input board, optically isolated, 16 diff. inputs, 8 digital I/O, 16-bit. Incl. technical description and software drivers.

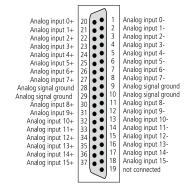
Options

Please indicate the number of channels **Option PC-diff:** Current input for 1 differential channel 0(4)-20 mA **Option DF:** Precision filter for 1 channel



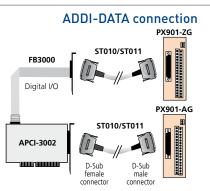


Pin assignment – 37-pin D-Sub male connector



Pin assignment – 16-pin male connector

24 V ext.	14 13 12 11 10 9 8 7	Digital input 3 + Digital input 2 + Digital input 1 + Digital input 0 + OC output 3 (24 V) OC output 2 (24 V)
24 V ext.	6 1 5	OC output 2 (24 V)
Ground (dig. outputs)	4 1 3	OC output 1 (24 V)
Ground (dig. outputs)	2 1 1	OC output 0 (24 V)



Ordering information

Accessories

I

PX901-AG:	Screw terminal panel with transorb diodes,
	with housing for DIN rail
	for connecting the analog inputs
PX901-ZG:	Screw terminal panel for connecting
	the digital I/O, for DIN rail
ST010:	Standard round cable, shielded, twisted pairs, 2 m
ST011:	Standard round cable, shielded, twisted pairs, 5 m
FB3000:	Ribbon cable for digital I/O



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Analog input board, optically isolated, 4 differential inputs, 16-bit





PCI 32-bit







LabVIEW^{TN}



LabWindows/CVI™



Customer-tailored modifications designed to suit your needs. Hardware and software, firmware, PLDs, ... Contact us! With the fast analog input board APCI-3003 you can achieve high transfer rates with a simultaneous conversion of 4 channels.

The board has 4 differential inputs, each channel has its own A/D converter.

All 4 inputs are optically isolated from each other up to 1000 V.

Features

PCI 3.3 V or 5 VData acquisition independent from PCI clock

- Analog inputs
- 4 differential inputs
- 16-bit resolution
- Throughput: 400 kHz per input
- Simultaneous conversion of 4 channels
- Input voltage: 0-10 V, ± 10 V, 0-5 V, ± 5 V, 0-2 V, ± 2 V, 0-1 V, ± 1 V, freely programmable through software for each channel
- Current inputs: 0-20 mA (option) can be combined freely with voltage inputs
- Gain PGA x1, x2, x5, x10 freely programmable through software for each channel

Analog acquisition

- Different input modes for the analog acquisition:
 1) Simple mode
 - 2) Scan modes
- 3) Sequence modes
- 4) Auto Refresh mode
- Trigger functions:
- software trigger or
- external trigger: the analog acquisition (single or sequence) is started through the signal on digital input 0 from 0 V to 24 V
- Onboard FIFO (for 512 analog values)
- PCI-DMA

Digital

- 24 V digital I/O enable a high interference distance and a long distance between signal transmitter and data acquisition
- 4 digital inputs, 24 V, optically isolated
- 4 digital outputs, 24 V, optically isolated

APCI-3003

PCI 3.3 V or 5 V

Optical isolation between all channels

4 differential inputs, 16-bit resolution

Simultaneous acquisition on all channels

400 kHz throughput per channel

PCI DMA, programmable gain

Trigger functions

8 optically isolated digital I/O, 24 V

Timer

• 1, 12-bit

• Timer as cyclic time counter

Safety features

- · For more protection in noisy industrial environment
- Optical isolation 1000 V min.
- Creeping distance IEC 61010-1
- Overvoltage protection ± 40 V
- Protection against high-frequency EMI
- Input filters
- Noise neutralisation of the PC supply

Applications

- Industrial process control
- Industrial Measurement and monitoring
- Multichannel data acquisition
- Control of chemical processes
- Factory automation
- Acquisition of sensors
- Laboratory equipment
- Current measurement
 Instrumentation
- Instrumentatio

Software drivers

A CD-ROM with the following software and programming examples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP

• Real-time use with Linux and Windows on request Drivers and samples for the following compilers and

- software packages:
- .NET
- Microsoft VC++ Borland C++
 Visual Basic Delphi
- LabVIEW LabWindows/CVI
- ADDIPACK functions:

Analog input • Digital input • Digital output • Timer

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com, download menu



info@addi-data.com www.addi-data.com

Specifications

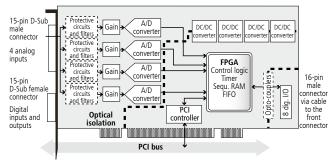
Number of inputs:	4 differential inputs
resolution:	16-bit
Optical isolation:	1000 V through opto-couplers from PC to peripheral
Input ranges:	Software-programmable for each channel
	0-10 V, ±10 V, 0-5 V, ± 5 V, 0-2 V, ± 2 V, 0-1 V, ± 1 V
	0-20 mA optional
Gain:	Software programmable (x1, x2, x5, x10)
Throughput:	400 kHz per input
Trigger:	Through software, timer, external event
	(24 V input)
Data transfer:	Data to the PC through FIFO memory,
	Interrupt at EOC (End Of Conversion),
	DMA transfer at EOC
Interrupts:	End of conversion, at timer overrun,
	End of scan
Digital I/O	
Number of I/O channels:	4 digital inputs, 24 V,
	4 digital outputs, 24 V,
	50 mA typ., Open Collector
Logical "0" level:	0-13 V
Logical "1" level:	16-30 V
Optical isolation:	1000 V through opto-couplers from PC to peripheral

EMC – Electromagnetic compatibility

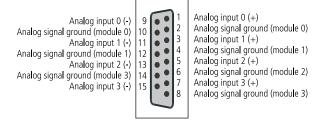
The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and enviro	nmental conditions		
Dimensions:	175 x 99 mm		
System bus:	PCI 32-bit 3.3/5V acc. to specification 2.2		
	(PCISiG)		
Space required:	1 PCI slot for analog inputs,		
	1 slot opening for digital I/O		
Operating voltage:	$+5$ V, ± 5 % from the PC		
Current consumption:	1.55 A typ.		
Front connector:	15-pin D-Sub male connector for analog inputs		
	15-pin female connector for digital I/O		
Temperature range:	0 to 60 °C (with forced cooling)		

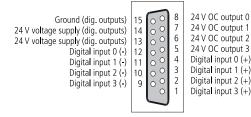
Simplified block diagram



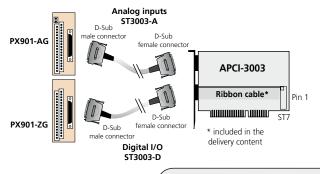
Pin assignment analog - 15-pin D-Sub male connector



Pin assignment digital - 15-pin D-Sub female connector



ADDI-DATA connection



Ordering information

APCI-3003

Analog input board, optically isolated, 4 differential inputs, 16-bit. Incl. technical description and software drivers

Versions APCI-3003:

Options

4 differential inputs, simultaneous acquisition, 8 digital inputs and outputs, 24 V

Please indicate the number of channels

Option PC-Diff: Current input for 1 differential channel 0(4)-20 mA Option DF: Precision filter for 1 channel

Accessories

PX901-AG:	Screw terminal panel with transorb diodes,	
	with housing for DIN rail	
	for connecting the analog inputs	
ST3003-A:	Shielded round cable, connection to PX-901-AG	
PX901-ZG:	Screw terminal panel for connecting	
	the digital I/O, for DIN rail	
ST3003-D:	Shielded round cable, connection to PX-901-ZG	



Analog input board, optically isolated, 16/8/4 SE or 8/4 differential inputs, 12-bit





Also for PCISS EXPRESS[®] see APCIe-3121, page 136

Compatible version for *CompactPCI™* See CPCI-3001, page 240







LabVIEW



DIA*dem**

DASYLab10

Features

Analog inputs

- 16 single-ended / 8 differential inputs or 8 single-ended / 4 differential inputs or 4 single-ended inputs
- 12-bit resolution
- Throughput: 100 kHz
- Input voltage: 0-10 V, ±10 V, 0-5 V, ±5 V, 0-2 V, ±2 V, 0-1 V, ±1 V, 0-20 mA (option), freely programmable through software for each channel
- Gain PGA x1, x2, x5, x10 freely programmable through software for each channel
- PCI-DMA for analog data acquisition
- Analog acquisition
- Single channel, several channels, several channels through scan list
- Automatic analog acquisition through cyclic timer control
- Acquisition through scan list: up to 16 entries with gain, channel, unipolar/bipolar
- Acquisition triggered through software, timer, external event
 - Trigger functions:
- Software trigger or
- External trigger: the analog acquisition (single or scan) is started through signal switching from 0 V to 24 V at the digital input 0.
- Interrupt: End of single channel, end of multichannel, end of scan list

Digital

- 4 digital inputs, 24 V, optically isolated
- 4 digital outputs, 24 V, optically isolated

Timer

• 24-bit, can be used as cyclic time counter

Safety features

- Optical isolation 500 V min.
- Creeping distance IEC 61010-1

Input filters: 159 kHz

Overvoltage protection ± 40 V
Protection against high-frequency EMI

• Noise neutralisation of the PC supply

DASILUDIO Data Acquisition System Laboratory

APCI-3001

16/8/4 single-ended or

8/4 differential inputs

12-bit resolution

Optical isolation 500 V

100 kHz throughput

PCI DMA, programmable gain

8 digital I/O, 24 V, optically isolated, timer

Trigger functions

Applications

- Industrial process control
- Industrial measurement and monitoring
- Multichannel data acquisition
- Control of chemical processes
- Factory automation
- Acquisition of sensors
- Laboratory equipment
- Current measurement
- Instrumentation

Software drivers

A CD-ROM with the following software and programming examples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

- Visual C++ Microsoft C
- Borland C++ Borland C
- Visual Basic Delphi
- LabVIEW DASYLab DIAdem

On request:

Further operating systems, compilers and samples.







Dig. I/O

16-pin

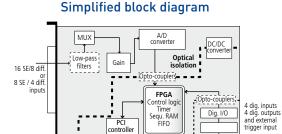
Specifications

Analog inputs	
Number of inputs:	16 single-ended/8 differential inputs 8 single-ended/4 differential inputs or 4 single-ended inputs
Resolution:	12-bit
Optical isolation:	500 V through opto-couplers from PC to peripheral
Input ranges:	Software-programmable for each channel 0-10 V, ±10 V, 0-5 V, ± 5 V, 0-2 V, ± 2 V, 0-1 V, ± 1 V 0-20 mA optional
Throughput:	100 kHz
Gain:	Software programmable (x1, x2, x5, x10)
Common mode rejection:	DC at 10 Hz, 90 dB minimum
Relative precision (INL):	± 1 LSB (ADC)
Diff. non-linearity (DNL):	± 0.5 LSB (ADC)
Input impedance (PGA):	$10^{12} \Omega$ // 10 nF single-ended, $10^{12} \Omega$ // 20 nF differential against GND
Bandwidth (- 3 dB):	Limited to 159 kHz with low-pass filter
Trigger:	Through software, timer, external event (24 V input)
Data transfer:	Data to the PC through FIFO memory, I/O commands, interrupt at EOC (End Of Conversion) and EOS (End of Scan), DMA transfer at EOC
Interrupts:	End of conversion, at timer overrun, End of scan
Timer	
Time base timer 2:	50 $\mu s;$ smallest programmable value: 100 μs
Digital I/O	
Number of I/O channels:	4 digital inputs, 4 digital outputs, 24 V
Optical isolation:	500 V through opto-couplers from PC to peripheral
Input range:	0-30 V - Logical "0": 0-5 V - Logical "1": 10-30 V
Input current at 24 V:	3 mA typ.
Output range:	5-30 V
Max. switching current:	10 mA typ.
Output type:	Open Collector

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and enviro	nmental conditions
Dimensions:	169 x 99 mm
System bus:	PCI 32-bit 3.3 / 5 V acc. to specification 2.1 (PCISiG)
Space required:	1 PCI slot for analog inputs,
	1 slot opening for digital I/O
Operating voltage:	$+5$ V, ± 5 % from the PC
Current consumption:	496 mA typ. ± 10 %
Front connector:	37-pin D-Sub male connector
Additional connector:	16-pin male connector for ribbon cable
	for connecting the digital inputs and outputs
Temperature range:	0 to 60 °C (with forced cooling)



Pin assignment – 37-pin D-Sub male connector

PCI bu

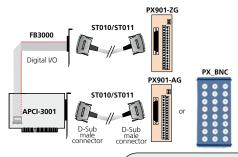
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5		- P			
DIFF	SE	\square		SE	DIFF
(+) An. input 0 (+) An. input 1 (+) An. input 2 (+) An. input 3 (-) An. input 3 (-) An. input 3 (-) An. input 2 (-) An. input 1 (-) An. input 0 1 Analog in Analog in		20 21 22 23 24 25 26 27 28 29 30 31 32 *	1 2 3 4 5 6 7 8 9 10 11 2 3	(+) An. input 8 (+) An. input 9 (+) An. input 10 (+) An. input 11 (+) An. input 13 (+) An. input 13 (+) An. input 13 (+) An. analog inp Analog inp	(-) An. input 6 (-) An. input 5 (-) An. input 4 put GND put GND
		33 34 35 36 37	14 15 16 17 18 19		
1 : The analog inputs have a common ground line					

Pin assignment – 16-pin male connector

Dig. output 0 (+)	1 🔳 🖿 2	Dig. output 0 (-)
Dig. output 1 (+)	3 🔳 🖬 4	Dig. output 1 (-)
Dig. output 2 (+)	5 🔳 🖬 6	Dig. output 2 (-)
Dig. output 3 (+)	7 🔳 🔳 8	Dig. output 3 (-)
Trigger/dig. input 0 (+)	9 🔳 🖬 10	Trigger/dig. input 0 (-)
Dig. input 1 (+)		
Dig. input 2 (+)	13∎∎14	Dig. input 2 (-)
Dig. input 3 (+)	15∎∎16	Dig. input 3 (-)

ADDI-DATA connection



Ordering information

APCI-3001

Analog input board, optically isolated, 16/8/4 SE or 8/4 diff. inputs, 12-bit. Incl. technical description and software drivers.

. .

Versions		Accessori	es
APCI-3001-16:	16 SE/8 diff. inputs, 8 dig. I/O	PX901-A:	Screw terminal panel with transorb diodes,
APCI-3001-8:	8 SE/4 diff. inputs, 8 dig. I/O		for connecting the analog inputs
APCI-3001-4:	4 SE inputs, 8 dig. I/O	PX901-AG:	Same as PX901-A with housing for DIN rail
Options	Please indicate the number of channels	PX_BNC:	BNC connection box for connecting the analog I/O
Option SF:	Precision filter for 1 single-ended channel	PX901-ZG:	Screw terminal panel for connecting
Option DF:	Precision filter for 1 differential channel		the digital I/O, for DIN rail
Option SC:	Current input for 1 single-ended channel 0(4)-20 mA	ST010:	Standard round cable, shielded, twisted pairs, 2 m
Option DC:	Current input for 1 diff. channel, 0(4)-20 mA	ST011:	Standard round cable, shielded, twisted pairs, 5 m
		FB3000:	Ribbon cable for digital I/O



Analog output board, optically isolated, 8 analog outputs, 14-bit





PCI 32-bit

Also for PCISS EXPRESS[®] see APCIe-3521, page 138









Features

- 8 or 4 analog outputs
- Optical isolation 500 V
 Setup time 30 µs typ.
- Jacup time so us typ.
 14-bit resolution (13-bit for 0-10 V)
- Output voltage: ±10 V, 0-10 V
- (switchable through software)
- Output voltage after reset: 0 V
- Each output has its own ground line
- (without optical isolation from each other)
 Driver capacity: 5 mA/500 pF
- Short-circuit protection, EMI filters
- Noise neutralisation of the PC supply
- Creeping distance IEC 61010-1
- Watchdog for resetting the analog outputs (4 different time bases: µs, ms, s, min) or as 12-bit timer (with interrupt possibility), when the watchdog function is not necessary.

Digital

- 2 digital inputs, 24 V, optically isolated
- 2 digital outputs, 24 V, optically isolated

Applications

- Industrial process control
- Industrial measurement and monitoring
- Control of chemical processes
- Factory automation
- Laboratory equipmentProgrammable voltage source
- Instrumentation
- ...

APCI-3501

8/4 analog outputs, 14-bit

Optical isolation 500 V

4 digital I/O, 24 V, optically isolated

Watchdog, timer

Software drivers

A CD-ROM with the following software and programming examples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request
- RTX drivers (real-time)

Drivers and samples for the following compilers and software packages:

- .NET
- Microsoft VC++ Borland C++
- Visual Basic Delphi
- LabVIEW
 LabWindows/CVI

ADDIPACK functions

- Analog output Digital input Digital output Timer
- Watchdog

On request:

Further operating systems, compilers and samples.



Specifications

Ana		-	
Ana	nn	OUT	niite
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Analog outputs	
Number of outputs:	8 or 4
Resolution:	14-bit resolution, 12-bit accuracy
Monotony:	12-bit
Optical isolation:	500 V through opto-couplers
Output range:	0-10 V, ±10 V switchable through software
Setup time at 2 k Ω , 1000 pF:	30 µs
Overvoltage protection:	±12 V
Max. output current/load:	±5 mA / 500 pF, 2 kΩ
Short-circuit current:	±25 mA
Output voltage after reset:	0 V
Watchdog:	software-programmable
	4 different time bases: μs, ms, s, min.

Digital I/O

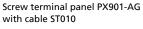
Number of I/O channels:	2 digital inputs, 2 digital outputs, 24 V			
Optical isolation:	500 V through opto-couplers from PC to peripheral			
Input current at 24 V:	3 mA typ.			
Input range:	0-30 V			
	- Logical "0": 0-5 V			
	- Logical "1": 10-30 V			
Max. switching current:	10 mA typ.			
Output range:	5-30 V			
Output type:	Open Collector			

EMC – Electromagnetic compatibility

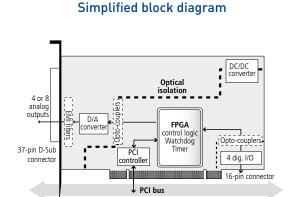
The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and environmental conditions

Dimensions:	175 x 99 mm
System bus:	PCI 32-bit 3.3/5 V acc. to specification 2.1 (PCISIG)
Space required:	1 PCI slot for analog outputs,
	1 slot opening for digital I/O with FB3000
Operating voltage:	$+5$ V, ± 5 % from the PC
Current consumption:	440 mA ± 10 % typ.
Front connector:	37-pin D-Sub male connector
Additional connector :	16-pin male connector for ribbon cable
	for connecting the digital inputs and outputs
Temperature range:	0 to 60 °C (with forced cooling)
lemperature range.	o to bo 'c (with forced cooling)



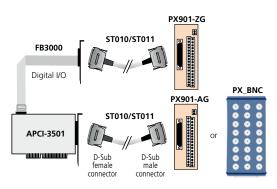




Pin assignment – 37-pin D-Sub male connector

		1	1		
	20	•	•	1	
	21		•	2	
	22		•		
	23	•	•	4	
	24	•	•	5	
	25	•	•	6	
	26	•	•	7	
	27	•	•	8	
	28	•	•	9	
	29	•	•	10	
An. output 0 GND	30	•	•	11	
An output 1 GND	31	•	•	12	An. output 0
An. output 2 GND	32	•	•	13	An. output 1
An. output 3 GND	33	•	•	14	An. output 2
An. output 4 GND	34	•	•	15	An. output 3
An. output 5 GND	35	•	•	16	An. output 4
An. output 6 GND	36	•	•	17	An. output 5
An output 7 GND	37	•	•	18	An. output 6
			•	19	An. output 7
			\sim		

ADDI-DATA connection



Ordering information

APCI-3501

Analog output board, optically isolated, 8/4 analog outputs, 14-bit. Incl. technical description and software drivers.

Versions A

A

APCI-3501-8	Version with 8 analog voltage outputs
APCI-3501-4	Version with 4 analog voltage outputs

Accessories

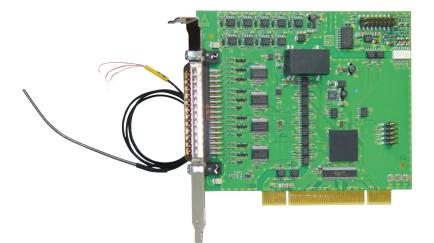
Accessories				
PX901-A: Screw terminal panel with transorb diodes,				
	for connecting the analog outputs			
PX901-AG:	Same as PX901-A with housing for DIN rail			
PX_BNC:	BNC connection box for connecting the analog I/O			

ST010: ST011:

Standard round cable, shielded, twisted pairs, 2 m Standard round cable, shielded, twisted pairs, 5 m PX901-ZG: Screw terminal panel for connecting the digital I/O, for DIN rail FB3000: Ribbon cable for digital I/O



Temperature measurement board, optically isolated, 16/8/4 channels for thermocouples, Pt100, RTD, 18-bit





PCI 32-bit













DIA*dem**

DASYLab10

Features

- PCI 3.3 V or 5 V
- 18-bit resolution, 16-bit accuracy
- Each channel can be configured either to thermocouples, RTD or as an analog voltage input channel
 - 16 analog inputs for thermocouple types
 J, K, T, E, R, S, B, N
 - or 8 diff. analog inputs for the acquisition of the resistance temperature detectors (Pt100)
 - or 16 SE/8 differential analog voltage
 - inputs, ± 1.25 V
- 8 independent current sources for resistance temperature detectors (RTD) and one current source for the cold junction compensation
- Cold junction compensation (on separate screw terminal board PX3200-G)
- Gain and offset calibration
- Linearisation through table and calculation for thermocouple types J, K, T, E, R, S, B, N and RTDs
- Programmable gain
- 16-bit accuracy with converter sample rate of 20, 40, 80 or 160 Hz (higher sample rate on request)
- 4 digital inputs, 24 V and 3 digital outputs, open collector, optically isolated
- Base address and IRQ channels set through BIOS
- The board is supplied with a monitoring program for testing and setting the board functions

Safety features

- Optical isolation 1000 V
- Creeping distance IEC 61010-1
 - Diagnostic: Short-circuits- and line break detection, depending on the type of sensor used
- Protection against overvoltage (±30 V) and high-frequency EMI

APCI-3200

Up to 16 channels for thermocouples

or 8 inputs for resistance temperature detectors (RTD)

Mixed configuration of the channels

18-bit resolution

Optical isolation 1000 V

Cold junction compensation on PX3200-G

Software linearisation

Software drivers

A CD-ROM with the following software and programming examples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

- .NET
- Microsoft VC++ Borland C++
- Visual Basic
 Delphi
- LabVIEW
 LabWindows/CVI
 DIAdem

ADDIPACK functions

- Analog input Temperature Resistance Digital input
- Digital output

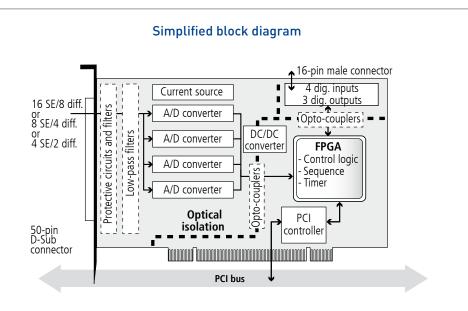
On request:

Further operating systems, compilers and samples.





DC



ADDI-DATA SPIRIT OF EXCELLENCE

Analog ir	nputs			
Analog inputs:		 16 x thermocouples or 8 x RTD with 2 or 4 wir 4 x RTD with 3 wire construction or 16 SE/8 diff. inputs, 3 	nnection	Dr
Resolution:		18-bit	± 2.5 V	
Accuracy:		16-bit		
Input amplifier Conversion sta		1, 2, 4, 8, 16, 32, 64, 128 Through software or exte		
Digital I/				
Number of I/O	channels:	4 digital inputs, 24 V, 3 digital outputs, 24 V, 125 mA typ., open collect	tor	
Logical "0" lev	el:	0-5 V		
Logical "1" lev Optical isolatio		12-30 V 1000 V through opto-cou and digital channels	plers for analo	g
Sampling	g frequenci	es		
Selectable Sampling frequ	iencies f _{adc}	f _{adc} = 160 Hz, 80 Hz, 40	0 Hz or 20 Hz	
Various sampli	ng rates F _s	in "Read 1" and in "Sc on the type of transduc		
	Selectable	Sampling		
Sensor	sampling frequencies f _{apc}	frequencies im "Read 1" Mode		frequencies an" Mode
	160 Hz	53 Hz / channel	32 Hz	
RTD	80 Hz	26 Hz / channel	16 Hz	for 2, 4, 6 and/or 8
(Pt100)	40 Hz	13 Hz / channel	8 Hz	channels
	20 Hz	6 Hz / channel	4 Hz	charmers
	160 Hz	26 Hz / channel	23 Hz	for 4, 8, 12
Thermo-	80 Hz	16 Hz / channel	11 Hz	and/or 16
couples	40 Hz	6 Hz / channel 3 Hz / channel	6 Hz	channels
	20 Hz	3 Hz / Channel	3 Hz	
Four cases a				
1. "Read 1" r F _s = ^f	node with RTD	With RTD (Pt100) 3 v at each measurement: - the measured value, - the offset, - the reference voltage F _s = 53 Hz, 26 Hz, 13 H	2.	Jired
2. "Read 1" r thermocou $F_{\rm s}= rac{f_{\rm s}}{-}$	uples (TC)	 With TC 2 x 3 values ar at each measurement: the measured value, the offset, the reference voltage One time for the acquis cold junction compense F_s = 26 Hz, 13 Hz, 6 Hz 	e. sition value an	d one time for the
3. "Scan" Mo $F_s = \frac{f_s}{f_s}$		With RTD (Pt100) 5 v acquired per scan meas 2 channels: for 2 values modules F _s = 32Hz, 16Hz, 8Hz, 4	surement to sa s for 1, 2, 3 an	mple
4. "Scan" Mo thermocor $F_s = \frac{f_s}{r_s}$	uples (TC)	With TC 7 values (bipol per scan measurement for 4 values for 1, 2, 3 a $F_s = 23$ Hz, 11 Hz, 6 Hz	to sample 4 ch and/or 4 modu	nannels:

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and environmental conditions				
Dimensions: 131 x 99 mm				
System bus:	PCI 32-bit 3.3/5 V acc. to spec. 2.2 (PCISiG)			
Space required:	1 PCI slot and			
	1 slot opening for the digital I/O			
Operating voltage:	+5 V, ±5 % from the PC			
Current consumption (typ.):	550 to 600 mA depending on the version			
Front connector (analog channels):50-pin D-Sub male connector				
Additional connector :	16-pin male connector for connecting the digital I/O			
via ribbon cable with 37-pin D-Sub connector				
Operating temperature:	0 to 60 °C (with forced cooling)			

Thermocouples accuracy

Type DIN EN 60584	Rang	e	Accuracy (+/-)
Type J	-200.0 °C	-0.1 °C	±0.6 °C
	0.0 °C	+599.9 °C	±0.2 °C
	+600.0 °C	+1200.0 °C	±0.6 °C
Type T	-200.0 °C	-80.0 °C	±0.7 °C
	-79.9 °C	+400.0 °C	±0.3 °C
Туре К	-200.0 °C	-0.1 °C	±0.8 °C
	0.0 °C	+999.9 °C	±0.4 °C
	+1000.0 °C	+1300.0 °C	±0.6 °C
Type E	-200.0 °C	+1000.0 °C	±0.5 °C
Type N	-200.0 °C	-0.1 °C	±1.0 °C
	0.0 °C	+799.9 °C	±0.2 °C
	+800.0 °C	+1300.0 °C	±0.5 °C
Type S	0.0 °C	+399.9 °C	±1.6 °C
	+400.0 °C	+1768.0 °C	±0.7 °C
Type R	0.0 °C	+399.9 °C	±1.6 °C
	+400.0 °C	+1768.0 °C	±0.6 °C
Туре В	+400.0 °C	+799.9 °C	±2.0 °C
	+800.0 °C	+1820.0 °C	±1.0 °C

Accuracy of the reference cold junction temperature

Range **Type** Pt1000 0° C+60° C

Accuracy (+/-) ± (0.30 °C + 0.0050 x [T]) (T: Temperature in °C)

Accuracy of the resistance thermometer (RTD)

Туре	Range		Accuracy (+/-)
DIN EN 60751			Worst Case (Gain=1 unipolar)
Pt100	-200.0 °C	+850.0 °C	±0.4 °C
Pt200	-200.0 °C	+850.0 °C	±0.4 °C
Pt500	-200.0 °C	+850.0 °C	±0.3 °C
Pt1000	-200.0 °C	+499.9 °C	±0.2 °C
	+500.0 °C	+850.0 °C	±1.0 °C
Ni100	-60.0 °C	+250.0 °C	±0.3 °C

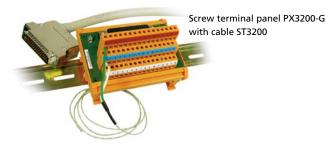
Accuracy in the temperature range of -20 °C to +40 °C with Pt100

Gain	Accuracy	
1	± 0.40 °C	
2	± 0.20 °C	
4	± 0.15 °C	
8	± 0.10 °C	
16	± 0.08 °C	
32	± 0.08 °C	
64	± 0.08 °C	

Sensor short-circuit / line break detection

Туре	short-circuits	line break
Thermocouple (SE)	no detection	no detection
Resistance	detection	detection
thermometer (diff.)		
Potentiometer (diff.)	detection	detection

Screw terminal panel with cold junction compensation PX3200-G



The PX3200-G screw terminal panel is used for connecting thermocouples/ RTDs. It is connected to the APCI-3200 through the ST3200 cable.

The housing of the female connector is connected to two ground terminals so that the board is additionally earthed for more security. All components of the board are enclosed in an earthing strip also connected to the ground terminals.

Each terminal is directly connected to one pin of the 50-pin D-Sub female connector. The designations on the terminals indicate the respective connections for the 50-pin D-Sub female connector.

The PX3200-G features an integrated cold junction compensation.

The voltage ($V_{cc}^{[1]}$) is measured through an RTD (Pt1000) at the cold junction and used as reference voltage for the temperature measurement of the thermocouples connected to the panel.

After each acquisition, a new measurement of the cold junction compensation is made for each channel and processed through software.

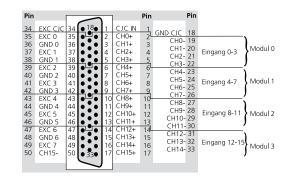
1 CJC: Cold Junction Compensation

Specifications

Possible connections

Versions	Number of thermocouples	Number of RTDs (diff. inputs)		
	(SE inputs)	2-wire connection	3-wire connection	4-wire connection
APCI-3200-4	4	2	1	2
APCI-3200-8	8	4	2	4
APCI-3200-16	16	8	4	8
Safety features: Connector: Dimensions of the Dimensions with I Temperature rang	50- board: (L > nousing: (L >	pund terminals pin D-Sub female (W x H) 110 x 70 (W x H) 113 x 87 0 °C	x 45 mm	

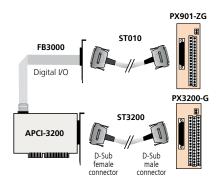
Pin assignment – 50-pin D-Sub male connector



Pin assignment – 16-pin male connector

24 V	1 2	GND
Dig. output 0 (+)	3 4	Dig. output 0 (-)
Dig. output 1 (+)	5 6	Dig. output 1 (-)
Dig. output 2 (+)	7 8	Dig. output 2 (-)
Dig. input 0 (+)	9 10	Dig. input 0 (-)
Dig. input 1 (+)	11 12	Dig. input 1 (-)
Dig. input 2 (+)	13 14	Dig. input 2 (-)
Dig. input 3 (+)	15 16	Dig. input 3 (-)

ADDI-DATA connection

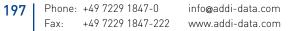


Ordering information

APCI-3200

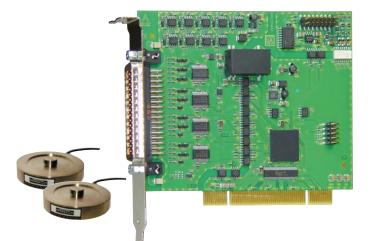
Temperature measurement board, optically isolated, 16/8/4 channels for thermocouples, Pt100, RTD, 18-bit. Incl. technical description, software drivers and monitoring program.

Versions			Accessories		
	APCI-3200-16:	16 analog inputs:	PX3200-G:	Screw terminal panel with cold junction compensation	
		16 thermocouples		and housing for DIN rail.	
		or 8 RTDs or 16 single-ended	PX3200:	Screw terminal panel with cold junction compensation and	
		or 8 diff. voltage inputs		4 mounting holes for wall mounting.	
	APCI-3200-8:	8 analog inputs: 8 thermocouples	ST3200:	Standard round cable, shielded, twisted pairs, 2 m	
		or 4 RTDs or 8 single-ended	FB3000:	Ribbon cable for digital I/O on separate bracket	
		or 4 diff. voltage inputs	PX901-ZG:	Screw terminal panel for connecting the digital I/O,	
	APCI-3200-4:	4 analog inputs: 4 thermocouples		for DIN rail	
		or 2 RTDs or 4 single-ended	ST010:	Standard round cable, shielded, twisted pairs, 2 m	
		or 2 diff. voltage inputs	ST011:	Standard round cable, shielded, twisted pairs, 5 m	





Pressure measurement board, optically isolated, up to 8 channels for strain gauges, 18-bit





PCI 32-bit







LabVIEW™



* On request

Features

• PCI 3.3 V or 5 V

Analog inputs

- 18-bit resolution, unipolar, 16-bit accuracy
- 8 or 4 differential inputs for strain gauges
- Voltage range from 0 to + 1.25 V
- 4 or 8 voltage sources for the connected pressure sensors
- Output voltage for the voltage sources 5 V, 30 mA
- Gain and offset calibration
- Calculation of the pressure value through software
- Programmable gain
- 16-bit accuracy with a sample rate of 20, 40, 80 or 160 Hz

Analog acquisition

- Acquisition triggered through software, timer, external event
- Trigger functions:
 Software trigger or
 - External trigger: the analog acquisition (single or sequence) is started through signal switching
 - from 0 V to 24 V at the digital input 0.
- Connection of linear sensors (Wheatstone Bridge)

Digital

 4 digital inputs, 24 V and 3 digital outputs, open collector, optically isolated

Safety features

- Optical isolation 1000 V
- Creeping distance IEC 61010-1
- Protection against overvoltage (±30 V) and high-frequency EMI

APCI-3300

Up to 8 channels for strain gauges	
Up to 8 onboard voltage sources	
18-bit resolution	
Optical isolation 1000 V	
Software linearisation	

Direct connection of the pressure sensors to

the screw terminal panel PX3200-G

Software

A CD-ROM with the following software and programming examples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

- .NET
- Microsoft VC++ Borland C++
- Visual Basic Delphi
- LabVIEW

ADDIPACK functions

Pressure • Digital input • Digital output

On request:

Further operating systems, compilers and samples.



Specifications

Number of inputs: 8 or 4 for stra	unipolar analog inputs
for stra	analog inputs
	indiog inputs
	in gauges,
one vo	tage source per channel
Input type: Differe	ntial channels
Optical isolation: 1000 V	through opto-couplers from PC to peripheral
Accuracy: 16-bit	
Overvoltage protection: ± 30 V	
Input voltage range: 0 to 1.2	25 V / PGA
Input amplifier (PGA): 1, 2, 4,	8, 16, 32, 64, 128
Conversion start: Throug	h software or
externa	l trigger, with or without timer
Voltage sources: 4 or 8	
Output voltage for the	
voltage sources: 5 V, 30	

Digital I/O

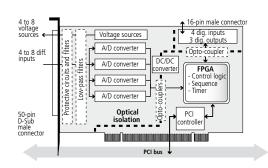
Number of I/O channels:	4 digital inputs, 24 V, 3 digital outputs, 24 V, 125 mA typ., open collector
Logical "0" level:	0-5 V
Logical "1" level:	12-30 V
Input current at 24 V:	2 mA
Max. switching current	
of the outputs:	125 mA
Optical isolation:	1000 V through opto-couplers
	for analog and digital channels

EMC – Electromagnetic compatibility

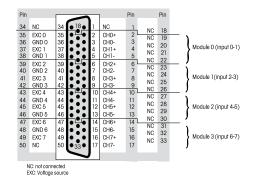
The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and environmental conditions				
Dimensions:	131 x 99 mm			
System bus:	PCI 32-bit 3.3 / 5 V acc. to spec. 2.2 (PCISiG)			
Space required:	1 PCI slot and			
	1 slot opening for			
	the digital I/O			
Operating voltage:	+5 V, ±5 % from the PC, +3.3 V			
Current consumption (typ.):	570 to 600 mA depending on the version			
Front connector				
(analog channels):	50-pin D-Sub male connector			
Additional connector:	16-pin male connector for connecting			
	of the digital I/O via ribbon cable			
	with 37-pin D-Sub connector			
Operating temperature:	0 to 60 °C (with forced cooling)			

Simplified block diagram



Pin assignment – 50-pin D-Sub male connector



Pin assignment – 16-pin male connector

24 V	1 • 2	GND
Dig. output 0 (+)	3 • 4	Dig. output 0 (-)
Dig. output 1 (+)	5 • 6	Dig. output 1 (-)
Dig. output 2 (+)	7 • 8	Dig. output 2 (-)
Dig. input 0 (+)	9 • 10	Dig. input 0 (-)
Dig. input 1 (+)	11 • 12	Dig. input 1 (-)
Dig. input 2 (+)	13 • 14	Dig. input 2 (-)
Dig. input 3 (+)	15 • 16	Dig. input 3 (-)

ADDI-DATA connection PX901-ZG FB3000 Digital I/O Digital I/O APCI-3300 D-Sub Connector D-Sub Connector

Ordering information

APCI-3300

Pressure measurement board, optically isolated, up to 8 channels for strain gauges, 18-bit. Incl. technical description and software drivers.

Versions	
	2

APCI-3300-4: APCI-3300-8:		alog i alog i		•		-	
Accessories	_						

PX3200-G:Screw terminal panel with housing for DIN rail**PX3200:**Screw terminal panel with 4 mounting holes

ST3200: FB3000: PX901-ZG: ST010: ST011:

Standard round cable, shielded, twisted pairs, 2 m
Ribbon cable for dig. I/O on separate bracket
ZG: Screw terminal panel for digital I/O for DIN rail
Standard round cable, shielded, twisted pairs, 2 m
Standard round cable, shielded, twisted pairs, 5 m



Noise and vibration measurement board, optically isolated, multifunction board, 8 analog inputs, 24-bit



Acoustic processes in test applications are not limited to simple noise and vibration measurements. The multifunction PCI board APCI-3600 by ADDI-DATA offers a PC-based solution to almost all additional measuring tasks which may arise thanks to its many functions.

- 8 analog input channels through SMB co-axial connectors
- Counter function: 4 chronometer inputs (up to 1 MHz 32-bit depth) allow applications in which precise coordinates must be determined.
- Current supply of the ICP[™] sensors
- Synchronous mode (cascading) of several APCI-3600 through Master/Trigger
- Digital I/O
- SDRAM memory module allows transfer rates up to 24 MBvte/s.

For a fast integration of the board in special test devices, the board is supplied with drivers and samples.

Features

Analog inputs (for all versions)

- 8 SE or diff. (+/-) inputs
- Sampling rate can be set between 2 and 200 $\rm kHz$
- SNR (signal/noise ration) > 105 dB
- 24-bit resolution
- One A/D converter per channel: simultaneous acquisition on all analog inputs
- Gain 1 to 10, software-programmable
- Input coupling AC, DC, GND, software-programmable for each channel
- Antialiasing filter to avoid sampling errors
- Overvoltage protection

Current sources

- 8 current sources for the direct connection of ICP[™] sensors (integrated circuit piezoelectric)
- 4 mA typ., 24 V max.

info@addi-data.com

www.addi-data.com

Chronometer inputs (only for version APCI-3600)

- 4 chronometer inputs, RS485, 32-bit for revolution counting
- 2 gate inputs

APCI-3600, APCI-3600-L

8 SE/diff. (+/-) inputs,

simultaneous sampling

Connection through SMB co-axial connectors

Onboard power supply for ICP[™] sensors

4 chronometer inputs (RS485)

2 analog outputs

8 digital inputs, 8 digital outputs

Onboard SDRAM module

Analog outputs (only for version APCI-3600)

- 2 analog outputs: both outputs are started synchronously with the A/D converter. Arbitrary function generators can be programmed.
- Settling time: 5 µs
- 16-bit resolution
- Simultaneous sampling on both channels
- 13-bit accuracy
- DAC type: R-2R
- Output range: ± 10 V

Digital (only for version APCI-3600)

- 8 digital inputs, 24 V, optically isolated
- 8 digital outputs, 24 V, optically isolated

Onboard SDRAM module

• 128 MB (256 MB or 512 MB on request)

Applications

The following applications can be realised with the APCI-3600:

Noise measurement with fault diagnosis on gear and drive over FFT:

Encoders are connected to the chronometer inputs and microphones are connected to the analog inputs. Encoders measure the position of the drive and the analog inputs measure the noise of the system at a specific position. For this purpose the analog inputs and the chronometer inputs are controlled synchronously. To each analog sample belongs a position of the chronometer. The synchronisation results from a FFT.

- Measurement of the transfer function of a DUT ("Device Under Test").
- Noise analysis: Evaluation of a washing maschine, measurements in the automotive field, etc.

Software

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Samples for the following compilers: Visual C++ • Borland C

Driver download: www.addi-data.com. download menu



Phone: +49 7229 1847-0 200 +49 7229 1847-222 Fax:

PCI 32-bit

Windows 64/32-bit drivers



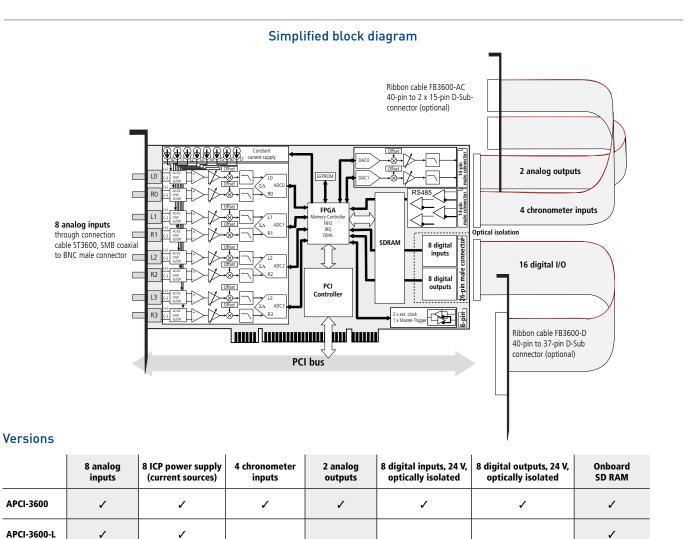
designed to suit vour needs. Hardware and software, firmware, PLDs, ... Contact us!

* Preliminary

product information



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Sp	eci	fic	ati	ons	*

Number:	8		
Input type:	Single-ended or d	lifferential through	software
resolution:	24-bit		
A/D Converter:	Delta-Sigma, 5th	order, multibit	
	Delta-Sigma mod		
Gain:	x1, x10 software		
Input ranges:	Gain x1		/ single-ended
	Gain x1		differential
	Gain x10		single-ended
	Gain x10		V differential
Sampling rate f _s :		Hz selectable thro	
Selectable frequencies:	2 kHz ≤ f₅	50 kHz \leq f _s	$100 \text{ kHz} \le f_S$
	≤50 kHz	\leq 100 kHz	\leq 200 kHz
	50000 Hz	100000 Hz	200000 Hz
	40000 Hz	80000 Hz	160000 Hz
	33333 Hz	66667 Hz	133333 Hz
	25000 Hz	50000 Hz	100000 Hz
	20000 Hz		
	16667 Hz		
	12500 Hz		
	10000 Hz		
	8000 Hz		
	5000 Hz		
	4000 Hz		
	3333 Hz		
	2500 Hz		
	2000 Hz		

Oversampling:	64 x f_s (for sampling rate f_s)		
Frequency precision:	± 50 ppm		
FIFO depth:	128 DWORD, for the rigth and the left channel		
	of the same ADC		
Data transfer:	DMA, I/O, IRQ		
Transmission ripple (rel. to 1 kł	Iz), max., DC-coupled:		
$2 \text{ kHz} \le f_S \le 50 \text{ kHz}$:	-0.1dB, DC to 0.47 x f _S		
50 kHz $\leq f_{S} \leq$ 100 kHz:	-0.1dB, DC to 0.45 x f _s		
100 kHz $\leq f_{S} \leq$ 200 kHz:	-0.1dB, DC to 0.24 x f _s		
-3 dB bandwidth:			
$2 \text{ kHz} \le f_{S} \le 50 \text{ kHz}$:	0.5 x f _s		
50 kHz $\leq f_{S} \leq$ 100 kHz:	0.5 x f _s		
$100 \text{ kHz} \le f_{S} \le 200 \text{ kHz}$:	0.358 x f _s		
Input coupling:	AC, DC, GND, selectable through software		
AC -3dB limit frequency:	1.6 Hz		
Overvoltage protection:			
R1-, L1-, R2-, L2-, L/R3+-, L/			
Max. direct current:	± 12 V, ± 200 mA		
at 1 ms, 10% duty cycle):	± 12 V, ± 300 mA		
at 1 ms, 10% duty cycle): R1+, L1+, R2+, L2+			
at 1 ms, 10% duty cycle): R1+, L1+, R2+, L2+ Max. direct current:	± 12 V, ± 300 mA ± 36 V, ± 30 mA		
Max. peak current (Impuls at 1 ms, 10% duty cycle): R1+, L1+, R2+, L2+ Max. direct current: Max. peak current (pulse	± 36 V, ± 30 mA		
at 1 ms, 10% duty cycle): R1+, L1+, R2+, L2+ Max. direct current:			

Fax:

Analog inputs (continued)

Dynamic properties

2 kHz \leq f_S \leq 50 kHz: Passband: DC (0Hz) up to 0.47 x f_s , min. to max. 0.58 x f_s min -95 dB min Stopband: Stopband attenuation: Total group delay: 12/fs s typical

 $\begin{array}{l} \textbf{50 kHz} \leq \textbf{f}_{\textbf{S}} \leq \textbf{100 kHz:} \\ \textbf{Passband:} \\ \textbf{Stopband:} \end{array}$ Stopband attenuation: Total group delay:

DC (0Hz) up to 0.45 x f_{S} , min. to max. 0,68 x f_{S} min -92 dB min 9/fs s typical

DC (0Hz) up to 0.24 x f_{S} , min. to max. 0.78 x f_{S} min -97 dB min

100 kHz ≤ **f**_S ≤ **200 kHz:** Passband: Stopband: Stopband attenuation: Total group delay:

Dynamic range SNR

2 kHz	$\leq f_{S} \leq$	50 kHz:	< -105 dB (short input gain x1) < -100 dB (short input gain x10)
			< -80 dB (open input gain x1) < -60 dB (open input gain x10)
50 kHz	$\leq f_{S} \leq$	100 kHz:	< -105 dB (short input gain x10) < -105 dB (short input gain x1)
			< -100 dB (short input gain x10)
			< -80 dB (open input gain x1)
			< -60 dB (open input gain x10)
100 kHz	≤ f _s ≤	200 kHz:	< -75 dB (short input gain x1)
			< -75 dB (short input gain x10)
			< -75 dB (open input gain x1)
			< -60 dB (open input gain x10)

5/f_S s typical

Crosstalk

Between channel R0 and L0, R1 and L1, R2 and L2, R3 and L3, Gain x1:

Short input at f _{in} = 100 Hz					
2 kHz	[¯] ≤f _S ≤ [¨]	50 kHz:	< -95 dB		
50 kHz	$\leq f_{S} \leq$	100 kHz:	< -95 dB		
100 kHz	$\leq f_{S} \leq$	200 kHz:	< -70 dB		
Short in	put at f	, = 1 kHz			
		50 kHz:	< -95 dB		
50 kHz	$\leq f_{S} \leq$	100 kHz:	< -95 dB		
100 kHz	$\leq f_{S} \leq$	200 kHz:	< -70 dB		
1 kΩ loa	d at f in =	= 100 Hz			
2 kHz	≤ f _s ≤	50 kHz:	< -95 dB		
		100 kHz:	< -95 dB		
100 kHz	≤ f _s ≤	200 kHz:	< -70 dB		
1 kΩ loa	d at f _{in} =	= 1 kHz			
		50 kHz:	< -95 dB		
50 kHz	$\leq f_{S} \leq$	100 kHz:	< -95 dB		
100 kHz	≤fc≤	200 kHz:	< -70 dB		

Phase error

Amplitude error	0.02° at $f_{in}^{m} = 1$ kHz sinus signal ± 0.02 dB max., at $f_{in} = 1$ kHz sinus signal (Gain x1 and x10)
2	0.2° at $f_{in} = 10$ kHz sinus signal
At $f_s = 200 \text{ kHz}$	0.3° max.
between channel R0 and L0, R	1 and L1, R2 and L2, R3 and L3

Offset error

Analog outputs		
Number of outputs:	2	
Resolution / accuracy:	16-bit / 13-bit	
DAC type:	R-2R	
Output range:	± 10 V	
Settling time: 10 V step, RL = 2 k	, CL = 1500 pF	
	± 0.1%:	5 µs typical
	± 0.01%:	5.6 µs typical
Overvoltage protection:	± 12 V, 100 mA ma	ax. direct current
Short-circuit current:	± 45 mA typical	
Output voltage after reset:	0 V	
FIFO depth:	256 Word	
Data transfer:	DMA, I/O, IRQ	

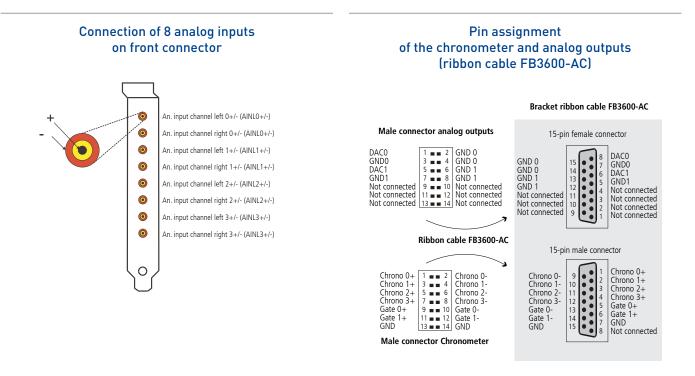
 \pm 200 $\mu\text{V}\text{,}$ max. at f_S = 2 kHz

* Preliminary product information

	8
Number of inputs: Filters/protective circu	-
Optical isolation:	1000 V
Nominal voltage:	24 V external
Input voltage:	0 up to 30 V
Input current:	7 mA at 24 VDC, typical
Logic input levels:	UH (max.): 30 V
5 1	UH (min): 19 V
	UL (max.): 14 V
	UL (min): 0V
Input frequency (max	.): 5 kHz at 24 V
Trigger input:	Digital input 0
Digital outpu	its
Number outputs:	8, open collector
Optical isolation:	1000 V
Nominal voltage:	24 V
Supply voltage:	5-30 V
Output current per ou	
Total current:	300 mA limited through PTC
Switch-on time:	0.25 µs typical
Switch-off time:	0.25 µs typical
Current sour	7085
Number:	
Numper:	8 constant current sources for the power supply of the ICP™ sensors, 4 mA typical, 24 V max.
Chronomete	r
Number:	4 x chronometer, 2 x gate on chronos 1+2
Input type:	RS485
Max. speed:	1 MHz max.
Counting depth:	32-bit
Divisor:	From 2° to 215 per chronometer
FIFO depth:	256 DWORD
	DMA, I/O, IRQ
Data transfer:	
Data transfer: Differential threshold	voltage: -200 mV min -50 mV max.
Differential threshold Input resistance:	voltage: -200 mV min -50 mV max. 120 differential
Differential threshold Input resistance: ESD protection:	120 differential ±15 kV Human Body Model
Differential threshold Input resistance: ESD protection: EMC – Electr	120 differential ±15 kV Human Body Model
Differential threshold Input resistance: ESD protection: EMC – Electr The product complies certified EMC laborat The limit values as se	120 differential ±15 kV Human Body Model comagnetic compatibility with the European EMC directive. The tests were carried out by a ory in accordance with the norm from the EN 61326 series (IEC 61326
Differential threshold Input resistance: ESD protection: EMC – Electr The product complies certified EMC laborat The limit values as se complied with. The re	120 differential ±15 kV Human Body Model romagnetic compatibility with the European EMC directive. The tests were carried out by a ory in accordance with the norm from the EN 61326 series (IEC 61326 t out by the European EMC directive for an industrial environment are
Differential threshold Input resistance: ESD protection: EMC – Electr The product complies certified EMC laborat The limit values as se complied with. The re	120 differential ±15 kV Human Body Model comagnetic compatibility with the European EMC directive. The tests were carried out by a ory in accordance with the norm from the EN 61326 series (IEC 61326 t out by the European EMC directive for an industrial environment are espective EMC test report is available on request.
Differential threshold Input resistance: ESD protection: EMC – Electr The product complies certified EMC laborat The limit values as se complied with. The re Physical and	120 differential ±15 kV Human Body Model romagnetic compatibility with the European EMC directive. The tests were carried out by a ory in accordance with the norm from the EN 61326 series (IEC 61326 t out by the European EMC directive for an industrial environment are spective EMC test report is available on request. environmental conditions 175 x 99 mm PCI 32-bit 3.3/5 V acc. to spec. 2.2 (PCISiG)
Differential threshold Input resistance: ESD protection: EMC – Electr The product complies certified EMC laborat The limit values as se complied with. The re Physical and Dimensions:	120 differential ±15 kV Human Body Model romagnetic compatibility with the European EMC directive. The tests were carried out by a ory in accordance with the norm from the EN 61326 series (IEC 61326 t out by the European EMC directive for an industrial environment are spective EMC test report is available on request. environmental conditions 175 x 99 mm PCI 32-bit 3.3/5 V acc. to spec. 2.2 (PCISIG) 1 PCI slot for the analog inputs
Differential threshold Input resistance: ESD protection: EMC – Electr The product complies certified EMC laborat The limit values as se complied with. The re Physical and Dimensions: System bus:	120 differential ±15 kV Human Body Model romagnetic compatibility with the European EMC directive. The tests were carried out by a ory in accordance with the norm from the EN 61326 series (IEC 61326 to ut by the European EMC directive for an industrial environment are spective EMC test report is available on request. environmental conditions 175 x 99 mm PCI 32-bit 3.3/5 V acc. to spec. 2.2 (PCISIG) 1 PCI slot for the analog inputs 1 slot opening for digital inputs and outputs
Differential threshold Input resistance: ESD protection: EMC – Electr The product complies certified EMC laborat The limit values as se complied with. The re Physical and Dimensions: System bus:	120 differential ±15 kV Human Body Model romagnetic compatibility with the European EMC directive. The tests were carried out by a ory in accordance with the norm from the EN 61326 series (IEC 61326 to ut by the European EMC directive for an industrial environment are spective EMC test report is available on request. environmental conditions 175 x 99 mm PCI 32-bit 3.375 V acc. to spec. 2.2 (PCISIG) 1 PCI slot for the analog inputs 1 slot opening for digital inputs and outputs 1 slot opening for chronometer and
Differential threshold Input resistance: ESD protection: EMC – Electr The product complies certified EMC laborat The limit values as se complied with. The re Physical and Dimensions: System bus: Space required:	120 differential ±15 kV Human Body Model romagnetic compatibility with the European EMC directive. The tests were carried out by a ory in accordance with the norm from the EN 61326 series (IEC 61326 tout by the European EMC directive for an industrial environment are espective EMC test report is available on request. environmental conditions 175 x 99 mm PCI 32-bit 3.3/5 V acc. to spec. 2.2 (PCISiG) 1 PCI slot for the analog inputs 1 slot opening for digital inputs and outputs 1 slot opening for chronometer and analog outputs
Differential threshold Input resistance: ESD protection: EMC – Electr The product complies certified EMC laborat The limit values as se complied with. The re Physical and Dimensions: System bus: Space required: Operating voltage:	120 differential ±15 kV Human Body Model romagnetic compatibility with the European EMC directive. The tests were carried out by a ory in accordance with the norm from the EN 61326 series (IEC 61326 tout by the European EMC directive for an industrial environment are spective EMC test report is available on request. environmental conditions 175 x 99 mm PCI 32-bit 3.3/5 V acc. to spec. 2.2 (PCISiG) 1 PCI slot for the analog inputs 1 slot opening for chronometer and analog outputs +5 V, ±5 % from the PC
Differential threshold Input resistance: ESD protection: EMC – Electr The product complies certified EMC laborat The limit values as se complied with. The re Physical and Dimensions: System bus: Space required: Operating voltage: Front connector:	120 differential ±15 kV Human Body Model romagnetic compatibility with the European EMC directive. The tests were carried out by a ory in accordance with the norm from the EN 61326 series (IEC 61326 t out by the European EMC directive for an industrial environment are spective EMC test report is available on request. environmental conditions 175 x 99 mm PCI 32-bit 3.3/5 V acc. to spec. 2.2 (PCISIG) 1 PCI slot for the analog inputs 1 slot opening for chigital inputs and outputs 1 slot opening for chronometer and analog outputs +5 V, ±5 % from the PC 8 SMB co-axial connector on bracket
Differential threshold Input resistance: ESD protection: EMC – Electr The product complies certified EMC laborat The limit values as se complied with. The re Physical and Dimensions: System bus: Space required: Operating voltage:	120 differential ±15 kV Human Body Model romagnetic compatibility with the European EMC directive. The tests were carried out by a ory in accordance with the norm from the EN 61326 series (IEC 61326 to ut by the European EMC directive for an industrial environment are spective EMC test report is available on request. environmental conditions 175 x 99 mm PCI 32-bit 3.3/5 V acc. to spec. 2.2 (PCISiG) 1 PCI slot for the analog inputs 1 slot opening for digital inputs and outputs 1 slot opening for chronometer and analog outputs +5 V, ±5 % from the PC 8 SMB co-axial connector on bracket • 37-pin D-Sub connector for digital I/O
Differential threshold Input resistance: ESD protection: EMC – Electr The product complies certified EMC laborat The limit values as se complied with. The re Physical and Dimensions: System bus: Space required: Operating voltage: Front connector:	120 differential ±15 kV Human Body Model romagnetic compatibility with the European EMC directive. The tests were carried out by a ory in accordance with the norm from the EN 61326 series (IEC 61326 to ut by the European EMC directive for an industrial environment are espective EMC test report is available on request. environmental conditions 175 x 99 mm PCI 32-bit 3.3/5 V acc. to spec. 2.2 (PCISiG) 1 PCI slot for the analog inputs 1 slot opening for digital inputs and outputs 1 slot opening for chronometer and analog outputs +5 V, ±5 % from the PC 8 SMB co-axial connector on bracket : 37-pin D-Sub connector for digital I/O : 15-pin D-Sub connector for digital I/O
Differential threshold Input resistance: ESD protection: EMC – Electr The product complies certified EMC laborat The limit values as se complied with. The re Physical and Dimensions: System bus: Space required: Operating voltage: Front connector:	120 differential ±15 kV Human Body Model romagnetic compatibility with the European EMC directive. The tests were carried out by a ory in accordance with the norm from the EN 61326 series (IEC 61326 to ut by the European EMC directive for an industrial environment are spective EMC test report is available on request. environmental conditions 175 x 99 mm PCI 32-bit 3.3/5 V acc. to spec. 2.2 (PCISiG) 1 PCI slot for the analog inputs 1 slot opening for digital inputs and outputs 1 slot opening for chronometer and analog outputs +5 V, ±5 % from the PC 8 SMB co-axial connector on bracket • 37-pin D-Sub connector for digital I/O

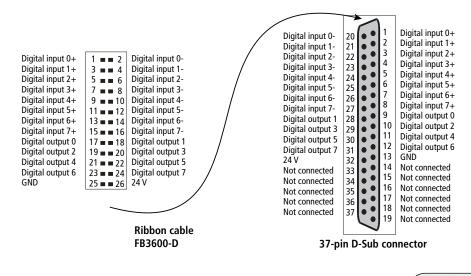






Pin assignment of the digital inputs and outputs

26-pin male connector on separate 37-pin D-Sub-male connector (ribbon cable FB3600-D)



Ordering information

APCI-3600, APCI-3600-L

Noise and vibration measurement board, optically isolated, 24-bit, multifunction board, 8 analog inputs, 8 current sources..., antialiasing filter. Incl. technical description and software drivers.

Versions		Accessori	es
APCI-3600:	8 analog inputs,	ST3601:	Connection cable, 2 m
	8 current sources for connecting ICP [™] sensors,		SMB co-axial female connector on
	2 analog outputs, 4 chronometer inputs,		BNC male connector
	8 digital inputs, 8 digital outputs,	ST3600:	Connection cable, 2 m (ST3600 = 8 x ST3601)
	128 MBytes SDRAM	FB3600-D:	Ribbon cable for connecting the digital I/O
APCI-3600-L:	8 analog inputs,		on separate bracket, 30 cm
	8 current sources for connecting ICP™ sensors,	FB3600-AC	Ribbon cable for connecting the chronometer
	128 MBytes SDRAM		and analog outputs on separate bracket,
			30 cm

* Preliminary product information



Length measurement board, 16-bit, simultaneous acquisition of 5 inductive transducers, LVDT, half-bridge





PCI 32-bit







The PCI length measurement board APCI-3702 is designed for the simultaneous acquisition of 5 half-bridge or LVDT transducers.

It operates with a 16-bit resolution.

It is suited for dynamic measurement, e.g. for measuring moving parts or applications with time-critical measurement cycles – especially in test equipment with several sensors.

The calibration tool SET3701 includes a data base with pre-calibrated transducers. It guides you through each step of the installation beginning with the selection of a transducer up to testing the channels.

Features

- PCI interface to the 32-bit data bus, 3.3 V or 5 V
- Acquisition of 5 inductive transducers
- (half-bridge, LVDT)
- 16-bit resolution
- Sampling rate depending on the transducer: 2-20 kHz
- Example for TESA transducers GT21:
 13.951 kHz per channel,
 0.072 ms for one sequence of up to 5 channels
- Measuring frequency through software programmable: 2-20 kHz
- Conversion triggered through software, digital input or timer
- End of conversion through software and/or interrupt
- PCI-DMA access
 - Onboard FIFO
 - Sequence RAM
 - 16 digital inputs and outputs, optically isolated, 24 V
 - Connection of the transducer through external box PX3701-8. The box type depends on the transducers used.
 - Please order separately.
 - Software operation
 - Automatic setting of the input levels (gain and offset) according to the transducer sensitivity
 - Tool for individual database-managed calibration of the transducers
 - Database for connecting/calibrating a large range of industry-standard transducers:
 - Solartron Tesa Marposs Schlumberger
 - Peter & Hirt Mahr RDP Schaevitz
 - SMPR Controle

Further transducers like for example Horst Knäbel can be calibrated on request.

APCI-3702

Simultaneous acquisition of 5 inductive

transducers

Half-bridge, LVDT

16-bit resolution

16 digital inputs and outputs, optically isolated

Safety features

- Input filters
- Diagnostic function in case of short-circuits or line break

Applications

- Gear wheel control
- Gauge block
- Acquisition of sensor data
- Quality control
- Industrial process control
- Automatic parts control
- R&D instrumentation

Software

Calibration tool SET3701 (supplied with the board)

- Easy transducer calibration
- Step by step from the transducer selection up to testing each single channel.
- Database with more than 50 pre-calibrated transducers
- Update of the APCI-3702 firmware

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP

• Real-time use with Linux and Windows on request Drivers for the following compilers and software packages:

- .NET
- Microsoft VC++ Borland C++ Visual Basic Delphi
- LabVIEW
 LabWindows/CVI

ADDIPACK functions:

Transducer • Timer • Digital input • Digital output On request:

Further operating systems, compilers and samples.





Specifications

Inputs for inductive transdu	ucers
Number	5 (simultaneous)
Input type	Single ended
Coupling	DC
Resolution / Accuracy:	16-bit / 13-bit
Sampling rate f_s on 5 channels	Depending on the transducer
selectable per software:	4.883 kHz (typ.)
	6.975 kHz (typ.)
	9.768 kHz (typ.)
	13.951 kHz (typ.)
	19.531 kHz (typ.)
Example with TESA GT21	13.951kHz (on 5 channels)
Input level	
Input impedance	2 kΩ software-programmable
	10 kΩ
	100 kΩ
	10 MΩ
Sensor supply (sinus genera	ator)
Туре	Sinus differential (180° phase-shift)
Number of outputs:	2
Coupling	AC
Programmed signals:	
output frequency $f_{ extsf{P}}$	2-20 kHz depending on the transducer
(primary frequency)	(50 kHz Knäbel)
Output level	
Output impedance	< 0.1 Ω typ.
	$>$ 30 k Ω typ. in shutdown mode
Short-circuit current	0.7 A typ. at 25°C with thermal protection

Digital I/O

Number of I/O channels:	8 dig. inputs, 8 dig. outputs, 24 V
Optical isolation:	1000 V through opto-couplers
Input current at 24 V:	11 mA typ.
Max. input frequency:	5 kHz (inputs 1 to 7)
Max. switching current at 24 V:	50 mA typ.
Input voltage:	0-30 V
Output voltage:	5-30 V

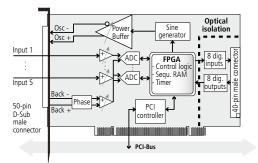
EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and environmental conditions

Dimensions:	109 x 138 mm
System bus:	PCI 32-bit 3.3/5 V acc. to spec. 2.2 (PCISiG)
Space required:	1 PCI slot for analog inputs,
	1 slot opening for digital I/O with FB3702
Operating voltage:	$+5 V$, $\pm 5 \%$ from the PC; 24 V external
Current consumption	990 mA typ. without load
(+ 5 V from the PC):	
Front connector:	50-pin D-Sub male connector
Additional connector:	16-pin male connector for connecting the dig. I/O
Temperature range:	0 to 60 °C (with forced cooling)

Simplified block diagram

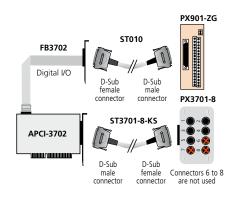


Pin assignment 50-pin D-Sub male connector

Pin		Pin				_			Pin
34 35 36 37 38 40 41 42 43 44 45 46 47 48 49 50	BACK+ BACK- OSC+ OSC- PWRGND CH0 PWRGND NC PWRGND NC PWRGND NC PWRGND NC	18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33	BACK+ BACK- OSC+ OSC+ OSC- PWRGND CH3 PWRGND NC PWRGND NC PWRGND NC	34 35 36 37 38 40 41 42 43 44 45 46 47 48 49 50	11		1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17	BACK+ BACK- OSC+ OSC- PWRGND CH1 PWRGND CH4 PWRGND NC PWRGND NC PWRGND	1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17

Osc+/-: Phase-shifted supply signal of the inductive transducers Back+/-: Return lines of the supply voltage for measuring the amplitude. Actual value signal of the oscillator for the supply voltage. CHx: Transducer input and input number PWRGND: Ground

ADDI-DATA connection



Ordering information

APCI-3702

Length measurement board, 16-bit, simultaneous acquisition 5 inductive transducers, LVDT, half-bridge. Incl. technical description and software drivers.

Accessories for HB and LVDT transducer:

PX3701HB-8: PX3701LVDT-8: ST3701-8-KS: Connection box of the APCI-3702 Connection box of the APCI-3702 Shielded coaxial cable between APCI-3702 and connection box PX3701-8

Accessories:

FB3702:Ribbon cable for digital I/OPX901-ZG:Screw terminal panel for digital I/O, for DIN railST010:Standard round cable, shielded, twisted pairs, 2 m



Length measurement board, 16-bit, 16 or 8 inductive transducers, LVDT, half-bridge





PCI 32-bit







With the length measurement board APCI-3701, you can connect directly and acquire up to 16 half-bridge or LVDT transducers. The calibration software "ConfigTools" guides you through each step of the installation, beginning with the selection of a transducer from a database including more than 50 pre-calibrated transducers up to testing each single channel.

Features

- PCI interface to the 32-bit data bus, 3.3 V or 5 V
- Acquisition of 8 or 16 inductive transducers
- (half-bridge, LVDT, Knäbel)16-bit resolution
- Sampling rate depending on the transducer: APCI-3701-8/-16: from 2 to 20 kHz
- Measuring frequency programmable through software: Standard version APCI-3701-8/-16: from 2 to 20 kHz (50 kHz on request)
- Conversion triggered through software, digital input or timer
- End of conversion through software and/or interrupt
- PCI-DMA access
- Onboard FIFO
- Sequence RAM
- 16 digital inputs and outputs, optically isolated, 24 V
- Connection of the transducer through an external box PX3701-8 or -16. The box type depends on the transducer, please order separately.
- Software operation
- Automatic setting of the input levels (gain and offset) acc. to the transducer sensitivity
- Tool for the individual calibration of the transducers with transducer database
- Database for connecting/calibrating a large range of industry-standard transducers (APCI-3701-8, or -16):
- Solartron Tesa Marposs Schlumberger
- Peter & Hirt Mahr RDP Schaevitz
- SMPR Controle
 Knäbel

Safety features

- Input filters
- Diagnostic function in case of short-circuits or line break

APCI-3701

Acquisition of 16 or 8 inductive transducers

Half-bridge, LVDT, Knäbel

16-bit resolution

16 digital inputs and outputs, optically isolated

Measurement of different transducer types

with the same board!

Applications

- Gear wheel control
- Gauge block
- Acquisition of sensor data
- Quality control
- Industrial process control
- Automatic parts control
- R&D Instrumentation

Software

ConfigTools (supplied with the board)

- Easy transducer calibration
- Step by step from the transducer selection up to testing each single channel.
- Database with more than 50 pre-calibrated transducers
- Update of the APCI-3701 firmware

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers for the following compilers and software packages:

- Microsoft VC++
 Borland C++
 Visual Basic
 Delphi
- LabVIEW
 LabWindows/CVI

ADDIPACK functions:

Transducer • Timer • Digital input • Digital output

On request:

Further operating systems, compilers and samples

Driver download: www.addi-data.com, download menu



SPIRIT OF EXCELLENCE

Specifications

Inputs for inducti	ve transducer	S
Channel features		
Number	-4/-8/-16/ multig	Jexed
Input type	Single ended	<u>neneu</u>
Coupling	DC	
Resolution	24-bit	
Sampling rate f_s	On 1 channel	At primary frequency f_p of 4.883 kHz 6.975 kHz
	$f_{\rm s} = f_{\rm p}$	6.975 kHz 9.768 kHz 13.951 kHz
		19.531 kHz
	Ab $n > 2$ channels	$f_{\rm c} = {\rm primary frequency}$
	f_{P}	SP . Settling period $5 \le SP \le 255$
	$f_s = \frac{1}{SP \times n}$	s $f_p = primary frequency$ SP . Settling period 5 \leq SP \leq 255 f_s here concerns all n channels
Example with TESA GT21		$f_{\rm s} = f_{\rm p}$ = 13.951 kHz
	Ab n \ge 2 channels	$f_s = \frac{13.951 \text{ kHz}}{5 \times 4} = 697.5 \text{ Hz for 4 channels}$
		$f_s = \frac{13.951 \text{ kHz}}{5 \times 8} = 348.7 \text{ Hz for 8 channels}$
		$f_{\rm s} = \frac{13.951 \text{ kHz}}{5 \times 16} = 174.4 \text{ Hz for 16 channels}$
Input level		
Input impedance	2 kΩ software-pr	ogrammable
	10 kΩ, 100 kΩ, 1	0 MΩ
Input ranges	± 3 V single ende	
Sensor supply (sinus gen	erator)	
Туре	Sinus differential	(180° phase-shift)
Coupling	AC	
Programmed signals:		
Output frequency $f_{\rm P}$	2-20 kHz dependi	ng on the transducer
(primary frequency)	(50 kHz Knäbel)	-
Output impedance	< 0.1 Ω typ., > 30	0 k Ω typ. in shutdown mode
Short-circuit current	0.7 A typ. at 25°C	with thermal protection
Digital I/O		

Digitati, o	
Number of I/O channels:	8 dig. inputs, 8 dig. outputs, 24 V
Optical isolation:	1000 V through opto-couplers
Input current at 24 V:	3 mA typ.
Max. input frequency:	5 kHz
Max. switching current:	50 mA typ.
Input range:	0-30 V
Output range:	5-30 V

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

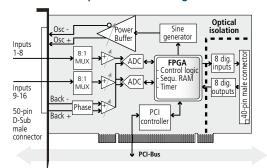
Physical and enviro	nmental conditions
Dimensions:	140 x 99 mm
System bus:	PCI 32-bit 3.3/5 V acc. to spec. 2.2 (PCISiG)
Space required:	1 PCI slot for analog inputs,
	1 slot opening for digital I/O with FB3701
Operating voltage:	$+5 V$, $\pm 5 \%$ from the PC; 24 V external
Current consumption	APCI-3701-8: typ. 630 mA
(+ 5 V from the PC):	APCI-3701-16: typ. 800 mA
Front connector:	50-pin D-Sub male connector
Additional connector:	16-pin male connector for connecting the dig. I/O
Temperature range:	0 to 60 °C (with forced cooling)

APCI-3701

Length measurement board, 16-bit, 16 or 8 inductive transducers, LVDT, half-bridge, Knäbel. Incl. technical description and software drivers.

APCI-3701-8:	For 8 inductive transducers
APCI-3701-16:	For 16 inductive transducers
APCI-3701-8-K:	For 8 Knäbel inductive transducers
APCI-3701-16-K:	For 16 Knäbel inductive transducers
Accessories:	
FB3702:	Ribbon cable for digital I/O
PX901-ZG:	Screw terminal panel for digital I/O, for DIN rail
ST010:	Standard round cable, shielded, twisted pairs, 2 m

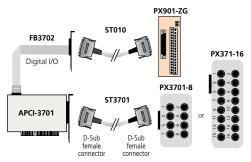
Simplified block diagram



Pin assignment 50-pin D-Sub male connector (APCI-3701-16)

34 BACK+ 18 BACK+ 34 18 1 BACK+ 1 35 BACK- 19 BACK- 35 2 BACK- 2 36 OSC+ 20 OSC+ 37 6 3 3 0SC+ 2 37 OSC+ 21 OSC+ 37 6 4 0SC+ 4 38 OSC- 22 OSC- 38 5 0SC- 5 39 PWRGND 23 OSC- 39 6 6 OSC- 6 40 CH0 24 PWRGND 24 PWRGND 7 PWRGND 7 42 CH3 26 PWRGND 42 6 9 9 PWRGND 9 43 PWRGND 24 PWRGND 43 6 9 9 PWRGND 9
43 PWRGND 20 FMGND 43 44 CH6 27 CH5 44 45 FWRGND 11 PWRGND 11 45 FWRGND 12 CH7 12 46 CH9 29 CH8 45 13 46 CH9 30 PWRGND 14 14 48 CH12 CH7 12 17 12 48 CH2 S PWRGND 13 14 49 PWRGND 32 PWRGND 49 15 PWRGND 15 50 CH15 S CH14 50 33 17 PWRGND 17
Osc+/-: Phase-shifted supply signal of the inductive transducers
Back+/-: Return lines of the supply voltage for measuring the amplitude. Actual value signal of the oscillator for the supply voltage.
CHx: Transducer input and input number PWRGND: Ground

ADDI-DATA connection



Ordering information

Accessories for half-bridge and LVDT transducer:

PX3701HB-8:	Connection box of the APCI-3701-8, 8 x half-bridge
PX3701HB-16:	Connection box of the APCI-3701-16, 16 x half-bridge
PX3701LVDT-8:	Connection box of the APCI-3701-8, 8 x LVDT
PX3701LVDT-16:	Connection box of the APCI-3701-16, 16 x LVDT
ST3701:	Connection cable between APCI-3701 and
	Connection box PX3701



PCI BOARDS: SERIAL COMMUNICATION

Modular serial interfaces

Large field of application

Serial products are still very popular and thus widespread in measurement technology. They are mainly used for the parameterising of machines or for data acquisition of measurement instruments or sensors. Examples are barcode scanners, magnetic card readers, various types of sensors, counter modules, speedometer modules, weighting devices, displays, CNC machines, robots, PLC systems etc.

Flexible mode configuration

ADDI-DATA serial interfaces are based on a concept of a basic circuit board and modules. For the 1-port, 2-port, 4-port or 8-port interfaces, the following modules are available in standard or 20 mA current loop (TTY) version: RS323, RS422 and RS485. The modules can be freely combined. Due to the modular structure, each interface can be configured as required. Thus the hardware can be adapted optimally to your requirements.

No data loss

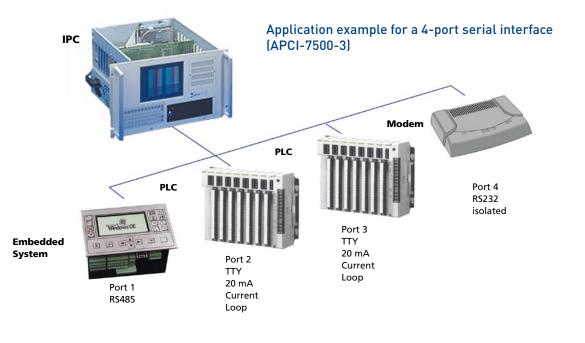
To guarantee a reliable data transfer, the baud rate (max. 1 MBaud) can be adapted to the required frequency and, for higher transfer rates, a 128-byte FIFO buffer is available.

Robust for a safe data transfer

In the industrial environment, potential differences can occur. Therefore, each port of the ADDI-DATA serial interfaces is optically isolated from the other ports. There is also an optical isolation on the PC side. For a reliable data transfer, further protective measures have been implemented: EMC protection such as ESD, burst and short-circuit protection.

Saving money with serial interfaces through retrofit

The serial interfaces of the APCI-7xxx-3 series are available over years in order to secure your investment. They are suitable for retrofit projects with sensors or devices with serial interfaces. You can thus keep on using your sensors for a long time which means huge cost savings.



YOUR ADVANTAGES

- Flexible through modular set-up
- Identified as COM port
- Optical isolation between the ports
- Long-term availability





Serial interfaces (base boards)

Serial interfaces	1-port	2-port	4-port	8-port
Boards	APCI-7300-3	APCI-7420-3	APCI-7500-3, APCI-7500-3/4C	APCI-7800-3
32-bit data bus	PCI 5 V / 3.3 V	PCI 5 V / 3.3 V	PCI 5 V / 3.3 V	PCI 5 V / 3.3 V
Operating mode, configura- ble through MX modules	RS232, RS485, RS422, 20 mA CL	RS232, RS485, RS422, 20 mA CL	RS232, RS485, RS422, 20 mA CL	RS232, RS485, RS422, 20 mA CL
Optical isolation	1000 V , optional	1000 V , optional	1000 V , optional	1000 V , optional
Can be configured as standard interface	✓	1	1	1
Interrupts	BIOS	BIOS	BIOS	BIOS
FIFO memory	128-byte	128-byte	128-byte	128-byte
Remarks		Common interrupt	Common interrupt	Common interrupt
Addressing				
Through software	BIOS	BIOS	BIOS	BIOS
сом	Free configuration	Free configuration	Free configuration	Free configuration
Connection cable			For APCI-7500-3 ST075: 4 x 9 pin ,ST074: 4 x 25 pin	ST7809: 8 x 9 pin ST7825: 8 x 25 pin
Page	210	210	210	210

Mode selectable through modules

For each interface, modules are available in the RS232, RS422, RS485 or 20 mA CL mode. Please order the modules additionally to the selected base boards.

Modules for APCI-7300-3, APCI-7420-3, APCI-7500-3, APCI-7800-3 and CPCI-7500

Operating mode	RS232		RS422		RS485		20 mA CL	
	MX232-G	MX232	MX422-G	MX422	MX485-G	MX485	ΜΧΤΤΥ	
Optical isolation 1000 V	1		1		1		1	
Creeping distance 3.2 mm	1		1		1		1	
Short-circuit protection			1	1	1	1		
ESD protection	1	1	 ✓ 		1			
Burst protection	1	1	~	1	1	1	1	
Duplex	Full	Full	Full	Full	Half	Half	Full	
Max. Baud rate*	1MBaud	1MBaud	1MBaud	1MBaud	1MBaud	1MBaud	19.2 kBaud	
Modem control signals	1	1	Optional RTS/CTS (MX-422-PEP)					
Autom. transmitter control					1	1		
Current consumption	16 mA	1 mA	15 mA	5 mA	15 mA	5 mA	82 mA	

* max 115.2 kBaud, optional up to 1 MBaud with crystal quartz adjustment (Quarz option)



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1 to 8-port serial interface, RS232, RS422, RS485, 20 mA CL, modular mounting through modules

APCI-7500-3/4C





PCI 32-bit

Also for PCISS EXPRESS[®] see APCIe-7xxx, page 142



Also for *CompactPCI*TM See CPCI-7500, page 242





The APCI-7xxx-3 communication boards are configured by inserting MX modules which the board identifies automatically. The 1- to 8-port serial interfaces APCI-7xxx-3 can be used as universal PCI boards in 3.3 V or in 5 V systems, and in PCI or PCI-X systems. The serial interfaces can be configured through modules in the following modes: RS232, RS422, RS485 (with or without optical isolation) and 20 mA current loop (with optical isolation). The MX modules with optical isolation allow a protection up to 1000 V for the use in noisy environments where earth loops can occur. The I/O lines are protected against short-circuits, fast transients, electrostatic discharge and high-frequency EMI. The interface is supported through a 128-byte FIFO buffer for sending and receiving data and guarantees reliable operation at high transfer rates.

Features

- Asynchronous serial interfaces
- PCI 3.3 V or 5 V
- Modular mounting through MX modules

 socket for 1-port serial interface (APCI-7300-3)
 sockets for 2-port serial interface (APCI-7420-3)
 sockets for 4-port serial interface
 (APCI-7500-3 and APCI-7500-3/4C)
 sockets for 8-port serial interface (APCI-7800-3)
- Can be configured as RS232, RS422, RS485 with/without optical isolation, 20 mA Current Loop (active, passive), with optical isolation through separate MX modules
- Automatic addressing through BIOS
- Automatic module identification
- 128-byte FIFO buffer for sending and receiving data
- Programmable transfer rate
- 5, 6, 7 or 8-bit character
- 1, 11/2 or 2 stop bits
- Parity: even, odd or none
- Automatic transmitter control for RS485 and transmitter control through FIFO level
- Common interrupt

Safety features

- MX modules available with optical isolation 1000 V
- Creeping distance IEC 61010-1 (VDE411-1)
- Protection against fast transients (Burst)
- Short-circuit protection for RS422 and RS485
- Detection of false start bits
- Internal diagnostic possibility, break, parity, overrun and framing error

APCI-7300-3 – 1-port serial interface APCI-7420-3 – 2-port serial interface APCI-7500-3 – 4-port serial interface APCI-7800-3 – 8-port serial interface

RS232, RS422, RS485, 20 mA Current Loop

Free mode configuration for each port through MX modules

With/without optical isolation 1000 V

128-byte FIFO buffer for each port

16C950 UART downward compatible

PCI 3.3 V or 5 V

Applications

- Data acquisition Industrial process control
- Direct connection to sensors
- Multi-user systems
- PLC interface
- Multidrop applications
- Weighting devices, modem and printer control, etc.

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

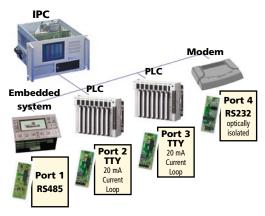
- Microsoft VC++
- Visual Basic Delphi

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com, download menu

Application example for APCI-7500-3





MX modules

Operating mode	RS232		RS422		RS485		20 mA CL	
	MX232-G	MX232	MX422-G	MX422	MX485-G	MX485	MXTTY	
Optical isolation 1000 V	1		1		1		1	
Creeping distance 3.2 mm	1		1		1		1	
Short-circuit protection			1	1	1	1		
ESD protection	1	1	1		1			
Burst protection	1	1	1	1	1	1	1	
Duplex	Full	Full	Full	Full	Half	Half	Full	
Max. Baud rate	1MBaud	1MBaud	1MBaud	1MBaud	1MBaud	1MBaud	19.2 kBaud	
Modem control signals	1	1	Optional RTS/CTS (MX-422-PEP)					
Autom. transmitter control					1	1		
Current consumption	16 mA	1 mA	15 mA	5 mA	15 mA	5 mA	82 mA	

Specifications

APCI-7300-3 / APCI-7420-3 / APCI-7500-3/4C / APCI-7500-3 / APCI-7800-3

Serial interface – 1-port, 2-port, 4-port, 8-port

Mode:	RS232, RS422, RS485, 20 mA Current Loop
	(active, passive) with or without optical isolation
	through separate MX modules
Transmission mode:	Asynchronous, full or half duplex (MX modules)
Addressing:	Automatic through BIOS
Memory:	128-byte FIFO buffer for transmitter and receiver
Transfer rate:	Programmable up to 115.2 kBaud
	Baud rate up to 1 MBaud on request
Protocol:	5-, 6-, 7- or 8-bit character 1,1½ or 2 stop bits
Parity:	Even, odd, none, mark, space
Interrupt lines:	Automatic configuration through BIOS

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Safety features 1000 V (MX modules) Optical isolation: Physical and environmental conditions 151 x 99 mm / APCI-7800-3: 175 x 99 mm Dimensions PCI 32-bit. System bus: 3.3 V/5V acc. to spec. 2.2 (PCISIG) Space required: 1 PCI slot Operating voltage: $+5 V_{,} \pm 5 \%$ from the PC Current consumption (without modules):160 mA typ. / APCI-7800: 220 mA 9-pin D-Sub male connector (APCI-7300-3) Front connector: 2 x 9-pin D-Sub male connector (APCI-7420-3) 4 x 9-pin D-Sub male connector on separate bracket (APCI-7500-3/4C) 37-pin D-Sub male connector (APCI-7500-3) 78-pin D-Sub female connector (APCI-7800-3) 0 to 60 °C (with forced cooling) Temperature range:

Ordering information

APCI-7300-3 / APCI-7420-3 / APCI-7500-3 / APCI-7800-3 APCI-7300-3: 1-port serial interface (1 x 9-pin D-Sub) APCI-7420-3: 2-port serial interface (2 x 9-pin D-Sub) APCI-7500-3: 4-port serial interface (1 x 37-pin D-Sub) APCI-7500-3/4C: 4-port serial interface incl. 4 x 9-pin D-Sub male connector on separate bracket (incl. ribbon cable) APCI-7800-3: 8-port serial interface (1 x 78-pin D-Sub) Each incl. technical description and software drivers. MX modules: Please order the modules separately! Accessories MX232-G: RS232 mode, optically isolated ST075: Shielded round cable, 37 to 4 x 9-pin (for APCI-7500-3) MX232: RS232 mode ST074: Shielded round cable, 37 to 4 x 25-pin (for APCI-7500-3) MY422 DC 433 ST7809: Shielded round cable, 78 to 8 x 9-pin (for APCI-7800-3) ST7825: Shielded round cable, 78 to 8 x 25-pin (for APCI-7800-3)

MX422-G:	RS422 mode, optically isolated
MX422-PEP	RS422 mode, optically isolated, with RTS/CTS
MX422:	RS422 mode
MX485-G:	RS485 mode, optically isolated
MX485:	RS485 mode
MXTTY:	20 mA Current Loop mode (active, passive), optically isolated
Option	



0

Motion control for servo or stepper motors







PCI 32-bit







Customer-tailored modifications designed to suit your needs. Hardware and software, firmware, PLDs, ... Contact us!

* Preliminary product information The board APCI-8008 for the PCI bus is used for the control of up to 8 servo or stepper motor axes through a PC. With this intelligent and flexible board, many control tasks from simple to complicated can be realised.

The board has three stepping/direction output channels (D/A channels, 16-bit). They are optically isolated from the digital current supply and are used for the control of commercially available power amplifiers connected as speed controlling devices or current regulators.

Incremental encoders, SSI encoders and EnDat encoders as well as end and reference switches can be connected to each axis channel.

Digital PID filters with forward compensation and optional Notch filters are also involved in the axis control. The "open" controlling concept of the APCI-8008 is intended in the first place for manufacturers of special-purpose machines and users who need a flexible integration as well as a CNC solution.

Features

Hardware/properties

- Intelligent board based on a 64-bit RISC processor
- Positioning of up to 3 axes either with servo or stepper motors. Mixed operating of servo and stepper motors possible. Up to 8 axes with slave board
- Interface for commercially available power amplifiers
- All input and output channels are optically isolated
- A multiple-axis system can be realised by inserting several APCI-8008 in the same PC.
- 2 Ethernet interfaces incl. one which can be used as an EtherCAT interface.

Software

- Linear, circular, helical, spline and CAD interpolation
 Point-to-point movement with independent control of
- each axis
- Function library for .NET, Pascal, C-Basic, Borland Delphi, Borland C++, Visual Basic, Visual C++, LabVIEW
- Programming through a PC application software or stand-alone (a compiler similar to pascal is supplied with the board)
- The operating program can be easily adapted to specific requirements using program modules supplied with the board (e.g. GEAR, SCANNER, ELCAM)

info@addi-data.com

www.addi-data.com

APCI-8008

For 3 servo or stepper motors Onboard 64-bit RISC processor

Ethernet/EtherCAT interfaces

Incremental encoder, SSI or EnDat 2.2

16-bit analog output channels

Can be extended to a total of 8 axes

- User programs created with the compiler can be processed automatically
- Multitasking: the board can simultaneously process up to 4 user programs.

Applications

- Motion control and position measurement (e.g. optical component measurement)
- Laser processing machines
- Bonding robots
- Water-jet cutting machines
- Tube bending machines
- Tube welding machines
- Component mounting machines (SMD)
- Fibreglass wrapping devices
- Handling systems for analysis technology
- Machines for contact lens production
- Stud welding machines
- Machines for processing dental prostheses
- Production quality control
- Cutting-to-length devices with flying saw

Software

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

- Microsoft C Lib. Borland C Lib.
- Visual Basic Visual C++ Delphi
- LabVIEW

Supplied with the board: McuWIN user interface

On request:

Other operating systems, compilers and samples



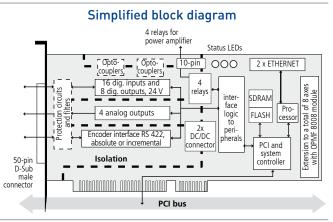
Specifications*

APCI-8008	
CPU system:	64-bit-RISC processor 333 MHz
RAM:	64 MB / Flash 32 MB (1 GB optional)
Data exchange with the PC:	Through PCI bus
Controller software:	PIDF (PID filters with forward compensation)
Interpolation:	2D 3D linear, 2D circular, 3D circular, 3D helix,
	spline, asynchronous and synchronous interpolation
	with secondary axes.
	With OPMF-8008 all interpolations
	2D 8D depending on the number of axes
Inputs for incremental encoders:	Diff. or TTL max. 16 MHz
	Word length: 32-bit with sign
	Short-circuit and line break protection
Inputs for SSI encoders:	Up to 32-bit, Gray / binary code,
	variable frequency 30 kHz to 2 MHz
Inputs for EnDat:	EnDat 2.2 up to 4 MHz
Setpoint value outputs (servo):	4 D/A converters, 16-bit resolution, ± 10 V
Pulse outputs (stepper motors):	1 stepper signal (RS422) and 1 directional signal
	(RS422) for each channel, pulse frequency up to 2 MHz
Isolated digital inputs:	16 inputs, 24 V, as end, reference switch
	or freely programmable
Isolated digital outputs:	8 channels, 24 V / 500 mA, for releasing
	the power amplifiers or freely programmable
Ethernet (option):	2 x Ethernet, 10/100 MBit
Interrupts:	Through PCI BIOS
DMA:	Bus master
Auxiliary voltage:	24 V external for digital I/O, 5 V, 1.1 A
Safety	
Optical isolation:	1000 V

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and environmental conditions		
Dimensions:	175 x 106 mm	
System bus:	PCI 32-bit 3.3/5 V acc. to spec. 2.2 (PCISiG)	
Space required:	Board APCI-8008: 1 PCI slot	
	Slave board OPMF: 1 PCI slot	
	Cable FB8008: 1 slot opening	
Operating voltage:	$+$ 5 V \pm 5 % from the PC	
Front connector APCI-8008:	Axis 1, 2, 3: 50-pin D-Sub male connector	
Front connector OPMF-8008:	Axis 4, 5, 6: 50-pin D-Sub male connector	
Ribbon cable FB8008:	Axis 7, 8: 50-pin D-Sub male connector	
Temperature range:	0 to 60 °C (with forced cooling)	

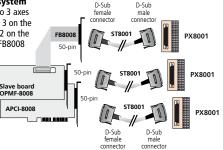


Pin assignment – 50-pin D-Sub male connector

Pin	Pin	\sim	1	Pin
34 Steppint value 3 / step 35 Steppint value 3 / step 30 True value 3 37 True value 3 38 True value 3 39 True value 3 30 True value 3 31 True value 3 32 True value 3 / step 3 34 True value 3 / step 3 35 True value 4 / step 3 36 True value 5 / step 3 37 True value 1 / step 1 38 True value 7 / step 1 39 Dig. input 10 40 Dig. input 12 40 Dig. input 14 40 Dig. input 14 40 Dig. input 16 50 O V ext. for dig. I/O		34 18.0 1 35 2 36 3 37 4 38 5 6 40 7 4 4 9 6 40 7 4 11 4 11 4 12 46 12 46 12 46 12 46 14 48 15 50 50 33 17 50 33 17 50 33 17 50 33 17 50 50 33 17 50	Dig. input 3 Dig. input 4 Dig. input 5 Dig. input 6 Dig. input 7 Dig. input 8	1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17

ADDI-DATA connection

Example for an 8-axes system APCI-8008: Standard 1 up to 3 axes OPMF/8A: 5 axes, including 3 on the 50-pin front connector and 2 on the connector for ribbon cable FB8008



Ordering information

 APCI-8008:
 Motion control board for servo or stepper motors. 16 dig. inputs and 8 dig. outputs, 24 V, optically isolated. Incl. technical description, software drivers.

 APCI-8008-STP:
 same as APCI-8008, only for stepper motors

Options: All options begin with OPMF-8008. Please complete with the following option name:

-Basis: Mezzanine board for the extension with -AI16-4, -AO and -DIO (only up to 3 axes) -4A-SRV/-4A-STP: 4th axis – 8 inputs and 4 dig. outputs in addition -5A-SRV/-5A-STP: 5th axis – 16 inputs and 8 dig. outputs in addition -6A-SRV/-6A-STP: 6th axis – 16 inputs and 8 dig. outputs in addition For the option -7A and more the FB8008 cable is required -7A-SRV/-7A-STP: 7th axis – 24 inputs and 12 dig. outputs in addition -8A-SRV/-8A-STP: 8th axis – 24 inputs and 12 dig. outputs in addition -AI16-4: 4 analog inputs (option available in single or double, max. 8 analog inputs), 16-bit resolution. -ETH: Mezzanine board for the connection of 2 Ethernet interfaces (Standard Ethernet / EtherCAT) -DIO: 8 digital inputs and 4 dig. outputs, opt. isolated (option available up to 3 times, max. 24 inputs and 12 outputs) -AO: 1 analog output, option available up to 5 times (max. 8 analog outputs) (output is only free when the axis is not used) OPT.CAN-8008: CAN bus connection of the APCI-8008 (not CAN Open).

Accessories:

///////////////////////////////////////	
FB-CAN:	Ribbon cable between OPMF and 9-pin D-Sub male con- nector with bracket for connecting the CAN bus.
FB-INTERBUS:	Ribbon cable between OPMF and 9-pin D-Sub male connec- tor with bracket for connecting the INTERBUS.
FB8008:	From the 1rst axis on for connecting the analog inputs (option OPMF-8008-AI-16-4). Ribbon cable between OPMF and a 50-pin D-Sub male connector with bracket. On request with female connector.
FB8008_50_25:	From the 4th axis on for connecting the analog inputs (OPMF-8008-Al16-4) or from the 7th axis on (OPMF/7; OPMF/8) for connecting additional axes. Ribbon cable between OPMF and D-Sub male connector on bracket and the 25-pin D-Sub for the connecting the relays.
FBRELAY:	For releasing the relays FBRELAY_9 : Standard, 9-pin cable with bracket FBRELAY_25 : more than 3 axes: 25-pin cable.
PX8001: ST8001:	3-row terminal panel for DIN rail Cable for connecting APCI-8008 and OPMF, 50-pin.

* Preliminary product information



Digital I/O board, optically isolated, 32 digital inputs and outputs, 24 V, for PC/104-Plus



















Features

2 programmable timers

Inputs

- 16 optically isolated digital inputs, 24 V, including 2 interruptible inputs
- Reverse voltage protection
- All inputs are filtered

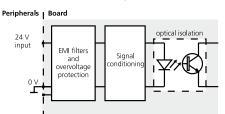
Outputs

- 16 optically isolated digital outputs, 11 V to 36 V
- Output current per channel 150 mA
- Timer-programmable watchdog for resetting . the outputs to "0"
- Diagnostic report through status register at shortcircuits, overtemperature, voltage drop or watchdog
- Interrupt triggered through watchdog, timer, error
- At Power-On, the outputs are reset to "0" Short-circuit current for 16 outputs ~ 2 A typ.
- Short-circuit current per output ~1.1 A peak
- Self-resetting fuse (electronic fuse)
- Overtemperature and overvoltage protection 24 V power outputs with protection diodes and filters
- Output capacitors against electromagnetic emissions
- Shutdown logic, when the external supply voltage

Safety features

- Optical isolation 1000 V
- Creeping distance IEC 61010-1
- Protection against fast transients (burst), overvoltage, electrostatic discharge and high-frequency EMI
- · Separate ground line for inputs and outputs

Protective circuit for the input channels



PC104-PLUS1500

16 digital inputs, 24 V,

including 2 interruptible inputs

16 digital outputs, 24 V, 150 mA/channel

Optical isolation 1000 V

Input and output filters

Watchdog, timer, counter

The outputs are reset to "0" at Power-On

Applications

- Industrial I/O control
- PLC coupling
- Signal switching
- Interface to electromechanical relays
- Automatic test equipment
- ON/OFF monitoring of motors, lights...
- ...

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

- Microsoft CVC++ Borland C++
- Visual Basic Delphi
- LabVIEW LabWindows/CVI

ADDIPACK functions

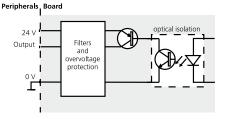
- Digital input Digital output
- Watchdog Timer Counter

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads

Protective circuit for the output channels





info@addi-data.com www.addi-data.com

- - Ext. 24 V voltage supply screened and filtered

drops under 7 V

Specifications

Digital inputs				
Number of inputs:	16 (common ground acc. to IEC 1131-2) including one input used as a counter input (channel 0)			
Interruptible inputs:	2 (channel 2 and 3	3)		
Optical isolation:		olers, 1000 V, from PC to peripheral		
Interrupt compare logic:	OR mode (with fixed filter times)			
Filters for interruptible inputs:	40 µs			
Nominal voltage:	24 V			
Input current at 24 V:	Channel 0:	6 mA typ.		
	Channel 1-15:	3.9 mA typ.		
Logic input levels:	U nominal: 24 V			
UH max.:	30 V/current 6 mA	typ.		
UH min.:	19 V/current 2 mA	typ.		
UL max.:	14 V/current 0.7 mA typ.			
UL min.:	0 V/current 0 mA t	yp.		
Maximal input frequency:	Channel 0:	100 kHz (at 24 V)		
	Channel 1-15:	5 kHz (at 24 V)		

Digital outputs

Number of outputs:	16, optically isolated up to 1000 V
	through opto-couplers
Output type:	High-side (load to ground) acc. to IEC 1131-2
Nominal voltage:	24 V
Supply voltage:	11 V up to 36 V
Current limit:	1.5 A typ. per 8 channels
Output current/output:	150 mA typ.
Short-circuit current/output	
shutdown at 24 V, $R_{load} < 0.1\Omega$:	1.1 A (typ.) pulse current
RDS ON resistance:	0.2 Ω at 25 °C max.
Switch-on time (typ.):	50 µs
Switch-off time (typ.):	75 μs
Overtemperature (shutdown):	135 °C (output driver)
Temperature hysteresis:	10 °C (output driver)

Safety

Shutdown logic:	When the ext. 24 V voltage drops below 7 V: The outputs are switched off.	
Diagnostics:	Status bit or interrupt to the PC	
Timer1/Watchdog:	1, 12-bit, time bases µs, ms, s	
Timer2:	1, 12-bit, time bases µs, ms, s	
Counter:	1, 16-bit, signal channel 0, Limit frequency 100 kHz	

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and environmental conditions

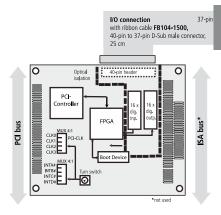
,	
Dimensions:	90 x 96 mm
System bus:	PCI 32-bit 5 V acc. to specification 2.1 (PCISIG)
Mounting in:	PC/104-Plus system
Operating voltage:	+5 V or +3.3 V, ± 5 % from the PC
Current consumption:	
+ 3.3 V from PC	95 mA
+ 5 V from the PC	45 mA
I/O-connector:	40-pin male connector (2-row, 2.54 mm grid)
Temperature range:	0 to 60 °C (with forced cooling)
	-40 to +85 °C (with forced cooling), PC104-PLUS1500-EXT

PC104-PLUS1500

PC104-PLUS1500:
 PC104-PLUS1500:
 Digital I/O board, optically isolated, 32 digital inputs and outputs, 24 V. Incl. technical description and software drivers.
 Digital I/O board, optically isolated, 32 digital inputs and outputs, 24 V, extended temperature range. Incl. technical description and software drivers.

Accessories FB104-1500: Ribbon cable, 40-pin to 37-pin D-Sub male connector, 25 cm ST010: Standard round cable, shielded, twisted pairs, 2 m PX901-D: Screw terminal panel, LED status display ST011: Standard round cable, shielded, twisted pairs, 5 m Same as ST010, for high currents (24 V supply separate) PX901-DG: Screw terminal panel, ST010-S: Round cable between FB104-1500 and PX 8500-G, LED status display, for DIN rail ST021: PX9000: 3-row screw terminal panel, shielded, twisted pairs, 2 m for DIN rail, LED status display ST022: Round cable between PX 8500-G and PX 901-DG, shielded, 2m PX8500-G: Relay output board for DIN rail, cascadable ST8500: Ribbon cable for cascading two PX 8500-G

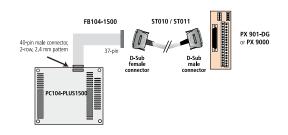
Simplified block diagram



Pin assignment – 40-pin to 37-pin male connector

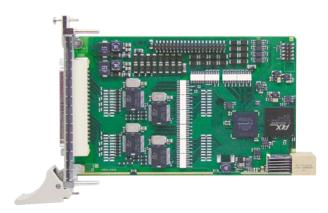
	/	F	B104-1500		
40-	40-pin connector				ctor
				1	7
Dig. input0 Dig. input2 Dig. input4 Dig. input6 Dig. input8 Dig. input10 Dig. input12 Dig. input14	1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16	Dig. input1 Dig. input3 Dig. input5 Dig. input7 Dig. input9 Dig. input11 Dig. input13 Dig. input15	Dig. input1 Dig. input3 Dig. input5 Dig. input7 Dig. input9 Dig. input11 Dig. input13 Dig. input15	20 1 21 2 22 3 23 4 24 5 25 6 26 7 27 8	Dig. input0 Dig. input2 Dig. input4 Dig. input6 Dig. input8 Dig. input10 Dig. input12 Dig. input14
24 V ext. Dig. input GND Dig. output0 Dig. output2 Dig. output2 Dig. output4 Dig. output6 Dig. output10 Dig. output10 Dig. output112 Dig. output14 Not connected Not connected	17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40	24 V ext. Dig. output GND Dig. output1 Dig. output3 Dig. output3 Dig. output7 Dig. output7 Dig. output11 Dig. output11 Dig. output15 Not connected	24 V ext. Dig. output GND Dig. output1 Dig. output3 Dig. output5 Dig. output5 Dig. output9 Dig. output11 Dig. output13 Dig. output15	28 99 29 10 30 112 32 33 34 35 36 7 77 16 16 37 16 17 16 17 16 17 16 16 17 16 16 16 16 16 16 16 16 16 16	Dig. output0 Dig. output2 Dig. output2 Dig. output4 Dig. output4 Dig. output6 Dig. output8 Dig. output10 Dig. output12 Dig. output14

ADDI-DATA connection



Ordering information





CompactPCI® Serial

YOUR BENEFITS

- Fast data transmission rate: up to 12 GB/s
- Star topology
- Standardized 19" technology (IEEE 1101)
- All protocols available on the connector
- Hybrid systems

More data - simply faster!

With its new serial data transfer, the new CompactPCI Serial bus is especially interesting for developing new high-performance systems in industrial environment. It also opens up new fields of application. ADDI-DATA now offers digital, analog and counter

boards for the new CompactPCI Serial bus technology, the Bus-Technologie, in the tried-and-tested quality we always deliver.

Multiple data transfer options

What was not possible with the CompactPCI bus is now possible: the extension boards can now be accessed via Ethernet, PCI Express, SATA or USB. All protocols are available on the connector with equal priority.

High immunity to interferences

The well-though concept of design and protective circuitry is the key for the high immunity to interferences featured by the CompactPCI Serial boards by ADDI-DATA. These boards are thus especially suited for use in extreme industrial environment. They are resistant to vibration, acceleration and dirt while supplying reliable data.

Faster through FPGA

A FPGA component has a programmable logic on which you can save your own algorithms in order to adapt the functionality of the PC board to your requirements. This adaptation makes your PC board unique and improves the performance of your applications. The onboard algorithms reduce the cycle time of signal acquisition and regulation tasks.

Most ADDI-DATA CompactPCI Serial boards are equipped with a FPGA component. Use the full potential of your PC board hardware and software resources and thus accelerate your processes.

Hybrid systems

Some housing manufacturers offer hybrid systems in which existing applications can run with CompactPCI while new functions can be added using CompactPCI serial. This saves time and money and allows for a smooth transition between these two technologies. READY FOR HARSH INDUSTRIAL ENVIRONMENT

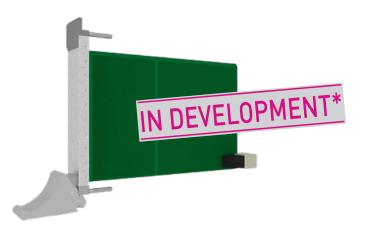


CompactPCI® Serial

	Dig	ital	Counter	Ana	alog
	CPCIs-1532	CPCIs-1564	CPCIs-1711	CPCIs-3121	CPCIs-3131
CompactPCI Serial bus	1	1	1	1	1
FPGA	1	1	1	1	1
Filter and protective circuits	1	1	1	1	1
Optical isolation	1000 V	1000 V	1000 V	500 V	1000 V
Digital, 24 V					
Input channels, 24 V	16	32	12 (dependent on function)	4	4
Output channels, 24 V	16	32	4	4	4
Output current per channel	500 mA (typ.)	500 mA (typ.)	500 mA (typ.)		
Timer / Counter / Watchdog	2 x 12 bit timer, 1 can be used as a watchdog.	2 x 12 bit timer, 1 can be used as a watchdog. 3 x 32 bit counter		1 x 24 bit timer can be used as a watchdog.	3 x 16 bit timer, 3 x 16 bit counter, 2 x 16 bit watchdog
Counter					
Function modules			4		
Functions Incremental counter, SSI synchronous serial interface, Counter/timer, Pulse acquisition, Frequency, Pulse width, Period duration measurement, velocity measurement, PWM, BiSS-Master, digital inputs and outputs,			Reprogrammable		
Input frequency			up to 5 MHz		
Signals			TTL, RS422, 24 V		
Analog					
Analog inputs, 16 bit				16 SE or 8 diff.	8 SE or 8 diff.
Throughput (kHz)				100	100
Voltage range				0-10 V ± 10 V	0-10 V ± 10 V
Gain PGA				x1, x2, x5, x10	x1, x10, x100, x1000
Trigger (software or 24 V)				1	1
Sequence RAM				1	
Analog outputs				8 or 4, 16 bit	4, 16 bit
0-10 V ± 10 V				1	1
Software	Current driver list on the web: www.addi-data.com				
Page	218	220	222	224	226



Digital I/O board, optically isolated, 32 digital inputs and outputs, 24 V, for CompactPCI Serial





Also for EXPRESS See APCIe-1532, page 118

Also for PCI See APCI-1500, page 146

Also for *CompactPCI*™ See CPCI-1500, page 230

Also for PC/104-PLUS see PC104-PLUS1500 page 214







+49 7229 1847-222

* Preliminary product information

Features

Inputs

- 16 optically isolated inputs, 24 V incl. 15 interruptible inputs
- Channel 0 can be used as a 16-bit counter input (up to 100 kHz)
- Reverse voltage protection
- All inputs are filtered

Outputs

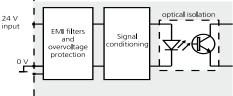
- 16 optically isolated outputs, 11 to 36 V
- Output current per channel 500 mA
- Total current: 3 A typ. (fused through PTC resistor)
- Watchdog for resetting the outputs to "0"
- At Power-On, reset of the outputs to "0"
- Current limit: ~1.5 A per 8 channels (through PTC) •
- Short-circuit current per output ~1.5 A typ.
- Self-resetting fuse (electronic fuse)
- Overtemperature and overvoltage protection •
- 24 V power outputs with protection diodes and filters
- Ext. 24 V voltage supply screened and filtered
- Shutdown logic, when the external supply voltage drops below 7 V

Timer / Counter

- 2 timers (12-bit resolution) • 1 timer can be used as watchdog
- - Safety features Optical isolation 1000 V
 - Creeping distance IEC 61010-1
 - Protection against fast transients (burst), overvoltage, electrostatic discharge and high-frequency EMI
 - · Separate ground line for inputs and outputs

Protective circuit for the input channels

Peripherals | Board



info@addi-data.com www.addi-data.com

CPCIs-1532

CompactPCI Serial interface

16 digital inputs, 24 V,

including 15 interruptible inputs

16 digital outputs, 24 V, 500 mA/channel

Optical isolation 1000 V

Input and output filters

Connection through industry-standard

D-Sub connector

Extended temperature range - 40 °C to + 85 °C

Applications

- Industrial I/O control
- PLC coupling
- Reading of encoder values for process control
- Signal switching
- Interface to electromechanical relays
- ON/OFF monitoring of motors, lights...
- Watchdog / timer
- Interface to machines

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000 •
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

MC .NET, C

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads

Protective circuit for the output channels

Peripherals Board 24 V Filters Output and I overvoltage protection ٥ ١



Phone: +49 7229 1847-0 218 Fax:

Simplified block diagram

compactPCI® Serial

CompactPCI serial

a 16 16 digital inputs FPGA control digital inputs Proloaic tective 16 16 digital outputs - Digital I/O - Watchdog digital outputs circuits and Diagnostic filters I. output 37-pin. D-Sub Optical male connecto PCIe controller isolation Input filters, transil diode, RC filters, Z diode, opto-couplers Pin assignment – 37-pin D-Sub male connector Dia, input 0 Dig. input 1 Dig. input 2 Dig. input 4 2 3 4 5 6 7 Dig. input 3 Dig. input 3 Dig. input 5 Dig. input 7 Dig. input 9 Dig. input 11 Dig. input 13 Dig. input 13 24 V ext. (Outputs) 0 V ext. Dig. output 1 Dig input 4 Dig input 6 Dig input 8 Dig input 10 Dig input 12 Dig input 14 24 V ext 8 9 (Inputs) 0 V ext. Dig. output 0 Dig. output 2 Dig. output 1 Dig. output 3 Dig. output 5 Dig. output 7 Dig. output 4 Dig. output 6 Dig. output 8 Dig. output 10 Dig. output 9

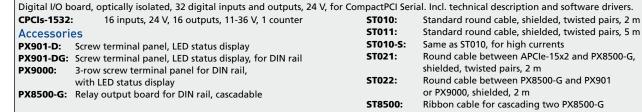
Dig. output 11 Dig. output 13 Dig. output 15 Dia. output 12

Dig. output 14 Diagnostic output

ADDI-DATA connection

Example 1 Connection of the inputs and outputs through screw terminal panel CPCIs-1532 D-Sub female D-Sub male Example 2 Connection of the inputs ST022 through screw terminal ST 8500 D-Sub male panel PX901-DG Connection of the outputs PX901-D through relay output board PX8500-G ST021 CPCIs-1532 D-Sub male PX8500

Ordering information



* Preliminary product information



Specifications*

Digital inputs

Digital inputs		
Number of inputs: (common ground acc. to IEC 1131-2)	16 digital inputs, channel 0 can be used (up to 100 kHz)	as a 16-bit counter input
Interruptible inputs:	15 channels (channel	1 to 15)
Optical isolation:		couplers, from PC to peripheral
Nominal voltage:	24 V (CPCIs-1532), 12	V (CPCIs-1532-12V)
Input current:	at 24 V	at 12 V (CPCIs-1532-12V)
Channel 0 or 0-1:	6.6 mA typ.	3.2 mA typ.
Channel 1-15 or 2-16:	2 mA typ.	1.5 mA typ.
Input frequency (max.):	at 24 V	at 12 V (CPCIs-1532-12V)
Channel 0 or 0-1:	100 kHz	100 kHz
Channel 1-15 or 2-16:	5 kHz	5 kHz
Logic input levels:	at 24 V	at 12 V (CPCIs-1532-12V)
UH (max.):	30 V	16 V
UH (min.):	19 V	9 V
UL (max.):	14 V	6 V
UL (min.):	0 V	0 V

Filters/protective circuit:

Digital outputs

	J			
Number of outputs:		16 digital outputs		
Output type:		High-side (load to ground) acc. to IEC 1131-2		
	Optical isolation:	1000 V (through opto-couplers), from PC to peripheral		
	Nominal voltage:	24 V		
	Supply voltage range:	11 to 36 V		
	Current limit:	1.5 A per 8 channels (through PTC)		
	Output current per output:	500 mA (typical)		
	Short-circuit current per output:	1.5 A (typ.) pulse current		
		shutdown at 24 V, R_{load} < 0.1 Ω		
	RDS ON resistance:	max. 0.2 Ω at 25 °C		
	Switch-on time:	l _{out} =0.5 A, load = resistance: 50 μs		
	Switch-off time:	l _{out} =0.5 A, load = resistance: 75 μs		
	Overtemperature (shutdown):	135 °C (output driver)		
	Temperature hysteresis:	15 °C (output driver)		

Timer/watchdog

Timer:	2 x 12-bit timers, 1 up to 4095 μs, ms, s 1 timer can be used as watchdog.
Safety	
Shutdown logic (V _{CC} diagnostic):	When the ext. 24 V voltage drops below 7 V:

The outputs are switched off. For resetting the outputs to "0" Watchdog: Common diagnostics: For all 16 channels at overtemperature of one channel

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and environmental conditions

149 x 99 mm			
PCI Express according			
CompactPCI Serial specification PICMG CPCI-S.0 R1.0			
1 x CompactPCI Serial slot			
+ 3.3 V from PC			
Inputs/outputs inactive	320 mA ± 10 %, typ.		
8 inputs/outputs active	400 mA ± 10 %, typ.		
16 inputs/outputs active	470 mA ± 10 %, typ.		
37-pin D-Sub male connector			
from – 40°C to + 85 °C			
	PCI Express according CompactPCI Serial specification 1 x CompactPCI Serial slot + 3.3 V from PC Inputs/outputs inactive 8 inputs/outputs active 16 inputs/outputs active 37-pin D-Sub male connector		

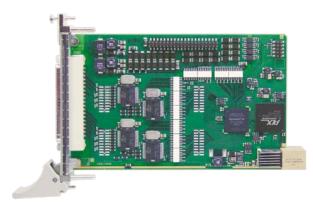
CPCIs-1532

21

9	Phone:	+49 7229 1847-0	into@addi-data.com	
	Fax:	+49 7229 1847-222	www.addi-data.com	

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Digital I/O board, optically isolated, 64 digital inputs and outputs, 24 V, for CompactPCI Serial





Also for FYP See APCIe-1564, page 124

Also for **PCI** See APCI-1564, page 150

Also for *CompactPCI™* See CPCI-1564, page 232







* Preliminary

product information

LabVIEW™ on request

Features

- Inputs
- 32 optically isolated inputs, 24 V, incl. 16 interruptible inputs
- Channels 0-2 can be used as 32-bit counter inputs (up to 500 kHz)
- Reverse voltage protection
- All inputs are filtered

Outputs

- 32 optically isolated outputs, 11 to 36 V
- Output current per channel 500 mA
- Total current: 3 A typ. (fused through PTC resistor) •
- Watchdog for resetting the outputs to "0"
- At Power-On, reset of the outputs to "0"
- Current limit: ~1.5 A per 8 channels (through PTC)
- Short-circuit current per output ~1.5 A typ.
- Self-resetting fuse (electronic fuse)
- Overtemperature and overvoltage protection
- 24 V power outputs with protection diodes and filters
- Ext. 24 V voltage supply screened and filtered
- Shutdown logic, when the external supply voltage drops below 7 V

Timer / Watchdog / Counter

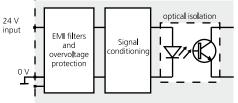
- 2 timers (12-bit), of which one can be used as a watchdog
- 3 counter (32-bit)

Safety features

- Optical isolation 1000 V
- Creeping distance IEC 61010-1
- Protection against fast transients (burst), overvoltage, electrostatic discharge and high-frequency EMI
- · Separate ground line for inputs and outputs

Protective circuit for the input channels

Peripherals | Board





CPCIs-1564

CompactPCI Serial interface

32 digital inputs, 24 V,

including 16 interruptible inputs

32 digital outputs, 24 V, 500 mA/channel

Optical isolation 1000 V

Input and output filters

Connection through industry-standard

D-Sub connector

Extended temperature range – 40 °C to + 85 °C

Applications

- Industrial I/O control
- PLC coupling
- Signal switching
- Interface to electromechanical relays
- Automatic test equipment
- ON/OFF monitoring of motors, lights...
- Watchdog/timer
- ...

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000 •
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

• C#.NET, C

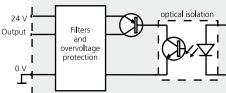
On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads

Protective circuit for the output channels

Peripherals Board





info@addi-data.com

www.addi-data.com

•

Specifications*

Digital inputs

Digital inputs					
Number of inputs: (common ground acc. to IEC 1131-2)	32 digital inputs, channel 0-2 can be used as 32-bit counter inputs (up to 500 kHz)				
Interruptible inputs:	16 channels (chan	nel 4 to 19)			
Optical isolation:	1000 V through op	oto-couplers, f	rom PC to peripheral		
Nominal voltage:	24 V				
Input current:	Channel 0-3:	6.6 mA	at 24 V, typical		
	Channel 4-31:	2 mA	at 24 V, typical		
Input frequency (max.):	Channel 0-2:	500 kHz	at 24 V		
	Channel 3-31:	5 kHz	at 24 V		
Logic input levels:	UH (max.):	30 V / 3.1 mA, typical (channel 4-3 19 V / 1 mA, typical (channel 4-31) 30 V / 11 mA, typical (channel 0-3) 19 V / 3.4 mA, typical (channel 0-3 14 V / 0.1 mA, typical			
	UH (min.):				
	UH (max.):				
	UH (min.):				
	UL (max.):				
	UL (min.): 0 V / 0 mA, typical				
Filters/protective circuit:	Input filters, trans				
RC filters, Z diode, opto-couplers					

Digital outputs

Number of outputs:	32 digital outputs
Output type:	High-side (load to ground) acc. to IEC 1131-2
Optical isolation:	1000 V (through opto-couplers), from PC to peripheral
Nominal voltage:	24 V
Supply voltage range:	11 to 36 V
Current limit:	1.5 A per 8 channels (through PTC)
Output current per output:	500 mA (typical)
Short-circuit current per output:	1.5 A (typ.) pulse current
	shutdown at 24 V, R_{load} < 0.1 Ω
RDS ON resistance:	0.2 Ω at 25 °C
Switch-on time:	l out=0.5 A, load = resistance: 50 μs
Switch-off time:	l out=0.5 A, load = resistance: 75 μs
Overtemperature (shutdown):	135 °C (output driver)
Temperature hysteresis:	15 °C (output driver)

Timer/watchdog

Watchdog:	12-bit, programmable as timer from $1\mu s$ to 4095 s
Safety	
Shutdown logic (V _{CC} diagnostic):	When the ext. 24 V voltage drops below 7 V, the outputs are switched off.
Watchdog:	For resetting the outputs to "0"
Common diagnostics:	For all 16 channels at overtemperature of one channel

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and environmental conditions

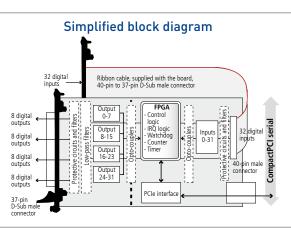
Dimensions:	3U/4TE		
System bus:	PCI Express according		
	CompactPCI Serial specification PICMG CPCI-S.0 R1.0		
Space required:	1 x CompactPCI Serial slot		
Operating voltage:	+12 V, ±5 %		
Current consumption:	in preparation		
Front connector:	37-pin D-Sub male connector		
Temperature range:	from – 40°C to + 85 °C		
MTBF	in preparation		

CPCIs-1564

Digital I/O board, optically isolated, 64 digital inputs and outputs, 24 V, for CompactPCI Serial. Incl. technical description and software drivers.

Accessories

Accessories			
PX901-D:	901-D: Screw terminal panel, LED status display		
PX901-DG:	(901-DG: Screw terminal panel,		
	LED status display, for DIN rail		
PX9000:	X9000: 3-row screw terminal panel		
	for DIN rail, with LED status display		
PX8500-G:	Relay output board for DIN rail, cascadable		

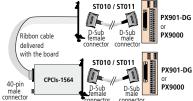


Pin assignment – 37-pin D-Sub male connector

Counter input 1	20	1	Counter input 0	Dig. output 1	20	1	Dig. output 0
Dig. input 3	21	2	Counter input 2	Dig. output 3	21	2	Dig. output 2
Dig. input 5	22	3	Dig. input 4	Dig. output 5	22	3	Dig. output 4
Dig. input 7	23	4	Dig. input 6	Dig. output 7	23	4	Dig. output 4
Dig. input 7	24	5	Dig. input 8	Dig. output 7	24	5	Dig. output 8
Dig. input 11	25	6	Dig. input 10	Dig. output 11	25	6	Dig. output 10
Dig. input 11	26	7	Dig. input 12	Dig. output 13	26	7	Dig. output 12
Dig. input 15 GND 0 GND 3 Dig. input 19 Dig. input 21 Dig. input 21 Dig. input 23 Dig. input 25 Dig. input 27 Dig. input 29 Dig. input 31	27 28 29 30 31 32 33 34	8 9 10 11 12 13 14 15 16 17 18 19	Dig. input 14 GND 1 GND 2 Dig. input 16 Dig. input 18 Dig. input 20 Dig. input 20 Dig. input 22 Dig. input 24 Dig. input 26 Dig. input 30 Not connected	big. output 15 24 V ext. GND Dig. output 17 Dig. output 17 Dig. output 11 Dig. output 21 Dig. output 23 Dig. output 25 Dig. output 27 Dig. output 29 Dig. output 31	27 27 28 29 30 31 32 33 34 35 36 37	8 9 10 11 12 13 14 15 16 17 18 19	Dig. output 14 24 V ext. GND Dig. output 16 Dig. output 16 Dig. output 20 Dig. output 20 Dig. output 22 Dig. output 24 Dig. output 26 Dig. output 26 Dig. output 30 Diagnostic (at 24V)

ADDI-DATA connection

- Example 1: Connection of the inputs
- (Ribbon cable)
- Connection of the outputs through screw terminal panel PX901-DG or PX9000



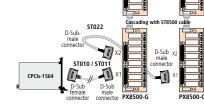
ector

PX8500-G

connecto

PX8500-G

Example 2: Connection of the outputs with relay output board PX8500-G cascaded in 32 relays



Ordering information

ST010:	Standard round cable, shielded, twisted pairs, 2 m
ST011:	Standard round cable, shielded, twisted pairs, 5 m
ST010-S:	Same as ST010, for high currents
ST022:	Round cable between PX8500-G and PX901
	or PX9000, shielded, 2 m
ST8500:	Ribbon cable for cascading two PX8500-G

* Preliminary product information







CPCIs-1711

Available functions: incremental counter, SSI Synchronous Serial Interface, counter/timer, pulse acquisition, frequency, pulse width, period duration, velocity measurement, PWM, BiSS-Master, digital I/O, Sin/Cos, EnDat 2.2 ...

Function selection through software

Optical isolation

Inputs and outputs: RS422, TTL, 24 V

Customised functions

Extended temperature range – 40 °C to + 85 °C



Also for EXPRESS See APCIe-1711, page 128

Also for **PCI** see APCI-1710 page 166

Also for *CompactPCI*TM see CPCI-1710 page 234







* Preliminary product information The board CPCIs-1711 is a fast multifunction and multichannel counter board for CompactPCI Serial. The strengths of this board are its wide range of applications and high precision and reliability in harsh industrial

environment. With this board you can realise many different applications on the same hardware base. The board is supplied with a pool of functions which provides the user with maximum efficiency yet minimum space and parts requirement. The functions are individually configured for each channel through the supplied software. The flexible programming facilities on this board allow many different user applications to be quickly and easily developed and reconfigured as further requirements arise. Thanks to the FPGA board structure, further counting applications can be realised through software adaptation. Contact us!

Features

- 32-bit data access
- RS422 driver 5 MHz (up to 20 MHz on request)
- With RS422/TTL input/output signals (CPCIs-1711) or 24 V input signals (CPCIs-1711-24 V)
- Four onboard function modules

Functions

- Incremental counter for the acquisition of incremental encoders (90° phase-shifted signals)
- BiSS-Master (B and C mode)
- SSI Synchronous Serial Interface. The SSI function is an interface for systems which allow an absolute position information via serial data transfer.
- Counter/timer (82C54)
- Pulse acquisition
- Frequency measurement
- Pulse width modulation (PWM)
- Period duration measurement
- Velocity measurement
- Digital inputs and outputs
- Edge time measurement (ETM)
- Parallel interface
- Sin/Cos (1 V_{SS}, 11 A_{SS})
- EnDat 2.2
- Customised functions

Available channels on one function module

- 4 channels, programmable either as digital inputs or outputs, optically isolated, RS422
 - 3 channels, digital inputs, optically isolated, 24 V
- 1 digital power outputs, optically isolated, 24 V

Additional channels

28 TTL I/O, without optical isolation

versions	RS422/ TTL- I/O	24 V inputs	5 V inputs	24 V outputs	TTL I/O
CPCIs-1711	16	12	-	4	28
CPCIs-1711-24V	-	28	-	4	28
CPCIs-1711-5V-I	16		12	4	28

Safety features

- Creeping distance IEC 61010-1
- Optical isolation 1000 V
- Noise neutralisation of the PC supply

Applications

- Event counting
- Position acquisition
- Motion control
- Batch counting
- ...

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

Microsoft VC++ • Borland C++ 5.01

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads



compactPCI® Serial

Specifications*

Free programming of the functions

- Acquisition of incremental encoders (1 x 32-bit or 2 x 16-bit)
- SSI (max. 3 encoders per module)
- Counter/Timer (3 counters similar to 82C54) Pulse counter (4 x 32-bit counters per module)
- Chronos (chronometer)
- TOR (pulse counter with time slices, ...)
- Digital I/O (8 I/O, 24 V, TTL, RS422)
- PWM (pulse width modulation, 2 x per module)
- BiSS-Master (B and C mode)
- ETM (Timer interface for period duration measurement, edge time, ...) TTL (TTL I/O without isolation)
- Parallel Interface
- EnDat 2.2
- Sin/Cos • Customised functions

Signals

Digital I/O signals, TTL or RS422, 24 V

Inputs

inputs				
Differential inputs or outputs (A, B, C, D)				
Differential inputs, RS422:	16 (can be used as inputs or outputs)			
Nominal voltage:	3.3 VDC			
Common mode range:	+12 / -7 V			
Input sensitivity:	200 mV			
Input hysteresis:	50 mV			
Input impedance:	12 kΩ			
Terminal resistor:	120 Ω (not supplied)			
Max. input frequency:	CPCIs-1711: 5 MHz (at nominal voltage)			
	up to 20 MHz on request!			
Mass-related inputs, 24 V (E	, F, G):			
Number of inputs:	12			
NI 1 L	241/07			

	Number of Inputs:	12	
	Nominal voltage:	24 VDC	
	Logic input levels:	Unominal:	24 V
		UH max.:	30 V
		UH min.:	19 V
		UL max.:	14 V
		UL min.:	0 V
	Maximal input frequency:	1 MHz (at nominal	voltage) depending on the function

Outputs

outputo		
	Nominal voltage:	3.3 VDC
	Maximum output frequency:	5 MHz (diff. outputs)
	Max. number of outputs:	16 (if they are not used as diff. inputs)
	Digital outputs, 24 V (H)	
	Output type:	High-side (load to ground)
	Number of outputs:	4
	Nominal voltage:	24 VDC
	Supply voltage range:	4.75 V to 35 VDC (via 24 V ext. pin)
	Maximum current:	90 mA per output /
		270 mA total current limit (PTC)
	Overtemperature:	165 °C (all outputs switch off)

Technical data CPCIs-1711-24 V version

	24 V inputs (Channels A, B, C, D). This board version is intended for the connection of 24 V encoders. Only 24 V signals can be connected to the inputs.		
Nominal voltage:	24 VDC		
Max. input frequency:	1 MHz (at nomi	nal voltage) depending on the function	
Logic input levels :	Unominal:	24 V	
(Standard)	UH max.:	30 V	
	UH min.:	18 V	
	UL max.:	16 V	
	UL min.:	0 V	
All functions using port A B C I	ot he used		

See the manuals of the functions!

Safety

Optical isolation: 1000 V

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

PC system requirements and environmental conditions

Dimensions:	3U/4TE	
System bus:	PCI Express according	
	CompactPCI Serial specification PICMG CPCI-S.0 R1.0	
Space required:	1 x CompactPCI Serial slot for digital I/O	
	1 x slot opening for TTL I/O with FB1711	
Operating voltage:	+12 V, ±5 %	
Current consumption:	in preparation	
Front connector:	78-pin D-Sub female connector	
Additional connector:	50-pin D-Sub male connector	
Temperature range:	from – 40 °C to + 85 °C	
MTBF	in preparation	

Ordering information

CPCIs-1711 Multifunction counter board, optically isolated, fast counter inputs – programmable functionality, for CompactPCI Serial. Incl. technical description and software drivers. CPCIs-1711: Multifunction counter board, optical isolated, RS422 CPCIs-1711-24V: 24 V instead of RS422 (A, B, C, D). CPCIs-1711-5V-I: 5 V inputs (E, F, G) instead of 24 V Option Outputs (H1, H2, H3, H4) 5 V instead of 24 V Opt. 5V: Accessories

PX8001: 3-row screw terminal panel with housing for DIN rail ST1711-50: Standard round cable, shielded, twisted pairs, 2 m, 78-pin male connector to 50-pin male connector

For the TTL I/O function ST370-16: Standard round cable, shielded, twisted pairs, 2 m FB1711: Ribbon cable

* Preliminary product information ADDI-DATA SPIRIT OF EXCELLENCE

Multifunction board, optically isolated, 16 SE/8 differential inputs, 4/8 analog outputs, 16-bit





Also for EXPRESS See APCIe-3121, page 134

Also for PCI see APCI-3120, page 180 Also for **CommactPCI** see CPCI-3120, page 238







LabVIEW



Features

Analog inputs

- 16 single-ended/8 differential inputs
- 16-bit resolution
- Optical isolation 500 V
 - Throughput: 100 kHz
- Input ranges: 0-10 V, ±10 V, 0-5 V, ±5 V, 0-2 V, ±2 V, 0-1 V, ±1 V, 0-20 mA (option) freely programmable through software for each channel
- Gain PGA x1, x2, x5, x10 freely programmable through software for each channel
- DMA for analog data acquisition
- Overvoltage protection
- Input filters: 159 kHz
- Analog acquisition
- One single channel, several channels, several channels through scan list
- · Automatic analog acquisition through cyclic timer control
- Acquisition through scan list: up to 16 entries with gain, channel, unipolar/bipolar
- Acquisition triggered through software, timer, external event
- Software trigger or external trigger: the analog acquisition (single or
- sequence) is started through signal switching from 0 V to 24 V at the digital input 0.
- Interrupt: end of single channel, end of multichannel, end of scan list
- Analog outputs 4 or 8 analog outputs, optically isolated 500 V
- Voltage or current outputs
- 16-bit resolution (15-bit for 0-10 V)
- Output voltage: ±10 V, 0-10 V (through software)
- Output voltage after reset: 0 V
- Each output has its own ground line (without optical isolation)
- Output current ±5 mA max. for voltage outputs
- Current outputs: 0-20 mA, min. load 10 Ω , max. load 560 Ω at 20 mA
- EMI filters

Digital

- 4 dig. inputs including 1 interruptible input
- 4 dig. outputs, 24 V, optically isolated

* Preliminary product information

CPCIs-3121

CompactPCI Serial interface

16 single-ended/8 differential inputs, 16-bit

8/4 analog outputs, 16-bit

Optical isolation of inputs and outputs, 500 V

PCI-Express DMA, programmable gain

Trigger functions

8 digital I/O, 24 V, isolated, timer, watchdog

Extended temperature range - 40 °C to + 85 °C

Timer

2 timers, incl. 1 which can be used as a watchdog

Safety features

- Optical isolation 500 V min.
- Creeping distance IEC 61010-1
- Overvoltage protection ± 40 V, analog inputs
- Protection against high-frequency EMI
- Input filters: 159 kHz
- Noise neutralisation of the PC supply

Applications

- Industrial process control
- Industrial measurement and monitoring
- Multichannel data acquisition
- Control of chemical processes
- Factory automation ٠
- Acquisition of sensor data, current measurement
- Laboratory equipment, instrumentation

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

- .NET
- Microsoft VC++ Borland C++
- Visual Basic Delphi
- LabVIEW LabWindows/CVI

ADDIPACK functions

- Analog input Analog output Digital input
- Digital output Watchdog Timer

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads



info@addi-data.com www.addi-data.com

Trigger functions:



compactPCI® Serial

CompactPCI Serial

Specifications Simplified block diagram Analog inputs 6-pin male connecto Number of inputs: 16 single-ended/8 differential inputs or 8 single-ended/4 differential inputs DC/DC converter Opto-couplers A/D MUX → Gain -Resolution: 16-bit converte 500 V through opto-couplers from PC to peripheral software-programmable for each channel Î 16 SE/8 diff. Optical isolation Optical isolation: pass • Input ranges: or 8 SE / 4 diff. filters 0-10 V, ± 10 V, 0-5 V, ± 5 V, 0-2 V, ± 2 V, 0-1 V, ± 1 V, input uplers FPGA (0-20 mA optional). ntrol loai freely programmable through software for each channel 4 or 8 Sequ. RAM FIFO Timer werter M analo 100 kHz Throughput: outputs Software programmable (1, 2, 5, 10) Gain[.] \pm 2 LSB max. (A/D converter) \pm 1 LSB max. (A/D converter) Relative precision (INL): CPCIs controller 37-pol D-Sub Diff. non-linearity (DNL): Bandwidth (- 3 dB): Limited to 159 kHz with low-pass filter Trigger: Through software, timer, external event (24 V input) Data to the PC through FIFO memory Data transfe I/O commands, interrupt at EOC (End Of Conversion) Pin assignment – 37-pin D-Sub male connector and EOS (End Of Sequence), DMA transfer at EOC Interrupts: End of conversion, at timer overrun, End of sequence (+) An. input 8 (+) An. input 9 +) An. input 10 +) An. input 1 (+) An. input 4 (+) An. input 5 (+) An. input 6 (+) An. input 7 (-) An. input 7 (-) An. input 6 (-) An. input 4 Analog outputs Number of outputs: 8 or 4 Resolution: 16-bit 500 V through opto-couplers 0-10 V, ±10 V switchable through software Optical isolation: Output range: (0-20 mA optional) Overvoltage protection ±15 V Max. output current / load: Short-circuit current: ± 5 mA, 2 k Ω ±35 mA (short time) Output voltage after reset: 1: The analog inputs have a common ground lin 2: Each analog output has its own ground line Digital I/O 4 dig. inputs, 4 dig. high-side outputs, 24 V 1000 V through opto-couplers Number of I/O channels: Pin assignment – 16-pin male connector Optical isolation: Input current at 24 V 10 mA typ. 0-30 V Dig. input 3-16 🔳 🔳 15 Dig. input 3+ Input range: Supply voltage: Max. switching current: Dia. input 2+ 8-32 V Dia. input 2-14 🔳 🗖 13 Dig. input 1-Dig. input 1+ 2 🔳 🔳 65 mA typ. Dig. input 0+ Dig. input 0-10 🔳 🔳 9 24 V voltage supply High-side output 3 (24 V) EMC – Electromagnetic compatibility 8 🔳 🔳 24 V voltage supply High-side output 2 (24 V) 6 🔳 🖬 5 The product complies with the European EMC directive. The tests were carried out by Masse (dig. output) High-side output 1 (24 V) a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC Masse (dig. output) 2 🔳 🖬 1 High-side output 0 (24 V) 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request. **ADDI-DATA** connection Physical and environmental conditions Dimensions: 3U/4TE PX901-ZG System bus: PICMG CPCI-S.0 R1.0 ST010/ST011 1 x CompactPCI slot for analog I/O Space required: FB3001 1 x slot opening for digital I/O with FB3001 Operating voltage +12 V, ±5 % Digital I/O Current consumption: in preparation PX_BNC 37-pin D-Sub male connector Front connector PX901-AG -20°C to +70 °C Temperature range 00000000 MTBF ST010/ST011 in preparation CPCIs-3121 D-Sub male connecto D-Sub ecto Ordering information

Version with 16 SE/8 diff. inputs, 8 analog outputs CPCIs-3121-16-8 CPCIs-3121-16-4 Version with 16 SE/8 diff. inputs, 4 analog outputs Version with 8 SE/4 diff. inputs, 8 analog outputs CPCIs-3121-8-8 CPCIs-3121-8-4 Version with 8 SE/4 diff. inputs, 4 analog outputs

Options

CPCIs-3121

Versions

Please indicate the number of channels					
Option SF: Precision filter for 1 single-ended channel					
Option DF:	Precision filter for 1 diff. channel				
Option PC:	Current input 0(4)-20 mA for 1 channel				
PC-SE: for single-ended PC-Diff: for differential					

Accessories

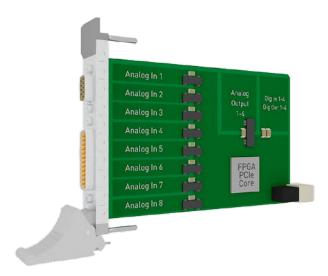
PX901-A: Screw terminal panel for connecting the analog I/O PX901-AG: Same as PX901-A with housing for DIN rail PX BNC: BNC connection box for connecting the analog I/O PX901-ZG: Screw terminal panel for connecting the dig. I/O ST010: Standard round cable, shielded, twisted pairs, 2 m ST011: Standard round cable, shielded, twisted pairs, 5 m FB3001: Ribbon cable for digital I/O with 3U bracket

* Preliminary product information



Multifunction board, opt.isolated, 16 SE/8 diff. inputs, 4/8 analog outputs, 16-bit, for CompactPCI Serial. Incl. techn. description and software drivers.

Multifunction board, optically isolated, 8 SE or 8 diff. inputs, 4 analog outputs, 24-bit



The CompactPCI Serial board CPCIs-3131 is a fast and highly-precise multifunction board. Each of the 8 inputs has an own A/D converter, the resolution is 24-bit. On the CPCIs-3131, not only the analog and digital part are optically isolated but also all analog channels are separated from each other.

Further protective circuits complete the interference resistance of the board and offer an excellent protection for your application in the harsh industrial environment. Please contact us for further information!





Features

• CompactPCI Serial (PICMG CPCI-S.0 R1.0)

Analog inputs

- 8 SE/diff. inputs, optically isolated 1000 V
- Optical isolation between channels 500 V
- 24-bit resolution
- Throughput: max. 100 kHz, programmable for each channel
- Input voltage:

PGA	unipolar	bipolar		
1	0-10 V	±10 V		
10	0-1 V	±1 V		
100	0-0.1 V	±0.1 V		
1000	0-0.01 V	±0.01 V		

- Current inputs: 0-20 mA, software-programmable for each channel
- Gain PGA x1, x10, x100, x1000 software-programmable for each channel

Analog acquisition

- Different acquisition modes are available:
 - 1) Simple Mode, 2) Scan Mode
 - 3) Sequence Mode 4) Auto Refresh Mode
- Onboard FIFO
- PCI-Express DMA for analog data acquisition
- MSI interrupt

Analog outputs

- Simultaneous output through DMA
- 4 analog outputs, optically isolated
- 16-bit resolution, setup time 18 µs max. (voltage in 10 V steps)
- Output voltage after reset: 0 V
- Each output has its own ground line (without optical

IN DEVELOPMENT*

CPCIs-3131-8-4

CompactPCI Serial interface				
8 SE or 8 differential inputs				
24-bit resolution, 250 kHz				
4 analog outputs, 16-bit				
8 digital I/O, optically isolated, 24 V				
Extended temperature range				

isolation)

- Output voltage range: 0-10 V, ±10 V, 0-5 V, ±5 V
 - 0-20 mA, 4-20 mA, 0-24 mA
- Output current: ±20 mA
- Short-circuit current: in preparation

24 V digital I/O

- 4 digital inputs, 24 V, optically isolated
- 4 digital outputs, 24 V, optically isolated

Timer / Counter / Watchdog

• 3/3/2, 16-bit

Safety features

- Optical isolation 1000 V min.
- Optical isolation between analog inputs: 500 V
- Creeping distance IEC 61010-1
- Circuit part of the analog acquisition is separated from the circuit part of the digital function
- Overvoltage protection
- Protection against high-frequency EMI
- Input filters
- Noise neutralisation of the PC supply
- Connection of the I/O signals via robust industrystandard D-Sub connector

Software

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

On request:

Further operating systems, compilers and samples

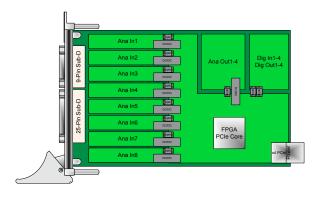
Driver download: www.addi-data.com/downloads



* Preliminary

product information

Simplified block diagram



Specifications*

Analog inputs

8 differential inputs						
24-bit						
1000 V thro	ough opto-couple	ers from PC to per	ipheral			
Each chann	Each channel is freely programmable through softwar					
PGA	PGA unipolar bipolar					
1	0-10 V	±10 V				
10	0-1 V	±1 V				
100	0-0,1 V	±0.1 V				
1000	0-0.01 V	±0.01 V				
0–20 mA (o	option)					
through so	ftware, timer, ext	. event (24 V inpu	it)			
Data to the	Data to the PC through FIFO memory,					
Interrupt at	Interrupt at EOC (End Of Conversion)					
DMA trans	DMA transfer at EOC					
End of conversion, end of timer, end of sequence						
4						
16-bit						
1000 V thro	ough opto-couple	rs				
uts						
		0-24 mA				
in preparation						
in preparat						
	24-bit 1000 V thro 500 V betw Each chann PGA 1 10 100 1000 0-20 mA (c max. 250 k through so Data to the Interrupt at DMA trans ¹ End of com 4 16-bit 1000 V thro 0-10 V, ±10 Option: 0-2 in preparat 13,6-bit for 14-bit for c in preparat Output volt Output volt	24-bit 1000 V through opto-couple 500 V between channels Each channel is freely progr. PGA unipolar 1 0-10 V 10 0-1 V 1000 -0.01 V 1000 0-0.1 V 0-20 mA (option) max. 250 kHz, software-pro through software, timer, ext Data to the PC through FIFC Interrupt at EOC (End Of Co DMA transfer at EOC End of conversion, end of til 1000 V through opto-couple 4 16-bit 1000 V through opto-couple 13, 6-bit for voltage outputs 14-bit for current outputs 13, 6-bit for voltage outputs 14-bit for current outputs 1000 V through opto-couple	24-bit 1000 V through opto-couplers from PC to per 500 V between channels Each channel is freely programmable through PGA unipolar 1 0-10 V 10 0-1 V 100 0-0.1 V ±0.01 V 0 0-0.1 V ±0.01 V 0 0-0.1 V ±0.1 V 1000 0-0.01 V ±0.01 V 0 0-20 mA (option) max. 250 kHz, software-programmable for earthrough software, timer, ext. event (24 V inpu Data to the PC through FIFO memory, Interrupt at EOC (End Of Conversion) DMA transfer at EOC End of conversion, end of timer, end of seque 4 16-bit 1000 V through opto-couplers uts 0 0 13,6-bit for voltage outputs 14-bit for current outputs in preparation 13,6-bit for voltage outputs 14-bit for			

Digital I/O	
Number of inputs:	4 digital inputs, 24 V 1 input is programmable as counter input
Number of outputs:	4 digital outputs (50 mA), 24 V
Input range:	0-30 V – logic "0": 0-14 V; logic "1": 19-30 V
Optical isolation:	1000 V through opto-couplers from PC to peripheral

EMC - Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and environmental conditions

Dimensions:	3U/4TE
System bus:	PCI Express nach CompactPCI Serial Specification
	PICMG CPCI-S.0 R1.0
Space required:	1 CompactPCI Serial slot for analog inputs,
	1 slot opening for digital I/O with FB300x
Operating voltage:	+ 12 V, ± 5 %
Current consumption:	in preparation
Front connector:	25-pin D-Sub male connector (analog input)
	9-pin D-Sub male connector (analog output)
Additional connector:	50-pin D-Sub male connector for 8 digital I/O through
	ribbon cable FB300x
Temperature range:	from - 40 °C to 85 °C
MTBF:	in preparation

Ordering information

CPCIs-3131-8-4

Multifunction board, optically isolated, 8 SE or 8 diff. inputs, 4 analog outputs, 24-bit. Technical description, software drivers and monitoring program included.

Accessories

FB300x: Ribbon cable for digital I/O



COMPACT PCI BOARDS







HIGH PROTECTION

- Optical isolation from 500 V to 1000 V
- Separation of analog and digital signals
- Protection against short-circuits, overtemperature, overvoltage
- Filters for the inputs and outputs
- Industry-standard D-Sub connectors

Reliable and available in the long term!

The CompactPCI bus is used particularly in applications with vibrations and shocks. To assure the reliability and longevity of a CompactPCI system it is important to use interference-free CompactPCI boards which are available in the long term – like the CompactPCI boards by ADDI-DATA.

High interference resistance

The key to the high interference resistance of the ADDI-DATA CompactPCI boards is the well thought-out concept of design and protective circuits. Therefore our boards are predestined for tasks in harsh industrial environments. They are resistant to vibrations, accelerations or dirt and provide reliable and accurate data.

3 U version

ADDI-DATA CompactPCI boards are available in 3 U version. 6 U brackets enable an installation in a 6 U rack. The 3 U version has been chosen because it is much more stable than longer boards. Thus the CompactPCI boards are more resistant to shocks and vibrations.

Faster through FPGA

A FPGA component has a programmable logic on which you can save your own algorithms in order to adapt the functionality of the PC board to your requirements. This adaptation makes your PC board unique and improves the performance of your applications. The onboard algorithms reduce the cycle time of signal acquisition and regulation tasks. Most ADDI-DATA CompactPCI boards are equipped with a FPGA component. Use the full potential of your PC board hardware and software resources and thus accelerate your processes.

Complete real-time system

- Combination of the PAC-system MSX-Box-CPCI and CompactPCI boards
- Compact and flexible
- Stand-alone system (own CPU)
- Long-term availability

Information about the MSX-Box-CPCI on page 20







	Dig	ital	Counter		Analog		Serial interfaces (base board)	Motion control
	CPCI-1500	CPCI-1564	CPCI-1710	CPCI-3009	CPCI-3120	CPCI-3001	CPCI-7500	CPCI-8004
32-bit CompactPCI bus	5 V	3,3 V / 5 V	5 V	3.3 V / 5 V	3.3 V / 5 V	3.3 V / 5 V	5 V	3.3 V / 5 V
FPGA		1	1	1	1	1		
Filters and protective circuits	1	1	1	1	1	1	1	1
Optical isolation 1000 V	1	1	1	1	1	1	optional	1
Digital, 24 V								
Input channels, 24 V	16	32	12 (depending on function)	4	4	4		24
Output channels, 24 V	16	32	4	4	4	4		12
Output current per output	500 mA (typ.)	500 mA (typ.)	500 mA (typ.)					
Watchdog / Timer / Counter	2 x 12-bit timer, incl. 1 which can be used as a watchdog	Timer (12-Bit)/ Watchdog (8-Bit)		16-bit 3/3/2	1 x 24-bit timer which can be used as a watchdog	1 x 24-bit timer which can be used as a watchdog		
Counter			•					
Function modules			4	1				
Functions Incremental counter, SSI synchronous serial interface, counter/timer, pulse acquisition, frequency, pulse width, Pe- riod duration, velocity measurement, PWM, BiSS master, digital inputs and outputs,			reprogramm- able	reprogramm- able				4 incremental counters or SS
Input frequency			up to 5 MHz	up to 5 MHz				
Signals			TTL, RS422, 24 V	TTL, RS422, 24 V				
Analog						1		
Analog inputs, 16-bit				16 SE / 8 diff.	16/8 SE or 8/4 diff.	16/8 SE or 8/4 diff.		
Throughput (kHz)				100	100	100		
Voltage range				0-10 V ± 10 V	0-10 V ± 10 V	0-10 V ± 10 V		
Gain 1, 2, 5, 10								
FIFO (value)								
Trigger (software or 24 V)				1	1	1		
Sequence RAM				1	1	1		
Analog outputs				4, 12-bit	8 or 4, 14-bit			4, 16-bit
0-10 V ± 10 V				1	1			
Settling time				15 µs	30 µs			
Serial interfaces (base board)							4-port	
Configuration of the operation mode through MX modules							RS232, RS422, RS485, RS485, 20 mA CL	
Motion Control								1 to 4 servo o stepper motor
Software	Current driver lis	t on the web: ww	w.addi-data.com	n		1		
Page	230	232	234	236	238	240	242	244



CompactPCI™

Digital I/O board, optically isolated, 32 digital inputs and outputs, 24 V





CompactPCI™ 32-bit

Also for PCI see APCIe-1532 EXPRESS[®] page 118

Also for PCI See APCI-1500 page 146

> **URS-1500-6U** 6U bracket









LabVIEW™ LabWindows/CVI™





Features

- Can be inserted in PXI systems, with restricted functionality
- 3 software-programmable timers
- Connector and software compatible to digital I/O boards APCI-1500/PA 1500
- Monitoring program for testing and setting the board functions

Inputs

- 16 optically isolated digital inputs, 24 V, including 14 interruptible inputs
- Reverse voltage protection
- All inputs are filtered

Outputs

- 16 optically isolated digital outputs, 10 V to 36 V
- Output current per channel 500 mA
- Timer programmable watchdog for resetting the outputs to "0"
- Diagnostic report through status register at shortcircuits, overtemperature, voltage drop or watchdog
- Interrupt triggered through watchdog, timer, error
- At Power-On, the outputs are reset to "0"
 Short-circuit current for 16 outputs ~ 3 A typ.
- Short-circuit current for 10 outputs ~ 3 A typ.
 Short-circuit current per output ~1.5 A typ.
- Self-resetting fuse (electronic fuse)
- Overtemperature and overvoltage protection
- 24 V power outputs with protection diodes and filters
- Output capacitors against electromagnetic emissions
- Ext. 24 V voltage supply screened and filtered
- Shutdown logic, when the external supply voltage drops below 5 V
- Programmable watchdog for resetting the outputs in case of error

Safety features

- Optical isolation 1000 V
- Creeping distance IEC 61010-1
- Protection against fast transients (burst), overvoltage,
- electrostatic discharge and high-frequency EMI
- Separate ground line for inputs and outputs

CPCI-1500

16 digital inputs, 24 V,

including 14 interruptible

16 digital outputs, 24 V, 500 mA/channel

Optical isolation 1000 V

Input and output filters

The outputs are reset to "0" at Power-On

MTBF: 85 150 hours at 45 °C

Timer, watchdog

Applications

- Industrial I/O control
- PLC coupling
- Acquisition of encoder data for process control
- Signal switching
- Interface to electromechanical relays
- ON/OFF monitoring of motors, lights...
- Watchdog timer
- Machine interfacing
- ...

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request
- RTX drivers (real-time)

Drivers and samples for the following compilers and software packages:

- Microsoft VC++
 Microsoft C
- Borland C++

 Borland C
 Borland C
 Borland C
 Borland C
- Visual Basic Delphi
- LabVIEW LabWindows/CVI DASYLab DIAdem

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads



1847-0 info@addi-data.com 1847-222 www.addi-data.com

• Di cir

CompactPCI^{III}

Specifications

Digital inputs

Jigital inputs			
Number of inputs:	16 (common ground acc. to IEC 1131-2)		
Interruptible inputs:	14 out of 16 digital inputs		
	IRQ line selected through BIOS		
Optical isolation:	Through opto-couplers, 1000 V		
	from PC to peripheral		
Interrupt compare logic:	AND and OR mode; OR priority		
Nominal voltage:	24 V		
Input current at 24 V:	6 mA typ.		
Logic input levels:			
U nominal:	24 V		
UH max.:	30 V/current 9 mA typ.		
UH min.:	19 V/current 2 mA typ.		
UL max.:	14 V/current 0.7 mA typ.		
UL min.:	0 V/current 0 mA typ.		
Signal delay:	70 μs (at 24 V)		
Maximal input frequency:	5 kHz (at 24 V)		

Digital outputs

· ·	
Number of outputs:	16
Optical isolation:	Through opto-couplers, 1000 V
Output type.:	High-side (load to ground) acc. to IEC 1131-2
Nominal voltage:	24 V
Supply voltage:	10 V to 36 V, min. 5 V (via front connector)
Max. current for 16 outputs:	3 A typ.
Output current/output:	500 mA typ.
Short-circuit current/output	
shutdown at 24 V, $R_{load} < 0.1 \Omega$:	1.5 A
RDS ON resistance:	0.4Ω m1ax.
Switch-on time:	l out=0.5 A, load = resistance: 120 μs
Switch-off time:	l out=0.5 A, load = resistance: 60 μs
Overtemperature:	170 °C (output driver)
Temperature hysteresis:	20 °C (output driver)

Safety

Shutdown logic:	When the ext. 24 V voltage drops below 5 V: The outputs are switched off.
Diagnostics:	Short-circuits, overtemperature, status bit or interrupt to the PC.
Timer:	3 (max. 10 kHz, 24 V)
Watchdog:	Timer programmable, 17 μs up to 36 s for switching off the outputs

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and environmental conditions

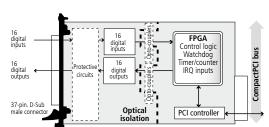
Dimensions:	3U/4TE
System bus:	CPCI 32-bit acc. to specification CompactPCI 2.1
Space required:	1 CPCI slot 3U
Operating voltage:	$+5 V_{2} \pm 5 \%$, from the PC
Current consumption:	220 mA typ. ± 10 %
Front connector:	37-pin D-Sub male connector
Temperature range:	0 to 60 °C (with forced cooling)
MTBF:	85 150 hours at 45 °C

CPCI-1500

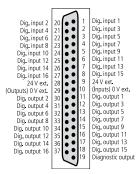
Digital I/O board, 32 digital inputs and outputs, optically isolated, 24 V. Incl. technical description, software drivers and monitoring program.

Option URS-1500-6U: Accessories	6U bracket for mounting in 6U housing	PX8500-G: ST010:	Relay output board for DIN rail, cascadable Standard round cable, shielded, twisted pairs, 2 m
PX901-D: PX901-DG:	Screw terminal panel, LED status display Screw terminal panel, LED status display, for DIN rail	ST011: ST010-S: ST021:	Standard round cable, shielded, twisted pairs, 5 m Same as ST010, for high currents (24 V supply separate) Round cable between CPCI-1500 and PX8500, shielded, twisted pairs, 2 m
PX9000:	3-row screw terminal panel, for DIN rail, LED status display	ST022: ST8500:	Round cable between PX8500 and PX901, shielded, 2 m Ribbon cable for cascading two PX8500

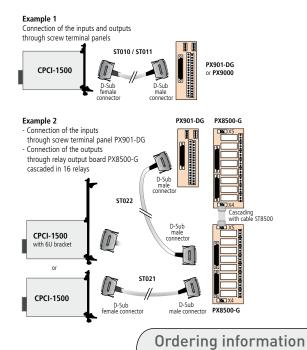
Simplified block diagram



Pin assignment – 37-pin D-Sub male connector



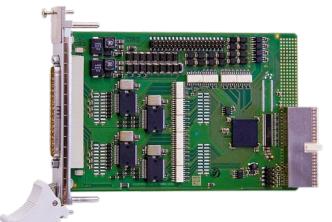
ADDI-DATA connection





Digital I/O board, optically isolated, 64 digital inputs and outputs, 24 V





Features

Inputs

Outputs

٠

CompactPCI 3,3 V or 5V



CompactPCI™ 32-bit

Also for PCI >>> see EXPRESS[®] page 122 see

Also for **PC** See page 150











on request

* Preliminary product information

Phone: +49 7229 1847-0 232 +49 7229 1847-222 Fax:

info@addi-data.com www.addi-data.com

CPCI-1564

32 digital inputs, 24 V, including 16 interruptible inputs, filtered

32 digital outputs, 24 V, 500 mA/channel, filtered

Optical isolation 1000 V

Watchdog, timer, 3 x 32-bit counter up to 500 kHz

The outputs are reset to "0" at Power-On

Applications

- Industrial I/O control PLC coupling
- Signal switching
- Interface to electromechanical relays
- Automatic test equipment
- ON/OFF monitoring of motors, lights...
- Watchdog Machine interfacing
- ...

Software drivers

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

• C#.NET, C

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads





At Power-On, the outputs are reset to "0" Total current for 8 outputs 1.85 A Short-circuit current per output max. 1.7 A

• Overtemperature and overvoltage protection

• 32 optically isolated digital outputs, 11 V to 36 V

24 V power outputs with protection diodes and filters

32 optically isolated digital inputs, 24 V, including

16 interruptible and 3 counter inputs

each group has its own ground line

Output current per channel 500 mA

Watchdog for resetting the outputs to "0"

Reverse voltage protection

• All inputs are filtered

• Inputs organised in 4 groups of 8 channels,

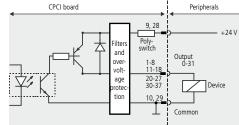
- Output capacitors against electromagnetic emissions
- Ext. 24 V voltage supply screened and filtered
- Shutdown logic, when the external supply voltage drops below 5 V

Safety features

Electronic fuse

- Optical isolation 1000 V
- Creeping distance IEC 61010-1
- Protection against fast transients (burst), overvoltage, electrostatic discharge and high-frequency EMI
- Interrupt started through counter, timer
- Separate ground lines for inputs and outputs

Connection principle of the outputs



•

0-31

Outputs

CompactPCI bus

24V)

PX901-DG

CompactPCI[™]

Output FPGA 0-7 logic IRQ logic Watchdog Output 8-15 Counter/ Timer Output Opto 16-23 Output PCI interfac 24-31 assignment - 37-pin D-Sub male connector Inputs

Simplified block diagram

Ribbon cable supplied with the board, 40-pin to 37-pin D-Sub connector

ADDI-DATA connection

Example 1:

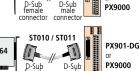
- Connection of the inputs (ribbon cable) - Connection of the outputs through screw terminal panel PX901-DG or PX9000

10] // -Sub D-Sub male Ribbon cable D-fei delivered connec with the board CPCI-1564 () ļ 40-pin D-Sub female D-Sub male male connector

ST022 D-Sub male

CPCI-1564

11 ST010 / ST 1Th



ST010 / ST011

Example 2:

Connection of the outputs through relay output board PX8500-G cascaded in 32 relays

Ordering information

CPCI-1564

Digital I/O board, 64 digital I/O, optically isolated, 24 V. Incl. technical description, software drivers

Accessories

Accessories			
URS-1564-6U:	6U bracket for mounting in 6U housing	ST011:	Standard round cable, shielded, twisted pairs, 5 m
PX901-D:	Screw terminal panel	ST010-S:	Same as ST010, for high currents (24 V supply separate)
PX901-DG:	Screw terminal panel for DIN rail	ST022:	Standard round cable between PX8500 and PX901,
PX9000:	3-row screw terminal panel		shielded, 2 m
PX8500-G:	Relay output board for DIN rail, cascadable	ST8500:	Ribbon cable for cascading two PX8500
ST010:	Standard round cable, shielded, twisted pairs, 2 m		

* Preliminary product information



Specifications*			
Digital inputs			
Number of inputs:	_ 32 digital		
Optical isolation:	Through opto-coupler	s, 1000 V	inputs ·
Nominal voltage 24 V			· · · ·
(CPCI-1564):	Digital inputs	Counter inputs	
Input current at 24 V:	4 mA typ.	10,5 mA typ.	8 digital
Logic input levels:			- outputs
UH max.:	30 V		8 digital 🖌
UH min.:	19 V		outputs
UL max.:	14 V		8 digital 🖌
UL min.:	0 V		outputs
Digital outputs			8 digital outputs
Number of outputs:	32, optically isolated	up to 1000 V	37-pin D-Sub
Output type:		und) acc. to IEC 1131-2	connector
Nominal voltage:	24 V (CPCI-1564); or 5	V (CPCI-1564-5V)	
Supply voltage:	Pin ass		
Max. current for 8 outputs:	1.85 A typ.		
Output current/output:	500 mA max.		-
Short-circuit current/output			-
-hutdaum -+ 2437 D 10			

shutdown at 24 V, $R_{load} = 10 \text{ m}\Omega$: max. 1.7 A RDS ON resistance: 150 mΩ typ. 40 µs typ. Switch-on time: 470 μs typ. 130 °C (output driver) Switch-off time: Overtemperature (shutdown): Temperature hysteresis: 15 °C (output driver) Safety Shutdown logic: When the ext. 24 V voltage drops below 5 V: The outputs are switched off. Pin 19: status bit or interrupt to the PC Diagnostics: Timer 12-bit

EMC – Electromagnetic compatibility

Watchdog:

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

in steps of 20 ms

8-bit, timer-programmable from 20 ms to 5 s

Physical and environmental conditions			
Dimensions:	160 x 100 mm		
System bus:	CPCI 32-bit 5 V acc. to specification CompactPCI 2.1		
Space required:	1 CPCI slot 3U (only at 3HE)		
Operating voltage:	+5 V, ± 5 % from CPCI system		
Current consumption:	395 mA \pm 15 mA typ.		
Front connector:	37-pin D-Sub male connector for 32 dig. outputs		
	37-pin D-Sub male connector for 32 dig. inputs		
	(only 6HE)		
Additional connector:	37-pin D-Sub male connector on separate		
	bracket for 32 digital inputs (only 3HE)		
Temperature range:	-40 °C to +85 °C (with forced cooling)		







CompactPCI™ 32-bit

Also for see PC >>>> see EXPRESS[®] page 128

Also for **PC** see page 166

URS-1710-6U 6U bracket



Description of the functions see datasheet of the APCI-1710 page 166







The board CPCI-1710 is a fast multifunction and multichannel counter board for the CompactPCI bus. The strengths of this board are its wide range of applications and high precision, speed and reliability for though industrial applications. With this board you can realise many different applications on the same hardware base. The board is supplied with a pool of functions which are individually configured for each channel through the supplied software. The flexible programming facilities on this board allow many different user applications to be quickly and easily developed or reconfigured as further requirements arise. Thanks to the FPGA board structure, further counting applications can be realised through software adaptation. Contact us!

Features

- Can be inserted in PXI systems, with restricted functionality
- 32-bit data access
- Counter component with 32-bit counting depth and 5 MHz counting frequency
- Signals in TTL or RS422 mode, 24 V signals optional
- Four onboard function modules
- Reprogrammable functions

Functions (detailed description see APCI-1710)

- Acquisition of incremental encoders (90° phase-shifted signals)
- Synchronous serial interface for systems allowing an absolute position information through serial data transfer
- Counter/timer (82C54)
- Pulse acquisition
- Frequency measurement
- Pulse width modulation / PWM
- Period duration measurement
- Velocity measurement
- **BiSS-Master**
- Digital inputs and outputs
- Customised functions

Available channels for all four function modules

- 20 channels for digital inputs, optically isolated
- 8 channels, programmable either as digital inputs or outputs, optically isolated
- 4 digital power outputs, optically isolated

CPCI-1710

Incremental counter, SSI synchronous serial interfaces, counter/timer, pulse acquisition, frequency, pulse width, period duration, velocity measurement, PWM, BiSS-Master, digital inputs and outputs, ...

Function selection through software

Optical isolation, MTBF: 54 287 hours at 45 °C

TTL. RS422. 24 V

Customised functions

Available lines for each function module

- 8 lines are available for each function module
- Input lines:
 - 2 x TTL and RS422 (CPCI-1710) or 2 x 24 V (option)
 - 3 x 24 V, optional 5 V for channels E, F, G
- Output lines: - 1 x 24 V, optional 5 V (power output)
- 2 channels, programmable either as digital inputs or outputs, optically isolated: 2 x TTL, RS422

Safety features

- Creeping distance IEC 61010-1
- Optical isolation 1000 V
- Noise neutralisation of the PC supply

Applications

- Event counting Position acquisition
- Motion control Batch counting ...

Software

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request
- RTX drivers (real-time)

Drivers and samples for the following compilers and software packages:

- .NET
- Microsoft VC++ Microsoft C •
- Borland C++ Borland C
- Visual Basic Delphi
- LabVIEW DIAdem

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads



info@addi-data.com www.addi-data.com



Specifications

Free programming of the functions

	-	-	
			32-bit or 16-bit acquisition of incremental encoders
			Acquisition of absolute encoders/SSI
			Counter/timer
			Chronos/TOR for frequency measurement
			Pulse acquisition
			Chronos for pulse width modulation
			Chronos for period duration measurement
			TOR for velocity measurement
			BiSS-Master
			Digital I/O, 24 V, TTL, RS422
			PWM
			Customised functions

Signals

Digital I/O signals, TTL or RS422

Number of inputs:	20			
Differential inputs or out	puts			
Differential inputs, 5 V	•	8/16 (8 can be used as inputs or outputs)		
Nominal voltage:	5 VDC	• • •		
Common mode range:	+12 / -7 V			
Max. differential voltage	±12 V			
Input sensitivity:	200 mV			
Input hysteresis:	50 mV			
Input impedance:	12 kΩ			
Terminal resistor:	150 Ω serial w	ith 10 nF (typ.)		
Signal delay:	120 nS (at nom	120 nS (at nominal voltage)		
Max. input frequency:	5 MHz (at nom	5 MHz (at nominal voltage)		
Mass-related inputs, 24 V	(channels E, F, G	:		
Number of inputs:	12			
Nominal voltage:	24 VDC			
Input current				
at nominal voltage:	11 mA			
Logic input levels:	Unominal:	24 V		
	UH max.:	30 V		
	UH min.:	19 V		
	UL max.:	15 V		
	UL min.:	0 V		
Signal delay:	120 ns (at nom	inal voltage)		
Maximal input frequency:	2.5 MHz (at no	2.5 MHz (at nominal voltage)		

Outputs

outputo	
Nominal voltage:	5 VDC
Maximum output frequency:	2.5 MHz (diff. outputs)
Max. number of outputs:	8 (if they are not used as diff. inputs)
Digital outputs, 24 V:	
Output type:	High-side (load to ground)
Number of outputs:	4
Nominal voltage:	24 VDC
Range of the supply	10 V up to 36 VDC (via 24 V ext. pin)
voltage:	
Maximum current for 4 outputs:	2 A typ. (limited to the voltage supply)
Maximum output current:	500 mA
Short-circuit current/	
output at 24 V, $R_{last} < 0.1 \Omega$:	1.5 A max. (output switched off)
ON-resistance of the output	
(RDS ON-resistance):	0.4 Ω max.
Overtemperature:	170 °C (all outputs switch off)

• • • • • •	(24.14		
Overtemperature protection			
Activated:	From approx. 150-170 °C (chip temperature)		
Deactivated (automatically).:	From approx. 125-140 °C (chip temperature)		
Outputs (at overtemperature):	Outputs switch off		
Protection against undervoltage	(effective at Vext <5 V):		
Outputs (at undervoltage):	All outputs switch o	ff	
Switching characteristics of			
(Vext = 24 V, T=25 °C, ohmic load	d: 500 mA) :		
Switch ON time:	200 µs		
Switch OFF time:	15 µs		
Digital outputs, 5 V (option):			
Output type:	TTL		
Number of outputs:	4		
Nominal voltage:	5 VDC		
Switching characteristics of	the outputs		
(T=25 °C, TTL load):			
Switch ON time:	0.06 µs		
Switch OFF time:	0.02 µs		
Technical data for the option 24 V			
24 V inputs (channels A up to D).			
This board version is intended for the connection		intended for the connection	
	of 24 V encoders. Only 24 V signals can be connected to		
	the input channels.		
Nominal voltage:	24 VDC / 10 mA		
Max. input frequency:	1 MHz (at nominal v	voltage)	
Logic input levels :	Unominal:	24 V	
(Standard)	UH max.:	25 V	
	UH min.:	15 V	
	UL max.:	11 V	
	UL min.:	0 V	
Safety			

Optical isolation:

EMC – Electromagnetic compatibility

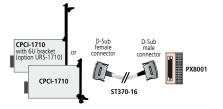
1000 V

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and environmental conditions

Dimensions:	3U/4TE
System bus:	CompactPCI 32-bit 5 V acc. to spec. 2.1 (PCISIG)
Space required:	1 slot
Operating voltage:	+5 V, ± 5 % from the PC
	+24 V ext. /10 mA
Current consumption:	CPCI-1710: 877 mA typ. ± 10 %
Front connector:	50-pin D-Sub male connector
Temperature range:	0 to 60 °C (with forced cooling)
MTBF:	54 287 hours at 45 °C

ADDI-DATA connection



Ordering information

CPCI-1710: CPCI-1710-10K20: MX1710:	Multifunction counter board, optically isolated, encoder, incremental counter, timer/counter, SSI, PWM. Incl. technical description and software drivers. Same as CPCI-1710, with additional function for connecting a BiSS interface. Peripheral module for the board CPCI-1710. 2 modules are necessary for each CPCI-1710 board. Please order with the board!
Ontions	Option 5 V 24 V inputs are supplied with 5 V (chappels F_E_G)

Options

URS-1710-6U:	6U bracket for mounting in 6U housing		
Option 24 V:	24 V for differential inputs	Accessories	
	(channels) (up to d, / tand b for counter), (index) and	ST370-16: PX8001:	Shielded round cable, 2 m
	UAS (error) signals	PA8001:	3-row screw terminal panel, 50-pin, for DIN-rail mounting



Multifunction board, optically isolated, 16 SE or 8 diff. inputs, 4 analog outputs, 16-bit





CompactPCI™ 32-bit





The board CPCI-3009 is a fast multifunction and counter board for the CompactPCI bus. It is characterised by flexible applications, high accuracy, speed and reliability in severe industrial environments.

With this board you can put into practice a large range of applications on the same hardware basis thanks to FPGA technology. The board is supplied with a pool of functions allowing a high efficiency on just one board. The functions are programmed using the supplied software. You can adapt the functions of the board to the requirements of your application and change them as required. On request, further counter applications can be adapted per software thanks to the the FPGA. Contact us!

Features

- CompactPCI 3.3 V or 5 V
- Can be inserted in PXI systems, with restricted functionalities

Analog inputs

- 16 diff. inputs, optically isolated 1000 V
- Resolution: 16-bit
- Throughput: 100 kHz
- Voltage inputs: 0-10 V, ±10 V, 0-5 V, ±5 V, 0-2 V, ±2 V, 0-1 V, ±1 V, 0-20 mA (option) freely programmable through software for each channel
- Gain PGA x1, x2, x5, x10 freely programmable through software for each channel
- Version with input range 0-30 V (only SE inputs)

Analog acquisition

- Different input modes for the analog acquisition:
 - 1) Simple mode
 - 2) Scan modes
 - 3) Sequence modes
 - 4) Auto Refresh mode
- Onboard FIFO
- PCI-DMA for analog data acquisition

Analog outputs

- 4 analog outputs, optically isolated
- 12-bit resolution, setup time 15 μs typ
- Output voltage after reset: 0 V
- Each output has its own ground line (without optical isolation)
- Output voltage range: 10 V to + 10 V

* Preliminary product information

Output current: ± 5 mA
Short-circuit current: ± 20 mA

CPCI-3009

16 SE or 8 diff. inputs

16-bit resolution, 100 kHz

Voltage and current inputs (optional)

4 analog outputs, 12-bit

Reprogrammable counter function module

8 optically isolated digital I/O, 24 V

24 V digital I/O

- 4 digital inputs, 24 V, optically isolated
- 4 digital outputs, 24 V, optically isolated

Reprogrammable counter function module

- 32-bit data access
- Counter component with 32-bit width and 5 MHz counting frequency, signals in RS422 mode
- Functions:
- Incremental counter for the acquisition of incremental encoders (90° phase-shifted signals)
- Chronos for frequency, pulse width and period duration measurement
- Digital inputs and outputs, 24 V, TTL, RS422
- Further functions on request:
- SSI synchronous serial interfaces. The SSI function is an interface for systems which allow an absolute position information via serial data transfer.
- Counter/timer (82C54)
- Pulse acquisition
- Velocity measurement
- PWM (Pulse Width Modulation)
- Customised functions

Timer/Counter/Watchdog

• 3/3/2, 16-bit

Safety features

- Optical isolation 1000 V min.
- Creeping distance IEC 61010-1
- Circuit part of the analog acquisition
- is separated from the circuit part of the digital function $\bullet\,$ Overvoltage protection $\pm\,40$ V
- Protection against high-frequency EMI
- Input filters
- Noise neutralisation of the PC supply
- Connection of the I/O-signals via robust industrystandard D-Sub connector

Software

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request On request:

Further operating systems, compilers and samples. Driver download: www.addi-data.com/downloads



0 info@addi-data.com 222 www.addi-data.com

CompactPCI^{II}

Specifications

Analog inputs	
Number of inputs:	16 differential inputs, 16-bit resolution
Optical isolation:	1000 V through opto-couplers from PC to peripheral
Voltage inputs:	software-programmable for each channel
	CPCI-3009: 0-10 V, ±10 V, 0-5 V, ± 5 V, 0-2 V, ± 2 V, 0-1 V,
	± 1 V, 0-20 mA optional
	CPCI-3009_30V: 0-30V
Gain:	Software programmable (x1, x2, x5, x10)
Throughput:	100 kHz
Trigger:	Through software, timer, ext. event (24 V input)
Data transfer:	Data to the PC through FIFO memory,
	Interrupt at EOC (End Of Conversion),
	DMA transfer at EOC
Interrupts:	End of conversion, End of timer, End of scan

Analog outputs

Number of outputs:	4, 12-bit resolution
Optical isolation:	1000 V through opto-couplers
Voltage outputs	
Output range:	- 10 V to + 10 V (- 1 LSB)
LSB:	4.8828 mV
Accuracy:	11-bit
Time to read:	typ. 5 μs
Setup time:	typ. 15 μs (at 10 V step)
Max. output current:	\pm 5 mA (each output)
Short-circuit current:	max. ± 20 mA (temporary)
Output voltage after reset:	0 V

Counter components

32-bit, counting frequency up to 5 MHz
1000 V
nctions
For programming your function module
select one function from the list on the right.
Digital I/O, 24 V signals, TTL or RS422
4 dig. inputs, 4 dig. outputs (50 mA), 24 V
0-14 V
19-30 V
1000 V through opto-couplers from PC to peripheral

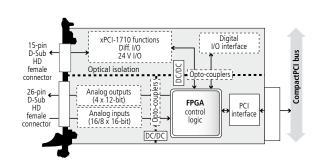
EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and environmental conditions

Dimensions:	3U/4TE
System bus:	PCI 32-bit acc. to CompactPCI specification 2.1
Space required:	1 x CompactPCI slot for analog I/O, counter
	1 x slot opening for digital I/O with FB3001
Operating voltage:	+5 V, ±5 %
Current consumption:	790 mA, ±10 %
Front connector:	26-pin D-Sub female connector (analog I/O)
	15-pin D-Sub female connector (counter module)
	Separ. 37-pin D-Sub connector for 8 dig. I/O via FB3001
Temperature range:	0 to 60 °C (with forced cooling)
	-30° up to +70° in preparation

Simplified block diagram



Reprogrammable function module allows many different applications

The function module has numerous functions which can be programmed quickly and easily. For the programming of your function module, choose one of the following functions. If your application changes, just reprogram the function module and use another function from the list below.

Select one of the following functions:

- 1 x 32-bit acquisition of incremental encoders
- 2 x 16-bit acquisition of incremental encoders
 1 x Chronos/TOR for frequency measurement
- 1 x Chronos/10k for frequency measurem
 1 x Chronos for pulse width modulation
- 1 x Chronos for period duration measurement
- 8 digital I/O, 24 V, TTL, RS422

Further functions on request:

- 3 x acquisition of absolute encoders/SSI
- 3 x counter/timer
- 4 x pulse acquisition
- 2 x TOR for velocity measurement
- 2 x PWM 2 x ETM
- 1 x SSI monitor

Accessories PX901-A: Sc

ST3009-A:

FB3001:

ST010:

For a detailed description of the functions, please see the data sheet of the board APCI-1710 on page 166

Screw terminal panel with transorb diodes

ST3009-DZ: 15-pin HD D-Sub female to 37-pin D-Sub male connector

with 37-pin D-Sub male connector on 3U bracket

Standard round cable, shielded, twisted pairs, 2 m

26-pin HD D-Sub female to 37-pin D-Sub male connector

for connecting the analog I/O **PX901-AG:** Same as PX901-A with housing for DIN rail

PX901-ZG: Screw terminal panel for connecting

the digital I/O, for DIN rail

Ribbon cable for dig. I/O,

Ordering information

CPCI-3009

Multifunction board, optically isolated, 16 SE or 8 diff. inputs, 4 analog outputs, 16-bit. Incl. technical description and software drivers.

Versions

CPCI-3009_30V: Same as CPCI-3009, only SE inputs, unipolar,
0-30 V input range

Options

237

Please specify the number of channels when ordering

 URS-3009-6U:
 6U bracket for mounting in 6U housing

 Option SF:
 Precision filter for 1 single-ended channel

 Option DF:
 Precision filter for 1 diff. channel (30Hz)

 Option PC:
 Current input 0(4)-20 mA for 1 channel

 PC-SE: For 1 single-ended channel
 PC-SE: For 1 diff. channel (30 Hz)

Option CAL3009: Only for 32-bit operation system. On-site calibration of the CPCI-3009. Do the fine adjustment fast and reliably and then save the calibration report file.



Multifunction board, optically isolated, 16 SE or 8 diff. inputs, 8 analog outputs, 16-bit





CompactPCI™ 32-bit

Also for PCI-Express See APCle-3121, page 134

Also for **PCI** See APCI-3120, page 180







LapAiew[™]



LabWindows/CVI™



Timer • 24-bit; as cyclic time counter or watchdog

CPCI-3120

16/8 single-ended or

8/4 differential inputs, 16-bit

8/4 analog outputs, 14-bit

Optical isolation of the inputs

and outputs, 500 V

Automatic analog acquisition

Output voltage after reset 0 V

MTBF: 75867 hours at 45 °C

Timer, watchdog

Safety features

- Optical isolation 500 V min.
- Creeping distance IEC 61010-1
- Overvoltage protection ± 40 V (analog inputs)
- Protection against high-frequency EMI
- Input filters: 159 kHz Noise neutralisation of the PC supply

Applications

- Industrial process control
- Industrial measurement and monitoring
- Multichannel data acquisition
- Control of chemical processes
- Factory automation
- Acquisition of sensor data, current measurement, pressure data
- Laboratory equipment, instrumentation

Software

Calibration tool (Option CAL3120): Do the fine adjustment fast and reliably and save the generated calibration report file. All you need is a highly precise calibration source and a precise digital multimeter (not included in the delivery content).

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

- Microsoft VC++
 Microsoft C
- Borland C++ Borland C
- Visual Basic Delphi
- LabVIEW LabWindows/CVI DASYLab DIAdem

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads



Phone: +49 7229 1847-0 238 +49 7229 1847-222 Fax:

info@addi-data.com www.addi-data.com

- Features
- Can be inserted in PXI systems, with restricted functionality

Analog inputs

- 16 single-ended/8 differential inputs or
- 8 single-ended/4 differential inputs 16-bit resolution
- Optical isolation 500 V
- Throughput: 100 kHz
- Input voltage: 0-10 V, ±10 V, 0-5 V, ±5 V,
- 0-2 V, ±2 V, 0-1 V, ±1 V, 0-20 mA (option) freely programmable through software for each channel
- Gain PGA x1, x2, x5, x10 freely programmable through software for each channel
- PCI DMA for analog data acquisition
- Overvoltage protection

Input filters: 159 kHz

- Analog acquisition
- Single channel, several channels, several channels through scan list
- Autom. analog acquisition through cyclic timer control
- Acquisition through scan list: up to 16 entries with gain, channel, unipolar/bipolar
- Acquisition triggered through software, timer, external event
 - Trigger functions:
 - Software trigger or
 - external trigger: the analog acquisition (single or sequence) is started through signal switching from 0 V to 24 V at the digital input 0.
- Interrupt: End of single, End of multichannel, End of scan list
- Analog outputs

 - Short-circuit protection, EMI filters
 - 4 or 8 analog outputs, optically isolated 500 V Setup time 30 µs 14-bit resolution (13-bit for 0-10 V)
 - Output voltage: ±10 V, 0-10 V (through software)
 - Output voltage after reset: 0 V
 - Each output has its own ground line
 - (without optical isolation)
 - Driver capacity: 5 mA/500 pF

Digital

• 4 dig. inputs, 4 dig. outputs, 24 V, optically isolated

CompactPCI^m

Specifications

Analog inputs	
Number of inputs:	16 single-ended/8 differential inputs or
	8 single-ended/4 differential inputs
Resolution:	16-bit resolution
Optical isolation:	500 V through opto-couplers from PC to peripheral
Input ranges:	Software-programmable for each channel
	0-10 V, ±10 V, 0-5 V, ± 5 V, 0-2 V, ± 2 V, 0-1 V, ± 1 V,
Throughput	0-20 mA optional
Throughput: Gain:	100 Kil
Common mode rejection:	Software programmable (1, 2, 5, 10) DC at 10 Hz, 90 dB minimum
Relative precision (INL):	± 1 LSB (ADC)
Diff. Non-linearity (DNL):	± 0.5 LSB (ADC)
Input impedance (PDA):	$10^{12} \Omega//10 \text{ nF single-ended},$
input impedance (i DA).	$10^{12} \Omega/20$ nF differential against GND
Bandwidth (- 3 dB):	Limited to 159 kHz with low-pass filter
Trigger:	Through software, timer, ext. event (24 V input)
Data transfer:	Data to the PC through FIFO memory, I/O commands,
	Interrupt at EOC (End Of Conversion) and EOS (End of
	Scan), DMA transfer at EOC
Interrupts:	End of conversion, End of timer, End of scan
Timer	
Time base of timer 2:	24-bit; 50 μs
Analog outputs	
	4 or 8
Number of outputs: Resolution:	4 or 8 14-bit resolution
Optical isolation:	500 V through opto-couplers
Output range:	$0.10 \text{ V}, \pm 10 \text{ V}$ switchable through software
Setup time at 2 k Ω , 1000pF:	$10 \ \mu s \ (10 \ V \ step)$
Overvoltage protection:	±12 V
Max. output current / load:	±12 v ±5 mA / 500 pF, 2 kΩ
Short-circuit current:	±25 mA
Output voltage after reset:	0 V
Digital I/O	
•	
Number of I/O channels:	4 dig. inputs, 4 dig. outputs, 24 V
Optical isolation:	1000 V through opto-couplers
Input current at 24 V:	3 mA typ.
Input range:	0-30 V
Output range:	5-30 V
Max. switching current:	10 mA typ.

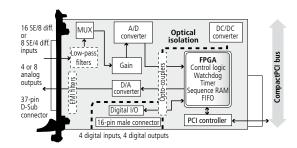
EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

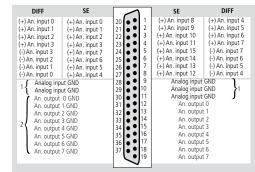
Physical and environmental conditions

Dimensions: 160 x 100 mm
System bus: PCI 32-bit acc. to CompactPCI specification 2.1
Space required: 1 PCI slot for analog I/O,
1 slot opening for digital I/O with FB3001
Operating voltage: + 5 V, ± 5 % , 3.3 V from CPCI system
Current consumption: 800 mA
Front connector: 37-pin D-Sub male connector
Additional connector : 16-pin male connector for connecting the dig. I/O
Temperature range: 0 to 60 °C (with forced cooling)
MTBF: 75867 hours at 45 °C

Simplified block diagram



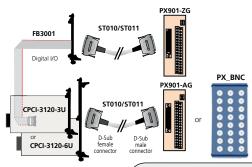
Pin assignment – 37-pin D-Sub male connector



1: The analog inputs have a common ground line 2: Each analog output has its own ground line

Pin assignment – 16-pin connector

ADDI-DATA connection



Ordering information

CPCI-3120

Multifunction board, optically isolated, 16 SE or 8 diff. inputs, 8 analog outputs, 16-bit. Incl. technical description, monitoring program and software drivers. Varcianc

ST

STO

FB3

101010		
CPCI-3120-16-4	16 SE/8 diff. inputs, 4 analog outputs	
CPCI-3120-16-8	16 SE/8 diff. inputs, 8 analog outputs	

Options: Please specify the number of channels when ordering URS-3120-6U: 6U bracket for mounting in 6U housing Precision filter for 1 single-ended channel Precision filter for 1 diff. channel (30 Hz) **Option SF:** Option DF:

Option PC: Current input 0(4)-20 mA for 1 channel

PC-SE: For 1 single-ended channel **PC-Diff:** For 1 diff. channel (30 Hz)

Option CAL3120: Only for 32-bit operation system. On-site calibration of the CPCI-3120. Do the fine adjustment fast and reliably and then save the calibration report file.

	8 SE/4 diff. inputs, 4 analog outputs 8 SE/4 diff. inputs, 8 analog outputs
--	--

Accessori	es
PX901-A:	Screw terminal panel with transorb diodes,
	for connecting the analog I/O
PX901-AG:	Same as PX901-A with housing for DIN rail
PX901-ZG:	Screw terminal panel for connecting the digital I/O, for DIN rail
PX BNC:	BNC connection box for connecting the analog I/O

_Dive.	bive connection box for connecting the analog i/o
010:	Standard round cable, shielded, twisted pairs, 2 m
011:	Standard round cable, shielded, twisted pairs, 5 m
3001:	Ribbon cable for digital I/O, with 37-pin D-Sub
	male connector on 3U bracket



Analog input board, optically isolated, 16 SE or 8 diff. inputs, 12-bit





CompactPCI™ 32-bit

Also for PCI-Express See APCIe-3021, page 134

Also for PCI See APCI-3001, page 190







LabVIEW™



LabWindows/CVI™



DASYLab10

Features

- Can be inserted in PXI systems, with restricted functionality
- Monitoring program for testing and setting the board functions

Analog inputs

- 16 single-ended/8 differential inputs or 8 single-ended/4 differential inputs or 4 single-ended inputs
- 12-bit resolution
- Throughput: 100 kHz
- Input voltage: 0-10 V, ±10 V, 0-5 V, ±5 V, 0-2 V, ±2 V, 0-1 V, ±1 V, 0-20 mA (option) freely programmable through software for each channel
- Gain PGA x1, x2, x5, x10 freely programmable through software for each channel
- PCI DMA for analog data acquisition

Analog acquisition

- Single channel, several channels, several channels through scan list
- Autom. analog acquisition through cyclic timer control
- Acquisition through scan list: up to 16 entries with gain, channel, unipolar/bipolar
- Acquisition triggered through software, timer, external event
- Trigger functions:
- Software trigger or
- external trigger: the analog acquisition (single or sequence) is started through signal switching from 0 V to 24 V at the digital input 0.
- Interrupt: end of single channel, end of multichannel, end of scan list

Digital

- 4 digital inputs, 24 V, optically isolated
- 4 digital outputs, 24 V, optically isolated

Timer

- 24-bit
- Timer 2 as cyclic time counter

Safety features

- Optical isolation 500 V min.
- Creeping distance IEC 61010-1
- Overvoltage protection ± 40 V (analog inputs)

CPCI-3001

16/8/4 single-ended or

8/4 differential inputs

12-bit resolution

Optical isolation 500 V

100 kHz throughput

Automatic analog acquisition

Trigger functions

MTBF: 75867 hours at 45 °C

Graphical display of the measured data

- Protection against high-frequency EMI
- Input filters: 159 kHz
- Noise neutralisation of the PC supply

Applications

- Industrial process control
- Industrial Measurement and monitoring
- Multichannel data acquisition
- Control of chemical processes
- Factory automation
- Acquisition of sensors
- Laboratory equipment
- Current measurement
- Instrumentation

Software

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

- Microsoft VC++ Microsoft C
- Borland C++ Borland C
- Visual Basic Delphi Turbo Pascal
- LabVIEW DASYLab DIAdem

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads



CompactPCI^m

Specifications

5 single-ended/8 differential inputs single-ended/4 differential inputs or single-ended inputs 2-bit
single-ended/4 differential inputs or single-ended inputs
single-ended/4 differential inputs or single-ended inputs
single-ended inputs
5 1
2-DIT
00 V through opto-couplers from PC to peripheral
oftware-programmable for each channel, 0-10 V, \pm 10 V,
5 V, ± 5 V, 0-2 V, ± 2 V, 0-1 V, ± 1 V,
20 mA optional
00 kHz
oftware programmable (x1, x2, x5, x10)
C at 10 Hz, 90 dB minimum
1 LSB (ADC)
0.5 LSB (ADC)
¹² Ω//10 nF Single-ended,
¹² Ω//20 nF Differential against GND
mited to 159 kHz with low-pass filter
rough software, timer, ext. event (24 V input)
ata to the PC through FIFO memory,
Commands, Interrupt at EOC (End Of Conversion)
nd EOS (End of Scan), DMA transfer at EOC
nd of conversion, End of timer, End of scan
1-bit; 50 μs; smallest programmable value: 100 μs
digital inputs, 4 digital outputs, 24 V
00 V through opto-couplers from PC to peripheral
30 V
Logical "0": 0-5 V - Logical "1": 10-30 V
mA typ.
30 V
) mA typ.
pen collector

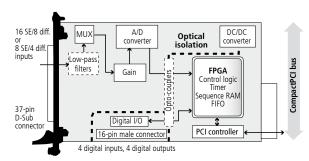
EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

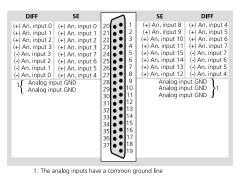
Physical and environmental conditions

Dimensions:	160 x 100 mm
System bus:	PCI 32-bit acc. to CompactPCI specification 2.1
Space required:	1 PCI slot for analog inputs,
	1 slot opening for digital I/O
Operating voltage:	+ 5 V, ± 5 % , 3.3 V from CPCI system
Current consumption:	550 mA typ.
Front connector:	37-pin D-Sub male connector
Additional connector:	16-pin male connector for ribbon cable
	for connecting the digital inputs and outputs
Temperature range:	0 to 60 °C (with forced cooling)
MTBF:	75867 Hours at 45 °C

Simplified block diagram



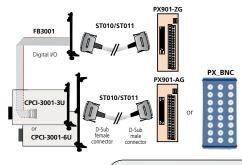
Pin assignment – 37-pin D-Sub male connector



16-pin male connector

Dig. output 0 (+) Dig. output 1 (+)	1	2	Dig. output 0 (-) Dig. output 1 (-)
Dig. output 2 (+)	5 🔳 🖿	6	Dig. output 2 (-)
Dig. output 3 (+)	7 🔳 🖬	8	Dig. output 3 (-)
Trigger/dig. input 0 (+)	9∎∎	10	Trigger/dig. input 0 (-)
Dig. input 1 (+)	11 🔳 🗖	12	Dig. input 1 (-)
Dig. input 2 (+)	13 🔳 🗖	14	Dig. input 2 (-)
Dig. input 3 (+)	15 🔳 🗖	16	Dig. input 3 (-)





Ordering information

CPCI-3001

Analog input board, optically isolated, 16 SE or 8 diff. inputs, 12-bit. Incl. technical description, software drivers and monitoring program.

0	CPCI-3001-16	16 SE/8 diff. inputs, 8 digital I/O	Accessori	es
0	CPCI-3001-8	8 SE/4 diff. inputs, 8 digital I/O	PX901-A:	Screw terminal pan
•	CPCI-3001-4	4 SE inputs, 8 digital I/O		for connecting the
0	Options: Pleas	e specify the number of channels when ordering	PX901-AG:	Same as PX901-A w
l	JRS-3001-6U:	6U bracket for mounting in 6U housing	PX901-ZG:	Screw terminal pan
0	Option SF:	Precision filter for 1 single-ended channel	PX_BNC:	BNC connection box
0	Option DF:	Precision filter for 1 diff. channel (30Hz)	ST010:	Standard round cab
0	Option SC:	Current input 0(4)-20 mA for 1 single-ended channel	ST011:	Standard round cab
0	Option DC:	Current input 0(4)-20 mA for 1 diff. channel	FB3001:	Ribbon cable with 3
				on 3U bracket for th

PX901-A:	Screw terminal panel with transorb diodes
	for connecting the analog inputs
2X901-AG:	Same as PX901-A with housing for DIN rail
2X901-ZG:	Screw terminal panel for connecting the dig. I/O, for DIN rail
PX_BNC:	BNC connection box for connecting the analog I/O
ST010:	Standard round cable, shielded, twisted pairs, 2 m
ST011:	Standard round cable, shielded, twisted pairs, 5 m
B3001:	Ribbon cable with 37-pin D-Sub male connector
	on 3U bracket for the digital I/O



4-port serial interface, RS232, RS422, RS485, 20 mA CL





CompactPCI™ 32-bit

Also for PCI-Express See APCIe-7xxx, page 142

Also for PCI See APCI-7500, page 210







The board CPCI-7500 is a 4-port serial interface for industrial applications. It is configured by inserting MX modules which the board identifies automatically. Each serial port can be configured individually through modules in the following modes: RS232, RS422, RS485 (with or without optical isolation) and Current Loop (optically isolated). The optically isolated modules allow a protection up to 1000 V for the use in noisy environments where earth loops can occur. Interrupts, addressing and transfer rate are controlled through the BIOS.

The I/O lines are protected against short-circuits, fast transients, electrostatic discharge and high-frequency EMI. Each port is supported through a 128-byte FIFO buffer for sending and receiving data which guarantees reliable operation with high data volumes.

Features

- Asynchronous 4-port serial interface
- 4 socket for MX modules
- Modular mounting through MX modules
- Can be configured as RS232, RS422, RS485 with or without optical isolation, 20 mA Current Loop (active, passive), with optical isolation
- Addressing through software
- No jumpers: software configuration
- Automatic module recognition
- 128-byte FIFO memory for each interface
- Common interrupts
- Programmable transfer rate
- 5-, 6- or 8-bit character
- 1, 1½ or 2 stop bits
- Parity: even, odd or none
- Automatic transmitter control for RS485

Safety features

- MX modules with optical isolation available
- Protection against fast transients (burst)
- Short-circuits protection for RS422 and RS485
- Internal diagnostic, break, parity, overrun and framing error
- Creeping distance IEC 61010-1 (MX modules)

CPCI-7500

4-port, RS232, RS422, RS485, 20 mA Current Loop Mode selection through MX modules With/without optical isolation Free mode configuration for each port 128-byte FIFO buffer per port MTBF: 98 551 hours at 45 °C

Applications

- Data acquisition
- Industrial process control
- Industrial communication
- Multi-user systems
- Modem and printer monitoring
- Multidrop applications

Software

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

- Microsoft VC++
- Visual Basic Delphi

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads





MX modules

Operating mode	RS232		RS422		RS485		20 mA CL
	MX232-G	MX232	MX422-G	MX422	MX485-G	MX485	ΜΧΤΤΥ
Optical isolation 1000 V	1		1		1		1
creeping distance 3.2 mm	1		1		1		1
short-circuit protection			1	1	1	1	
ESD protection	1	1	1		1		
Burst-protection	1	1	1	1	1	1	1
Duplex	Full	Full	Full	Full	Half	Half	Full
Max. Baud rate	1 MBaud	1 MBaud	1 MBaud	1 MBaud	1 MBaud	1 MBaud	19,2 kBaud
Modem control signals	1	✓	Optional RTS/CTS (MX-422-PEP)				
Autom. transmitter control					1	1	
Current consumption	16 mA	1 mA	15 mA	5 mA	15 mA	5 mA	82 mA

4-port serial interface

Modes:	RS232, RS422, RS485, 20 mA Current Loop (active, passive) with or without optically isolated via separate MX modules
Transmission mode:	Asynchronous, full /half duplex (MX modules)
Addressing:	Automatic through BIOS
Memory:	128-byte FIFO buffer for each interface
Transfer rate:	Programmable up to 1 MBaud (optional)
Protocol:	5-, 6-, or 8-bit Character 1, 1 ¹ / ₂ or 2 Stop bits
Parity:	Even, odd, none, mark, space
Interrupt:	Interrupt configuration through BIOS

Safety

Optical isolation:

1000 V (MX modules)

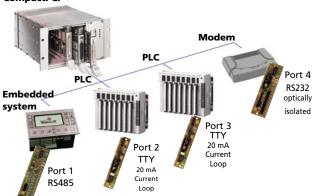
EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and environmental conditions

Dimensions:	3U/4TE
System bus:	CPCI 32-bit acc. to specification CompactPCI 2.2
Space required:	CPCI-slot, 3U
Operating voltage:	+5 V, \pm 5 % from the PC
Current consumption:	192 mA typ.
Front connector:	37-pin D-Sub male connector
Temperature range:	0 to 60 °C (with forced cooling)
MTBF:	98 551 Hours at 45 °C

CompactPCI



Application example

Connection cables



4 x 9-pin D-Sub male connector (ST075) or 4 x 25-pin D-Sub male connector

Ordering information

(ST074)

CPCI-7500

4-port serial interface, RS232, RS422, RS485, 20 mA CL. Incl. technical description and software drivers.

MX modul	es: Please order separately!		RS485 mode
MX232-G:	RS232 mode optically isolated	MXTTY:	20 mA Current Loop (active, passive), optically isolated
MX232:	RS232 mode	Option:	
MX422-G:	RS422 mode optically isolated	URS-7500-6U	J: 6U bracket for mounting in 6U housing
MX422-PEP	RS422 mode optically isolated, with RTS/CTS	Quarz:	Up to 1 MBaud transfer rate
MX422:	RS422 mode	Connection	n cables:
MX485-G:	RS485 mode optically isolated	ST075:	Shielded round cable, 37-pin to 4 x 9-pin
		ST074:	Shielded round cable, 37-pin to 4 x 25-pin.

Motion control for 4 servo or stepper motors



CompactPCI™ 32-bit



Also for PCI See page 212

Windows[®]

64/32-bit drivers

The board CPCI-8004 for the CompactPCI bus is used for the control of up to four servo or stepper motor axes through a PC. With this intelligent and flexible board, many control tasks from simple to complicated can be realised.

The board has four stepping/direction output channels (D/A channels, 16-bit). They are isolated from the digital current supply and are used for the control of commercially available power amplifiers connected as speed controlling devices or current regulators.

Incremental encoders, SSI encoders and EnDat encoders as well as end and reference switches can be connected to each axis channel.

Digital PID filters with forward compensation and optional Notch filters are also involved in the axis control. The "open" controlling concept of the CPCI-8004 is intended in the first place for manufacturers of special-purpose machines and users which need a flexible integration as well as a CNC solution.

Features

Hardware/Properties

- Intelligent board based on a 64-bit RISC processor
- Positioning of up to 4 axes either with servo or stepper motors. Mixed operating of servo and stepper motors possible.
- Interface for all commercially available power amplifiers
- All input and output channels are optically isolated
- A multiple-axis system can be realised by inserting several CPCI-8004 in the same PC.

Software

- Linear, circular, helical, spline and CAD interpolation
 Point-to-point movement with independent control of
- each axis
 Function library for Pascal, C-Basic, Borland Delphi, Borland C++, Visual Basic, Visual C++
- Programming through a PC application software or stand-alone
- The operating program can be easily adapted to specific requirements using program modules supplied with the board
- User programs created with the compiler can be processed automatically
- Multitasking: the board can simultaneously process up to 4 user programs.

CPCI-8004

For 1 to 4 servo or stepper motors

Onboard 64-bit RISC processor

Optical isolation

16-bit analog output channels

24 digital inputs and 12 digital outputs,

optically isolated

Applications

- Precision positioning
- CNC control
- Semi-conductor manufacturing
- Event counting
- Motion control
- Robots
- X-Y-Z position control
- Stepper motor control
- Machine monitoring
- Research and development

Software

A CD-ROM with the following software and programming samples is supplied with the board.

Standard drivers for:

- Linux
- 32-bit drivers for Windows 8 / 7 / Vista / XP / 2000
- Signed 64-bit drivers for Windows 8 / 7 / XP
- Real-time use with Linux and Windows on request

Drivers and samples for the following compilers and software packages:

- Visual C++ Microsoft C Lib. Borland C Lib.
- Visual Basic Delphi

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com/downloads



CompactPCI^{III}

Up to 32-bit, gray / binary code 78-pin D-Sub variable frequency 30 kHz to 1.5 MHz 1 per channel, D/A converter, 16-bit resolution, \pm 10 V 1 stepper signal (RS422) and 1 directional signal (RS422) for each channel, pulse frequency up to 24 inputs, 24 V, as end or reference switch or freely programmable 12 channels, 24 V / 500 mA, for releasing the power amplifiers or freely programmable Through PCI BIOS

Options: Safety

Interrupts:

Auxiliary voltage:

DMA:

Optical isolation:

Specifications

Data exchange with the PC:

CPCI-8004

Controller software:

Inputs for SSI encoders:

Isolated digital inputs:

Isolated digital outputs:

Setpoint value outputs (servo):

CPU system: RAM:

Interpolation:

Pulse outputs: (stepper motors)

1000 V

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

64-bit RISC processor 150 MHz

Interpolation with secondary axes.

Word length: 32-bit with sign

PIDF (PID filters with forward compensation)

2D .. 4D linear, 2D circular, 3D circular, 4D helix,

Through CompactPCI bus

16 MB

2 MHz

Bus master

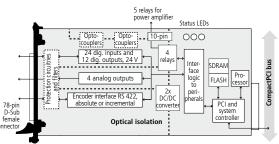
24 V external for dig. I/O Interbus or CAN-Bus

Inputs for incremental encoders: Diff. or TTL max. 2 MHz.

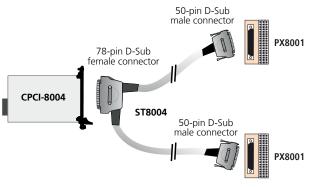
Physical and environmental conditions

Dimensions:	160 x 100 mm
System bus:	CompactPCI - universal
Space required:	1 Compact-PCI slot
Operating voltage:	+ 5 V and 3.3 V, \pm 5 % from the PC
Front connector for CPCI-8004:	Axis 1, 2, 3, 4: 78-pin D-Sub female connector
Temperature range:	0 to 60 °C (with forced cooling)

Simplified block diagram



ADDI-DATA connection



Ordering information

CPCI-8004

Motion control board for 4 servo or stepper motors. Incl. technical description and software drivers.

Accessories

nounting
ector



Screw terminal panels, Relay output boards, connection cables

How important are cables and terminal panels?

When the PC runs important controlling and regulating tasks in a processing system, then data transfer must be reliable in order to ensure the reliability of the whole system. This is why ADDI-DATA cables and terminal panels have the same high safety and EMC standards as the PC boards and MSX-E systems.

What makes the difference between cables?

The connection cable as a mechanical device is not submitted to the EMC specifications, though it can affect the emission immunity of the devices to which it is connected.

The use of cables with industry-standard D-Sub connectors has many advantages:

- Robustness
- Protection against EM fields
- Earthing on both connector ends
- High noise immunity

Application

Suitable for use as control or signal cables in noisy environment, for indoor or outdoor applications. The tight braid reduces the emissions. The copper braid is used as "ground". Twisted pairs provide protection against crosstalk and external interference. The cables are suited for dry or damp environments. Robust industry-standard D-Sub connector

Protection against electromagnetic fields

High noise immunity

Indispensable terminal panels

Terminal panels are essential in most industrial applications. They dispatch to the sensors, tracers or control modules the numerous signals which are to be processed.

Prevent connection errors

- The terminal panels are pin-compatible with the PC boards
- The terminal panels lead the control signals in increasing order from the PC to the screw terminal which also corresponds to the bit set in the board

Helpful LEDs

Indicate the status of each digital signal

Integrated 24 V supply

- Separate 24 V supply terminal for the easy connection of digital 24 V PC boards
- Varistors and diodes for overvoltage protection are connected to the screw terminals to prevent emissions from the external supply voltage.

High noise immunity

• The connection between housing and shield through the ground connection terminal creates an earthing on both sides



PX901PX9000PX8001PX8001PX8500PX_BNCDescriptionPanel for connecting up to 32 signal linesPanel for connecting up to 32 signal linesPanel for connecting signal linesRelay output board with 8 relay, cacadable in 16.2, 40 signal linesConnection of up to 8 diff. or 15 5 input through signal linesFunction indication with LEDSPX901-D: yesFor 24 V and sensor supplyFor relay and sensor supplyFor relay and sensor supplyOvervoltage protection of the 24 V supply voltageThrough varistors and transil diodesThrough varistors and transil diodesAvailable versionsPX901-D: PX901-D: For analog boards with transil diodes for the overvoltage protection of the analog looPX901-D: the data lines.PX901-D: PX901-A with housing for DIN railPX901-A PX901-A with housing for DIN railPX901-A PX901-A the analog boards APCI-12102/3001, APCI-12102/3001, APCI-12102/3001, APCI-12102/3001, APCI-12102/3001, APCI-12102/3001, APCI-12102/3001, APCI-12102/3001, APCI-12102/3001, APCI-12102/3001, APCI-12102/3001, APCI						
Description Parket for connecting up to 22 signal lines Parket for connecting So signal lines Parket for connecting and 32 relays Parket for connecting and constant transit diodes Parket for connecting transit diodes Park		PX901	PX9000	PX8001	PX8500	PX_BNC
Function indication with LEDS PX01-D: yes Por Z4 v and sensor supply sensor supply Overvoltage protection of the 24 vand sensor supply Through variators and transil diodes Through variators and transil diodes Through variators and transil diodes Ithrough variators and transil diodes Available versions PS01-D: For digital boards, with 32 LEDS for status indication of the data lines. PX801-D: For digital boards, with 32 LEDS for status indication of the data lines. PX800-Vt+G: With variators and housing for DIN rail PX901-D: For analog boards with transil diodes for the overvoltage protection of the analog 1/0 PX800-Vt+G: With housing for DIN rail PX800-Vt+G: With housing for DIN rail PX901-A: For analog boards with transil diodes for the overvoltage protection of the analog 1/0 PX801-A with housing for DIN rail PX801-A with housing for DIN rail PX901-2: With housing for DIN rail PX901-3: NPX01-4 with housing for DIN rail ADI-DATA digital analog 1/2 U/3 2/1 3/2 1 and counter boards PA1700-2; with housing for DIN rail APCI-1710, CPCI-1710 APCI-2312/3 2/1 3/2 1 and counter boards PA1700-2; with digital boards ADDI-DATA digital boards ADDI-DATA digital boards ADDI-DATA digital boards ADDI-DATA digital boards ADDI-DATA analog boards	Description				relays, cascadable in 16, 24	or 16 SE inputs through
of the 24 V supply voltage transil diodes transil diodes transil diodes Available versions PX901-D: for digital boards, with 32 LEDs for status indication of the data lines . PX901-DC: PX901-DC: Same as PX901-D with housing PX901-DC: PX901-A: For analog boards with transil diodes for the overvoltage protection of the analog loards with housing for DIN rail PX801-A: For analog boards with transil diodes for the overvoltage protection of the analog loards with housing for DIN rail PX901-A: For analog boards with transil diodes for the overvoltage protection of the analog loards with housing for DIN rail PX901-AC: PX901-AC: To analog boards with transil diodes for the overvoltage protection of the analog loards and public protection of the analog loards and public protection of the analog boards PX901-AC: PX901-AC: PX901-AC: To analog boards analog or counter boards PX901-AC: PX901-AC: PX901-AC: ADDI-DATA digital, analog or counter boards All ADDI-DATA digital boards APCI-1710, CPCI-1710, APCI-1311/301x, ADDI-DATA digital analog boards	Function indication with LEDs	PX901-D : yes	For 24 V and sensor supply			
For digital boards, with 32 LDS for status indication of the data lines. For digital boards, with 32 LDS for status indication of the data lines. For DIN rail PX901-DC: Same as PX901-D with housing PX901-A: For analog boards with transil diodes for the overvoltage protection of the analog I/O For analog boards with transil diodes for the overvoltage protection of the analog JOC For digital PX901-AC: Same as PX901-A with housing for DIN rail For digital PX901-AC: Same as PX901-AC: Same as						
Connection to ADDI-DATA digital, analog or counter boards All ADDI-DATA digital boards All ADDI-DATA digital APCI-3200, APCI-2200, APCI-311x/301x, ADDI-DATA digital boards with digital outputs ADDI-DATA	Available versions	For digital boards, with 32 LEDs for status indication of the data lines . PX901-DG : Same as PX901-D with housing PX901-A : For analog boards with transil diodes for the overvoltage protection of the analog I/O PX901-AG : Same as PX901-A with housing for DIN rail PX901-ZG : For digital I/O boards, analog boards APCI-3120/3001, APCIe-3121/3021/3521 and counter boards PA1700-2,			for DIN rail PX8500-Vt+G : With varistors and housing for	
Page 247 250 250 248 251	Connection to			APCI-8008, CPCI-8004 APCI-2200, APCIe-2200		
	Page	247	250	250	248	251



Screw terminal panel for DIN rail



The screw terminal panel PX901-xx is used for the connection of maximum 32 signal or signal-reference lines.

ADDI-DATA boards can be connected through 37-pin D-Sub female connector with our standard cables of STxxx series.

The housing of the female connector is connected with two ground terminals so that the board is additionally earthed for more security. All components of the board are enclosed in an earthing strip also connected to the ground terminals.

Each terminal is directly connected to one pin of the 37-pin D-Sub female connector. Designations on terminals indicate respective connections for the 37-pin D-Sub female connector.

The PX901-D version is equipped with LEDs which are ideal for status display when working with ADDI-DATA digital 24 V I/O boards.

The PX901-A version is fitted with transil diodes for analog signals, but without LEDs.

An additional 4-pin terminal is available in order to be able to connect more than one 24 V operating voltage and ground line.

The 24 V or the ground terminal can be connected very easily through wire wrap to the 4-pin terminal.

The 24 V operating voltage lines are additionally protected against overvoltage through varistors and transil diodes.

Features

- Connection of up to 32 signal lines
- Separate ground connections
- Connection through screw terminals
- 2 rows of terminals
- Terminals can be labelled
- Additional 4-pin terminal for connecting the ground or the supply voltage
- With housing for mounting on a standard DIN rail
- All terminals intended for large conductor cross sections: up to 2.5 mm²

PX901

Screw terminal panel. Incl. technical description.

Versions

PX901-D:For digital boards, with status indication through LEDs
and the Ethernet systems MSX-E312x and MSX-E3701-DIO**PX901-DG:**Same as PX901-D, with housing for mounting on DIN rail**PX901-A:**For analog boards, with transil diodes**PX901-AG:**Same as PX901-A, with housing for mounting on DIN rail

PX901

32 terminals for signal lines

LED status indication for digital signals

Transil diodes for analog signals

DIN-rail mounting

Direct connection to ADDI-DATA boards

Safety features

• Overvoltage protection of the 24 V supply terminals through varistors and transil diodes

Applications

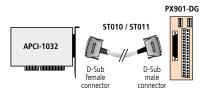
- Process control
- Industrial measuring
- Acquisition of sensor data
- Signal analysis

Specifications

Signal line terminals:	32 for the connection of peripherals
Additional terminals:	 – 4 for feeding the external
	operating voltage (digital I/O)
	 – 2 for the connection of ground lines
status indication:	32 LEDs for status indication, 1 LED for
	status display of the operating voltage (version D)
Safety features:	Varistors and transil diodes
Connector:	37-pin D-Sub female connector
Dimensions of the board:	(L x W x H) 130 x 70 x 35 mm
Dimensions with housing:	(L x W x H) 132 x 87 x 70 mm
Temperature range:	0-60 °C

Example:

Connection of a digital input board to the screw terminal panel PX901-DG



Ordering information

PX901-ZG: - For the counter boards PA 1700-2

- and APCIe-1711 (function Sin/Cos)
- for connection to digital I/O
- for connecting digital I/O to analog PC boards
- for the relay boards (digital inputs) APCIe-2200 and APCI-2200 With housing for DIN-rail mounting

Accessories please order separately!

	· · · · · · · · · · · · · · · · · · ·
ST010:	Standard round cable, shielded, twisted pairs, 2 m
ST011:	Standard round cable, shielded, twisted pairs, 5 m



8-port relay output board





The PX8500 is an external 8-channel relay board for the connection of digital output boards. It can be cascaded in 16, 24 and 32 relays and is intended for mounting on DIN supporting rails. The board provides a convenient interface between an industrial process and the D-Sub connectors on ADDI-DATA boards.

The change-over contacts of the relay are controlled through 24 V signals. The 24 V voltage supply is protected through varistors and transil diodes.

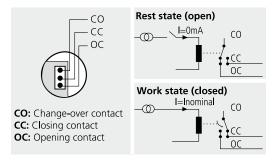
The board is intended for the use with 220 V supply. The creeping distance (acc. to DIN VDE0110) and the connector cross sections allows high-power switching (up to 2,500 VA). The board has a female D-Sub connector for connecting an ADDI-DATA digital 24 V output board through a standard I/O cable ST010. The red LEDs display the state of the relays (open/closed). A green LED displays the ON/OFF of the operating voltage.

The 37-pin cable shielded can be grounded on both sides for the protection against high-frequency EMI

Features

- Relay output board with 8 relays, cascadable in 16, 24 and 32 relays
- Max. switching voltage: 30 VDC / 277 VAC
- Max. switching current: 10 A
- All terminals intended for large conductor cross sections up to 2.5 mm²
- Operating voltage display through green LED
- Relay state display through red LED
- Relays mounted on sockets
- High switching capacity
- Long-lasting life

Function principle of the relays



PX8500

For the connection of digital output boards
Cascadable in 16/24/32 relays
8 relays on socket
DIN-rail mounting
30 VDC / 277 VAC
300 W / 2500 VA
10 A

Safety features

- Overvoltage protection of the 24 V supply voltage through varistors and transil diodes
- Contact protection of the relays through varistors
 (option Vt)
- 4 mm creeping distance between change-over, closer and opening contact
- 6 mm creeping distance between change-over contact and closer of adjoining relay
- Free-wheeling diode in the coil circuit
- With housing for mounting on a standard DIN rail, (option G)
- Operating safety tested according to the low-voltage directive: 73/23/EEC

Applications

- Industrial digital I/O control
- Automatic test equipment
- External high power relay control
- Alarm monitoring
- Test automation
- Alarm monitoring
- Digital monitoring
- ON/OFF monitoring of motors, lights ...
- ..



Specifications

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Contact side

Type of contacts:	8 change-over
Max. switching voltage:	30 VDC / 277 VAC
Max. switching capacity:	300 W / 2500 VA
Max. switching current:	10 A
Contact resistance:	<100 mΩ
Response time:	15 ms
Release time:	5 ms
Mechanical life:	5.000.000 operations
Life at max.	
switching capacity:	100.000 operations

Control side

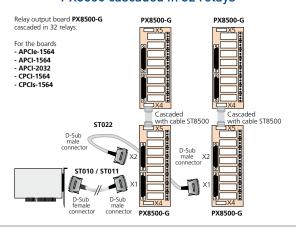
Switching behaviour:	Monostable
Operating voltage:	24 VAC
Operating efficiency:	533 mW
Switch. frequency at max. load:	20 switchings/minute
Threshold voltage at +20 °C:	16.8 V
Release voltage at +20 °C:	2.4 V

Physical and environmental conditions

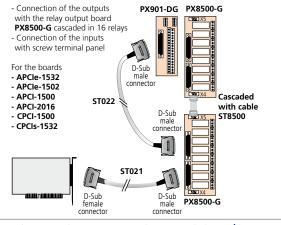
Operating voltage:	+ 24 V
Current consumption:	210 mA typ.
Dimensions (L x W x H):	with housing 212 x 87 x 72 mm
Connector:	2 x 37-pin D-Sub female connector
X1:	For the connection to the PC
X2:	For cascading the PX8500 in max. 32 relays,
	for example the digital output board
	APCI-2032. In this case the digital output signal 1
	corresponds to the 24 V control signal
	of the relays 1, output 2 to relays 2, etc.
Temperature range:	0-60 °C
Humidity:	30-95 %



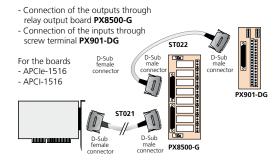
PX8500 cascaded in 32 relays



PX8500 cascaded in 16 relays



Connection example for the digital I/O board APCI-1516



PX8500

8-port relay output board. Incl. technical description.

PX8500-G:	With housing for mounting on DIN rail
PX8500-VtG:	PX8500 with varistors and housing for
	mounting on DIN rail

Accessories

ST8500: ST021:	Ribbon cable for cascading the board in 16, 24 or 32 relays Standard round cable, shielded, for connecting
	to APCI-1500 or APCI-1516
ST022:	Standard round cable, shielded, for cascading two PX8500
ST010:	Standard round cable, shielded, twisted pairs, 2 m, for connecting to APCI-2032, APCI-1564
ST011:	Same as ST010, 5 m



Ordering information

Screw terminal panels for DIN rail



PX9000

3-row screw terminal panel

LED status indication

DIN rail mounting

For digital or analog boards

The screw terminal panel PX9000 is intended for the connection of maximum 32 signal lines and the voltage supply for the external sensors/actuators. All components of the board are enclosed in an earthing strip which is also connected to the ground terminals.

On the 3x39-pin terminal block, all 37 contacts of the 37-pin female connector are assigned a contact on a row of terminals. Each signal line (terminal 1-32) is assigned a status LED.

Both other rows of terminals are intended for connecting the voltage supply for the sensors/actuators. These rows are protected against unintentional voltage reversal through a diode. A LED indicates when a voltage is applied.

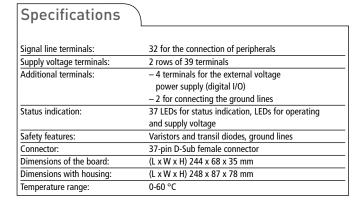
These rows of terminals are equipped with 2 additional terminals, one on the right and one on the left side, for the easy connection of the voltage supply to a further terminal panel.

4 further screw terminals are at disposal for the supply voltage of ADDI-DATA digital I/O boards: two for the connection of the 24 V operating voltage and two for the operating ground.

Both terminals for the operating voltage 24V are in addition protected against overvoltages through varistors and transorb diodes.

Features

- 3 rows of terminals, terminals can be labelled
- LED indicator status
- Additional 4-pin terminal for the direct connection of the ground and the 24 V supply voltage to ADDI-DATA boards
- With housing for DIN-rail mounting
- All terminals intended for large conductor cross sections: up to 2.5 mm²
- 2 x 39 screw terminals to the distribution of the voltage supply e.g. on sensors and for cascading several PX9000





PX8001

3-row screw terminal panel, 50-pin, for DIN rail

Connection of 50 signal lines

With numbered screw terminals)

Features

- Screw terminal panel for 50 signal line terminals
- Ground connection of the connector is lead directly to the connecting terminal
- With 50-pin female connector
- For free mounting

Specifications

Cross conductor section up to:	4 mm ²	0.2 – 2.5 mm² (flexible)
Input/output test voltage:	2.5 kV, 50 Hz, 60 s	
Operating temperature:	-20 °C to +50 °C	
Dimensions in mm (L x W x H):	69 x 98 x 62	
Current/Voltage:	2 A / 125 V	

Ordering information

PX9000

3-row screw terminal panel, 37-pin, with housing for DIN-rail mounting. Incl. technical description.

PX8001

3-row screw terminal panel, 50-pin, with housing for DIN-rail mounting. Incl. technical description.

Accessory

please order separately!		
ST010:	Shielded round cable, twisted pairs, 2 m, 37-pin	
ST011:	Shielded round cable, twisted pairs, 5 m, 37-pin	
ST370-16:	Shielded round cable, twisted pairs, 2 m, 50-pin	
ST8001:	Cable for connecting the APCI-8001 and APCI-8008 to the	
	screw terminal panel PX8001	



BNC connection box for DIN rail



PX_BNC

BNC connection box

For analog I/O boards

DIN-rail mounting

Features

The connection box PX_BNC allows the direct connection of analog voltage and current signals through BNC connectors. Many ADDI-DATA analog boards can be connected (see table on the right). With the PX-BNC, you can connect up to 8 differential or 16 single-ended analog inputs as well as 8 analog output channels through BNC connectors.

Housing

The compact housing consists of black painted aluminium, profile IP65 with good impact resistance.

Accessories

The standard delivery contains 2 clamps for DIN rail mounting.

Connection to the board

The connection to the board is made through the 37-pin D-Sub female connector, the pin assignment depends on the type of board connected. The connection between PX_BNC and the ADDI-DATA analog board is made through the standard round cable ST010 (shielded cable, 2 m). Please order the cable separately.

16 BNC connectors for analog inputs

The connection box has 16 BNC connectors In 0 to In 15 for the connection of the analog input channels (Channel 0-15) of many ADDI-DATA input and multifunction boards (see table on the right).

The BNC ground is connected to the ground of the analog signals. The connection of the differential channels (DIFF) is only possible through a special BNC cable.

8 BNC connectors for analog outputs

The connection box has 8 BNC connectors Out 0 to Out 7 for the connection of the analog output channels (channel 0-7) of many ADDI-DATA multifunction and output boards (see table on the right).

The BNC ground is connected to the ground of the respective analog output channel.

The PX-BNC can be connected to the following ADDI-DATA analog boards:

Multifunction boards	Analog output board
APCI-3110 / APCI-3116 APCI-3120 / CPCI-3120 APCIe-3121 / APCIe-3123 CPCIs-3121	APCI-3501 APCIe-3521
	APCI-3110 / APCI-3116 APCI-3120 / CPCI-3120 APCIe-3121 / APCIe-3123

Specifications

BNC connector:	For the connection of peripherals	
BNC connector:	In 0-15 for analog inputs	
	Out 0-7 for analog outputs	
D-Sub connector	37-pin D-Sub female connector	
Dimensions:	(L x W x H) 210 x 105 x 50 mm	
Weight:	727 g	
Temperature range:	0-60 °C	

PX_BNC

BNC connection box for DIN rail. Incl. technical description.

Accessories

Please order separately!

 ST010:
 Standard round cable, shielded, twisted pairs, 2 m

 ST011:
 Same as ST010, 5 m

 Other cable version on request



Ordering information

Shielded cables for industrial applications



CABLES

Dedicated cables

Special versions on request

Standard cables for industrial applications

More safety for your application

Specifications of the cables (STxxxx type)

What makes the difference between cables?

The connection cable as a mechanical device is not submitted to the EMC specifications, though it can affect the emission immunity of the devices to which it is connected. The use of cables with industrial standards has many advantages:

 Protection against EM fields: The shield of the cable is connected to the metallised hood of the D-sub connector. The connection between housing and shield creates an earthing on both sides.

 High noise immunity: More protection through adapted pin assignment of the cables. The way the cable leads are twisted in pairs corresponds to the pin assignment of the boards.

Industry-standard D-sub connectors

versus SCSI-connector

D-sub connectors fit the high requirements of industrial measurement and control. They are robust and have a high noise immunity.

This is why we equip all our boards with D-sub connectors.

Application

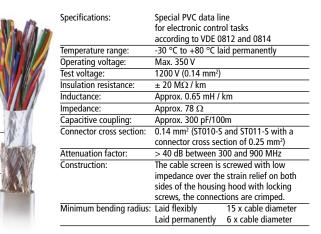
Suitable for use as control or signal cables in noisy environment, for indoor or outdoor applications. The tight braid reduces the emissions. The copper braid is used as "ground". Twisted pairs provide protection against crosstalk and external interference. The cables are suited for dry or damp environments.

Design of the cables

- Plain copper conductor, fine-strand according to IEC 60228
- Special PVC conductor insulation
- Twisted-pair conductors
- Core identification according to DIN 47100
- Conductors laid up in layers
- Aluminium foil
- Tinned copper braid shielding
- Covering grade approx. 85%
- Special outer sheath, grey PVC
- Oil and petrol resistant according to VDE 0250 and 04772
- Self-extinguishing (SE) and flame-retardant, according to IEC 60332-1

Special versions on request

- Other lengths
- Open cable end, on one or on both ends
- Bent connectors on one or on both ends



Ribbon cables

Twisted pairs

Aluminium foil

Copper braid

outer sheath

shielding

PVC

Cable designation	Description
5	
FB MSX-DIG-IO	For the MSX-Box option MSX-DIG-IO, 9-pin ribbon cable with D-Sub male connector.
FB-INTERBUS	For the APCI-8008, for connecting the Interbus. Ribbon cable, 9-pin D-Sub female connector with bracket.
FB-PROFIBUS	For the MSX Box, for connecting the Profibus.
	Ribbon cable, 9-pin D-Sub female connector with bracket.
FB104-1500	For the digital I/O port of the PC104-PLUS1500. Ribbon cable, 37-pin D-Sub male connector.
FB3000	Ribbon cable for the digital I/O port,
	37-pin D-Sub male connector with bracket.
FB3001	Ribbon cable for the digital I/O port of the CompactPCI boards. 37-pin D-Sub male connector with 3U bracket.
FB3003	Ribbon cable for the digital I/O port,
1 20000	37-pin D-Sub male connector with bracket.
FB3600-AC	For the analog and counter functions of the APCI-3600.
	Ribbon cable, 2x15-pin D-Sub male connector with bracket.
FB3600-D	For the digital I/O port of the APCI-3600.
	Ribbon cable, 37-pin D-Sub male connector with bracket.
FB3702	For the digital I/O port of the APCI-3701 and APCI-3702.
	Ribbon cable, 37-pin D-Sub male connector with bracket
FB8001/FB8008	For APCI-800x, APCI-30xx and APCI-31xx.
,	Ribbon cable, 50-pin D-Sub male connector with bracket.
FB-CAN	For APCI-800x, between OPMF and 9-pin D-Sub male connector with bracket for external CAN connection.



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Shielded standard cables with metallised hoods

Cable designation	Description	Twisted pairs	Shielded round cable	Length
ound cable, 1 to 20	m, 2 x 37-pin D-Sub connector			
Г010_1	Female connector / male connector	1	1	1 m
010	Female connector / male connector	1	1	2 m
010 3	Female connector / male connector	1	1	3 m
	Female connector / male connector	1	1	5 m
011 10	Female connector / male connector	1	1	10 m
011 15	Female connector / male connector	1	1	15 m
011_20	Female connector / male connector	1	1	20 m
	e 90° bent female connector, 2 x 37-pin D-Sub connector		I	1
010 1 ABGW	90° bent female connector / male connector	1	1	1 m
010 ABGW	90° bent female connector / male connector	1	1	2 m
010_ABGW	90° bent female connector / male connector	· ·		3 m
011 ABGW	90° bent female connector / male connector	· ·		5 m
	o 90° bent connectors, 2 x 37-pin D-Sub connectors	•	· ·	1.2.11
	Female connector / male connector	1	1	1 m
010_1_2XABGW	Female connector / male connector	<i>v</i> <i>v</i>		2 m
010_2XABGW			v	2 111
	d 5 m, or high currents (for 24 V digital outputs), 2 x 37-pin D-Sub conn			2
010_S	Female connector / male connector, with separate connection for 24 V voltage supply	<i>✓</i>	1	2 m
011_S	Female connector / male connector, with separate connection for 24 V voltage supply	1	1	5 m
	e open end, 1 x 37-pin D-Sub connector			[.
010_1_0	Female connector / other side open and bared, incl. colour table according to DIN 47100	1	1	1 m
10_0	Female connector / other side open and bared, incl. colour table according to DIN 47100	1	1	2 m
10_3_0	Female connector / other side open and bared, incl. colour table according to DIN 47100	1	1	3 m
11_0	Female connector / other side open and bared, incl. colour table according to DIN 47100	1	1	5 m
und cable betwee	n digital I/O boards and relay output board PX8500, 2 x 37-pin D-Sub co	onnectors		
21	Between digital I/O boards (APCI-1500/-1516/-1532/-2016, CPCI-1500) and PX8500 female connector / male connector	1	1	2 m
)22	Between two PX8500 or PX90x male connector / male connector	1	1	2 m
500	Ribbon cable between two PX8500-x			5 cm
cellaneous cable	S			
711-50	Connection cable for the APCIe-1711, for connecting the PX8001, 78-pin D-Sub male connector / 50-pin D-Sub male connector Enables the compatibility with the APCI-1710	1	1	2 m
3003-A	Connection cable for the APCI-3003, for the analog input signals, 15-pin male connector / 37-pin male connector	1	1	2 m
3003-D	Cable for the APCI-3003, for the digital signals,15-pin male connector / 37-pin male connector	1	1	2 m
200	50-pin female connector / 50-pin male connector	1	1	2 m
601	Coaxial cable for the APCI-3600			2 m
nd cables, 2 x 50	-pin D-Sub connector		•	
370-16_1	Female connector / male connector	1	1	1 m
370-16	Female connector / male connector	1	1	2 m
370-16 5	Female connector / male connector	1	1	5 m
370-16 1 ABGW	90° bent female connector / male connector	1	1	1 m
370-16 ABGW	Female connector / 90° bent male connector	1	1	2 m
370-16 5 ABGW	90° bent female connector / male connector	· ·	· ·	5 m
3701	Round cable for the APCI-3701 female connector / male connector	· ·	-	2 m
	e APCI-8001 and CPCI-8004, 2 x 50-pin D-Sub connector	•		1
	Female connector / male connector	1	1	2 m
	Female connector / male connector	<i>v</i> <i>v</i>	✓ ✓	5 m
001_5	<u> </u>	~	v	1.11
	e connection of serial interfaces			25
	4-port serial interfaces, 37-pin D-Sub female connector / 4 x 25-pin D-Sub male connector			35 cm
)74				I JE cm
TO74 TO75 TO75_ABGW	4-port serial Interfaces, 37-pin D-Sub female connector / 4 x 9-pin D-Sub male connector 4-port serial interfaces, 37-pin D-Sub female connector / 4 x 9-pin D-Sub male connector		<i>J</i>	35 cm 35 cm
074 075				



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